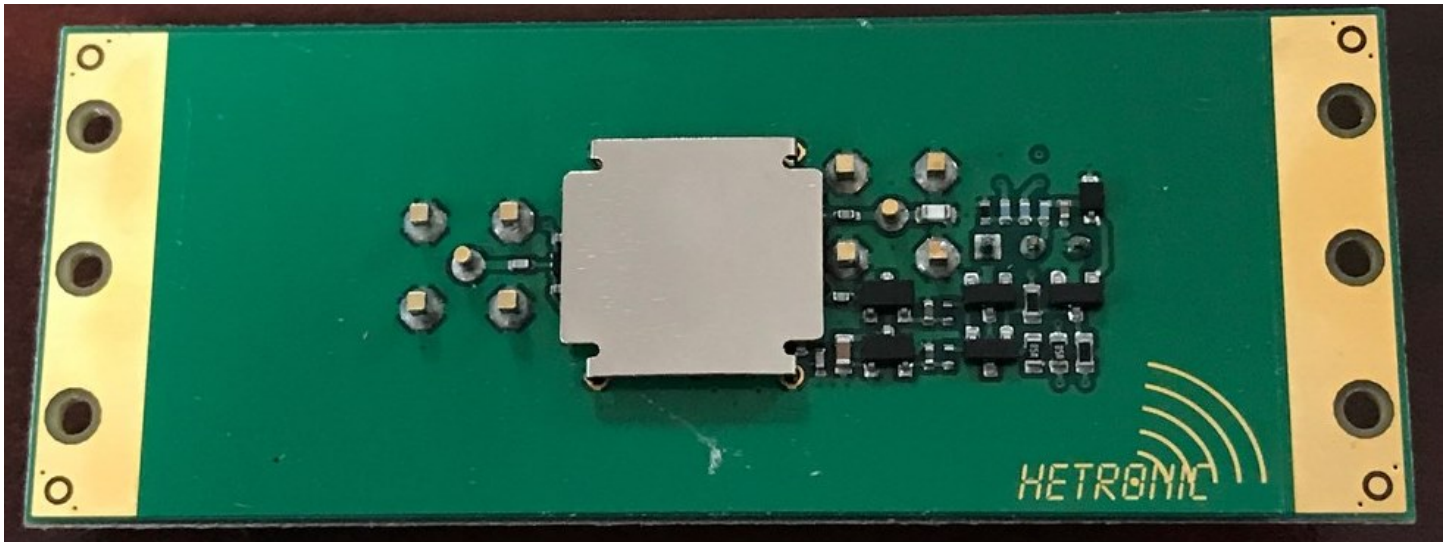
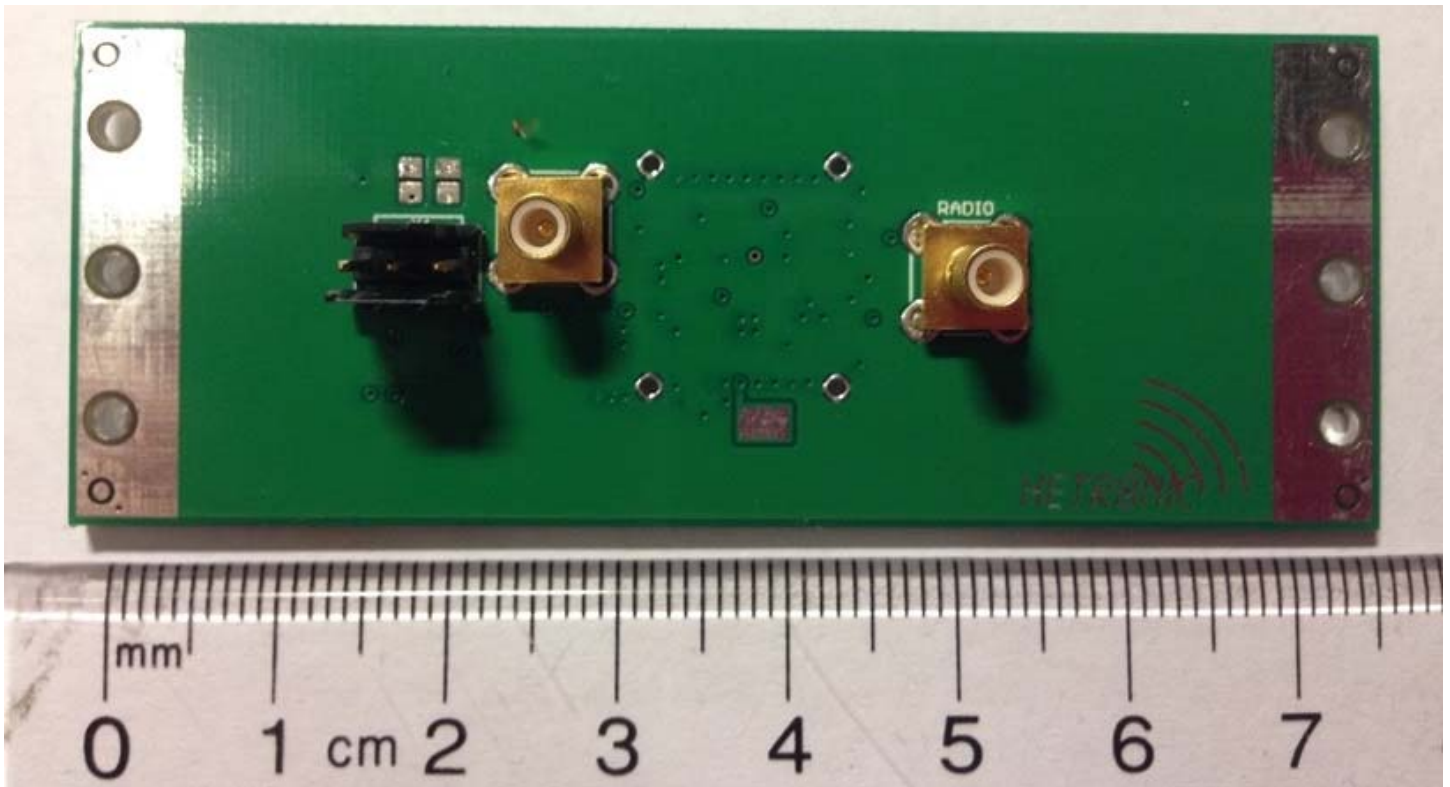


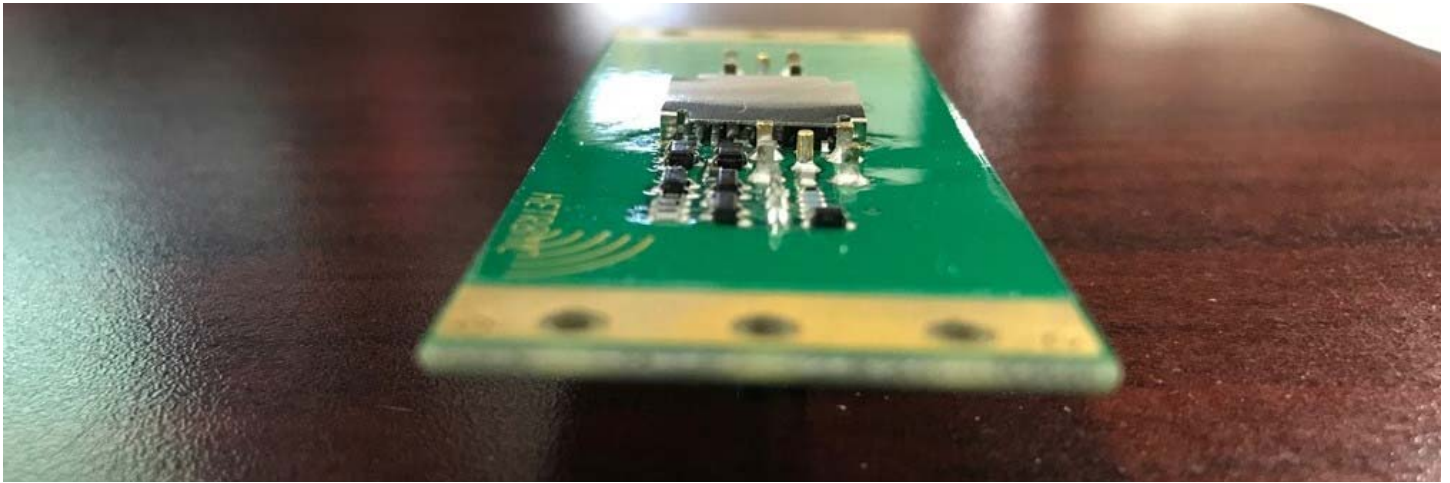


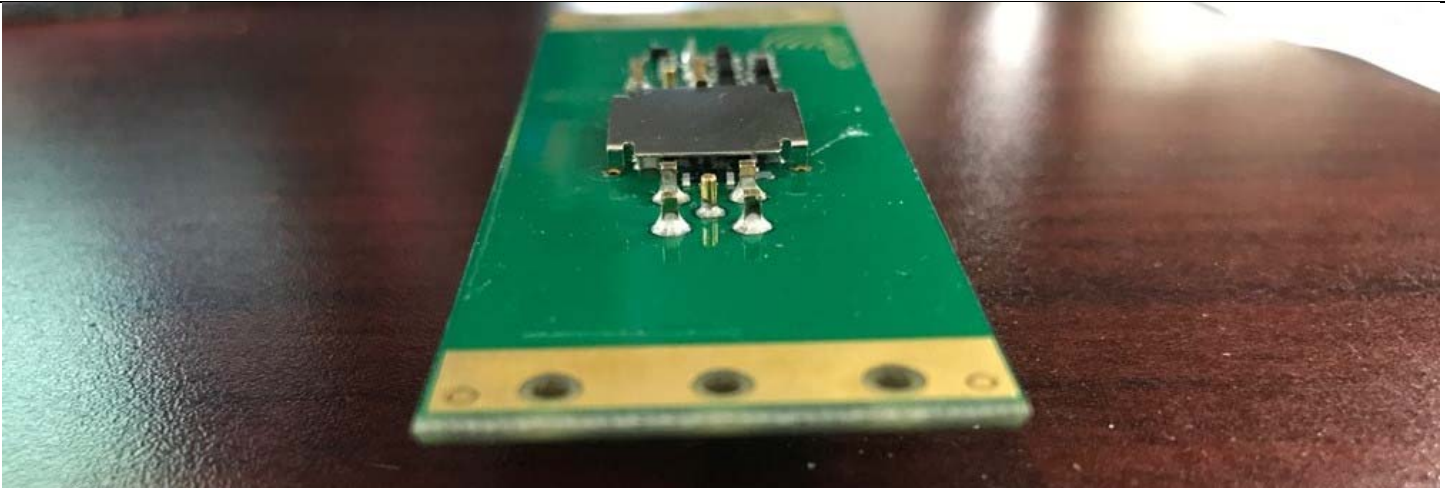
Side 1 of 6; component side with shield



Side 2; connector side

Note vias connecting top layer to bottom layer.

**Side 3****Side 4****Side 5**



Side 6 of 6
