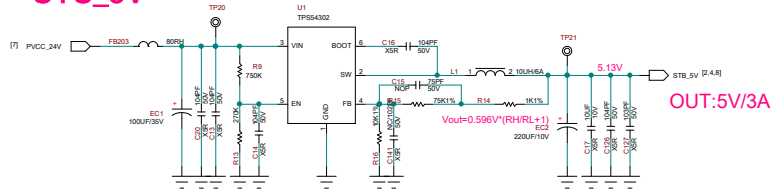


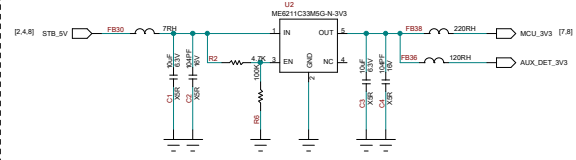
Version	SCH-Change History	Prepare By	Date
V0.0	- First Release	Li De Xin	2022.11.30
V0.1	- 完善DIPSPLAY部分电路 - DMIC电路的供电改为PMIC芯片1.8V供电 - SOC_RESET的SYSRSTB引脚改为电阻直接上拉到VIO18_MT8518S - I2SO_LRCK/BLK Buffer (U502) 修改接法 - U9工位的I2SIN D1_ D3、LRCK、 BCK增加0R电阻 - DMIC_1.8V_DET功能从L1改到V10	Li De Xin	2022.12.01
V0.2	- U803芯片工位 5PIN和4PIN引脚对换 - 贴片电阻R705 R706工位 NC改为0R - 贴片电容C520 C521 C526 C527 C528 C529 2.2UF 改为22UF,改善AUXIN模式Bypass 低频曲线 - 贴片电容C625 C626 C646 C647C648 C649 684PF 改为474PF,改善Bypass模式高频曲线 - 贴片电容C623 C624 684PF 改为105PF,改善Bypass模式低音通道高频曲线 - 贴片电容C30 C34 C74 C79 C82 C85工位4PF改为NC,改善因滤波电容过大导致WIFI模块通讯不稳定 - 增加LDO-3.3V(U5) 单独给ADC和模拟音频电路供电 - C108工位 10uF改为22uF改善MTK仿真的电源要求 - U10工位 RTL8201F_VB_CG改为RTL8201G_VX_CG , 贴片电容C33 C134工位0.1uF改为1uF - 贴片电阻R63 R238 4.7K改为NC 改善网络接收发包速度 - U3工位 eMMC型号THGBMNG5D1LBAIL改为KLM8G1GETF-B041 - U3工位 eMMC选用KLM8G1GETF-B041型号滤波电容匹配 C22工位0.1uF改为0.22uF,C23工位1uF改为4.7uF,C29工位0.1uF改为0.22uF,C166工位1uF改为2.2uF - 更改高音左右通道位置和极性	Li De Xin	2023.02.13
V0.3	- U700(MT32G030K8)工位增加复合按键,17P定义为VOL+_KEY,18P定义VOL-_KEY - 修改AUXIN 差分电路 , 增加一个OP芯片U501工位 , 改善交流阻抗≥10K - SOC芯片晶振的贴片电容C71 C78工位12PF改为18PF,减少晶振频偏误差范围 - RTL8201G_VX_CG芯片晶振的贴片电容C105 C125工位22PF改为15PF,减少晶振频偏误差范围	Li De Xin	2023.04.10
V0.4	- 贴片电容C532 4.7UF改为10UF,增加贴片电容C549 22UF , 改善AUXIN模式 100HZ高频通道的分离度偏低问题 - 增加贴片电容C553 C554 C555 C556工位 22UF , 贴片电阻R553 R554 R555 R556 工位 电阻100Ω , 改善AUXIN模播放Ferry_180HZ信号有Pop Noise问题	Li De Xin	2023.06.01

Version	PCB Layout-Change History	Prepare By	Date
EV1	- First Release	Li De Xin	2022.12.22
EV2	-Wifi模组供电(U4)电路部分改到模组下面放置,并单独供电 -Wifi模组通讯IO走线(Data/Clk)按照要求进行包地处理Layout -ADC芯片(TLV320ADC5120)电路部分C540 C541供电滤波电容靠近芯片地下地 -USB DM/DP 测试点移到连接座附近	Li De Xin	2023.02.15
DV1	- 低音通道贴片电感 L600 L601 工位 , 将一体成型材质的电感改用屏蔽电感 , 改善6.5KHZ频点失真度大问题 - 更改高音左右通道位置和极性	Li De Xin	2023.03.03
DV2	- 修改AUXIN 差分电路 , 增加一个OP芯片U501工位 , 改善交流阻抗≥10K - 为避让结构位置 , 修改USB供电限流芯片U18位置	Li De Xin	2023.04.15
PV	- 修改AUXIN 差分电路中点电压纹波 , 增加C549 22UF , 改善100HZ分离度 > 60dB - 修改AUXIN 差分电路输出端电路 , 增加耦合电容和电阻 , 改善播放Ferry_180HZ 歌曲有Pop noise问题	Li De Xin	2023.06.01

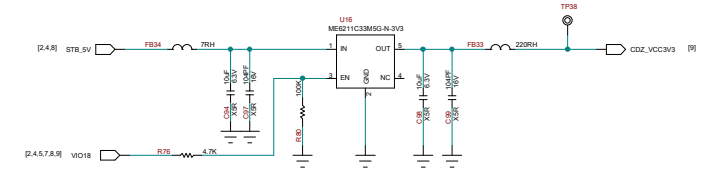
SYS_5V



MCU POWER



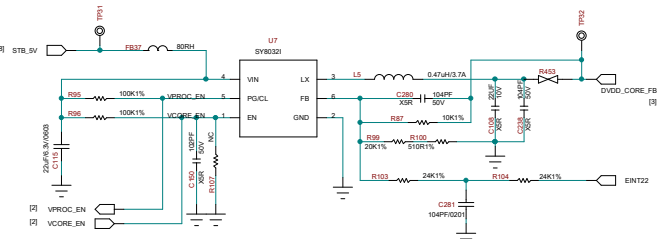
Zigbee MG21 POWER



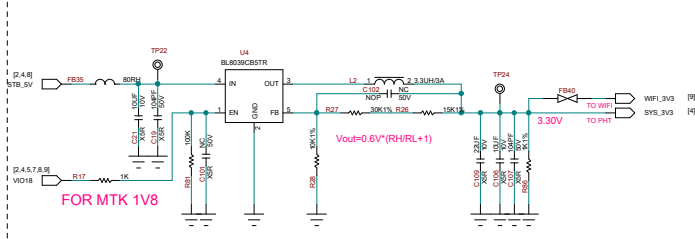
SOC CORE POWER

SY80321

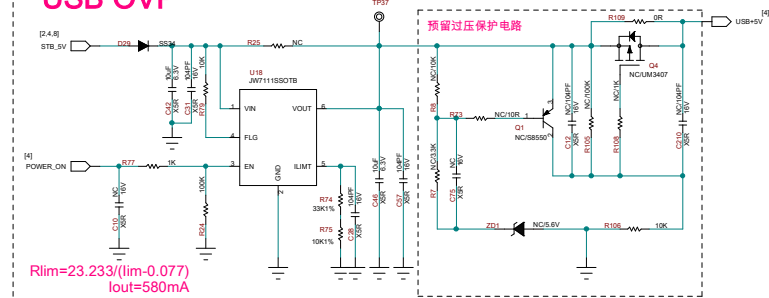
当ENT22控制脚为低时，DVDD_CORE_FB为1.01V；
当ENT22控制脚为高时，DVDD_CORE_FB为0.89V；



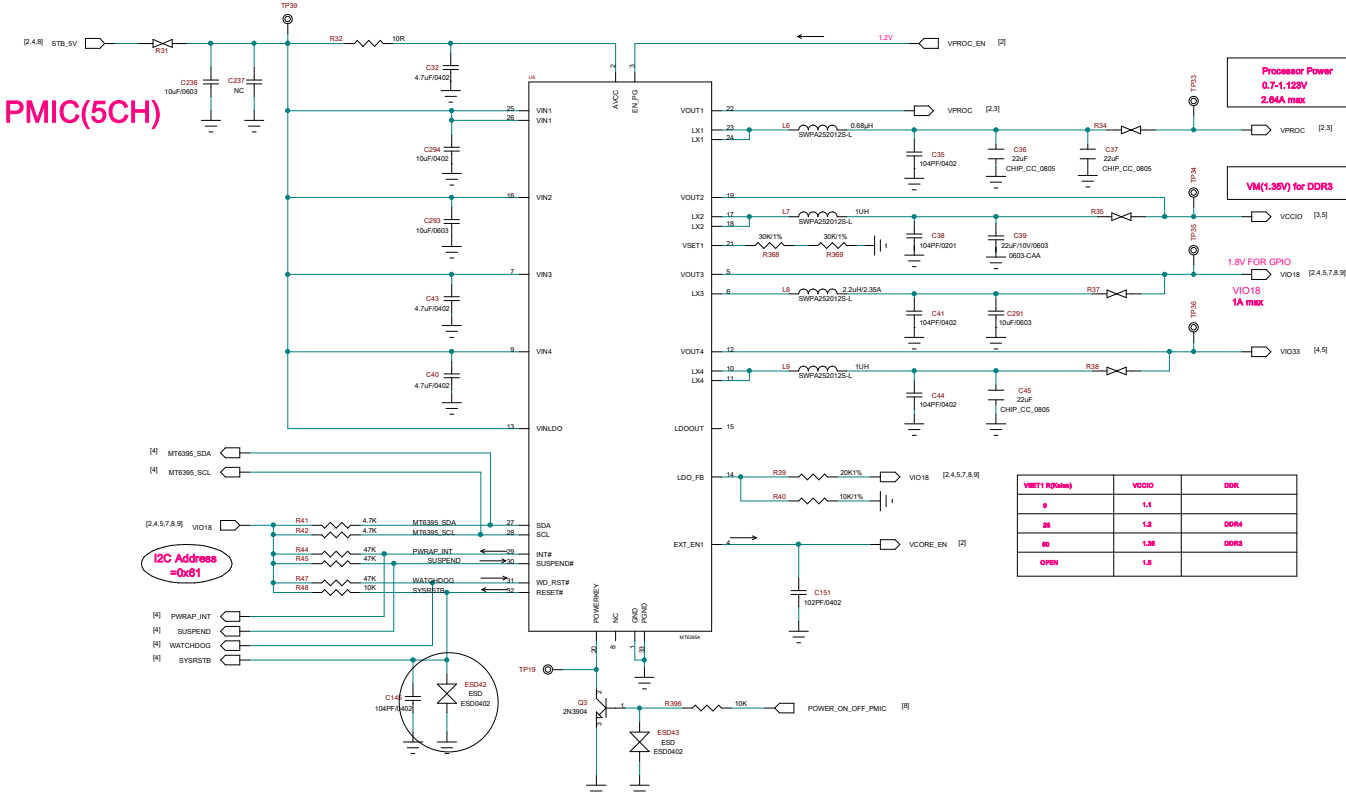
SYS&WIFI 3V3 POWER



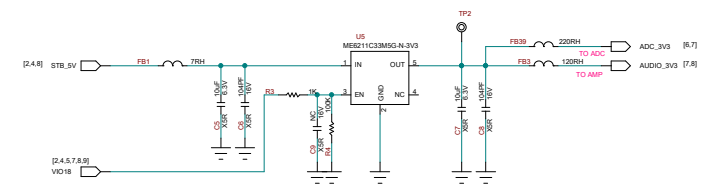
USB OVP



PMIC(5CH)



ADC&AUDIO 3V3 POWER



实际板载匹配负载电容CL=18PF±5%

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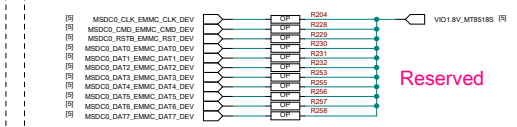
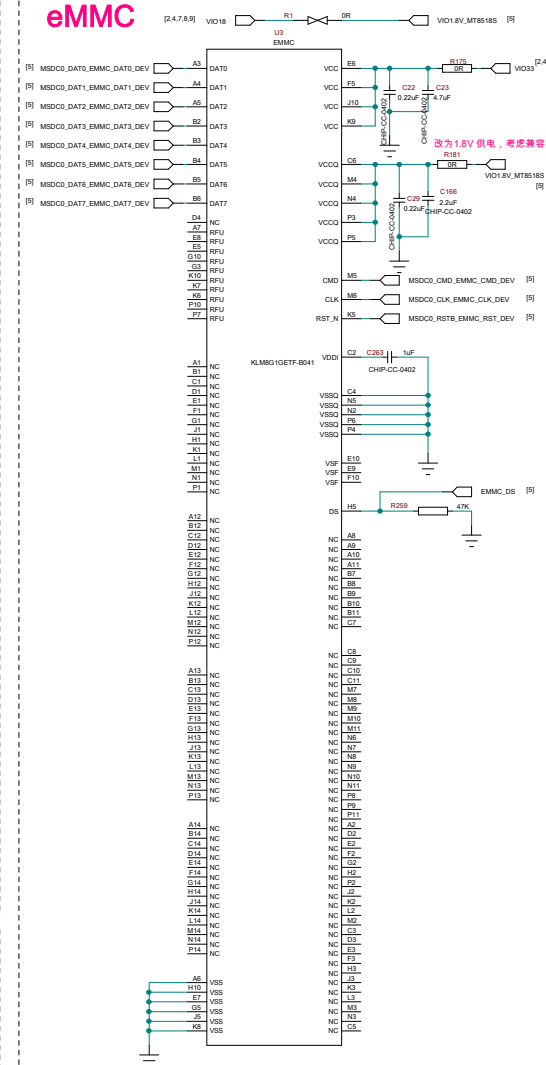
[3,4,5,7] V018_M18518S RS9 10R AVDD18_AP Y2 AVDD18_AP

From PMC MT6395A

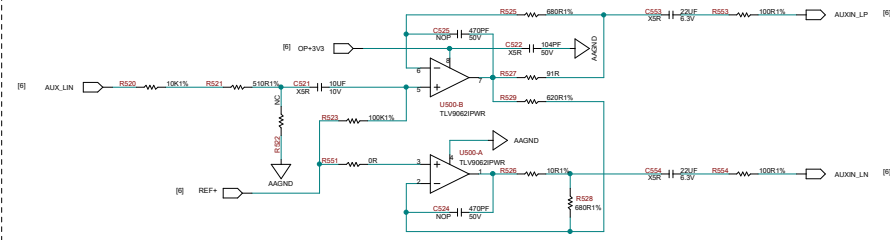
MCU	MT8518S_UTXD2_MCU	R102	22R0201	UTXD2
	MT8518S_URXD2_MCU			URXD2

[8]	MT8518S_GPIO2_MCU	AB3	SPI1_MO
[8]	MT8518S_GPIO1_MCU	AC2	SPI1_MI
[8]	MT8518S_GPIO3_MCU	AB2	SPI1_CS

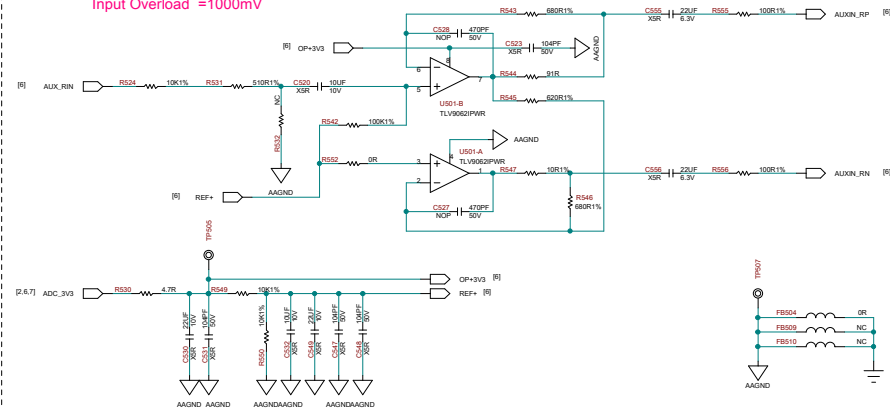
eMMC



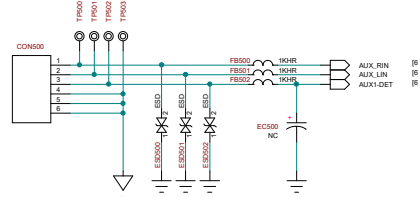
Aux_in Differential Circuit



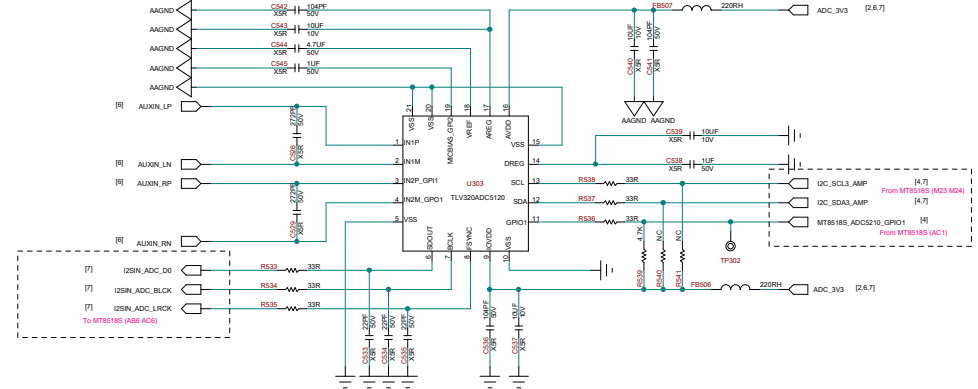
Input Sensitivity =370mV
Input Overload =1000mV



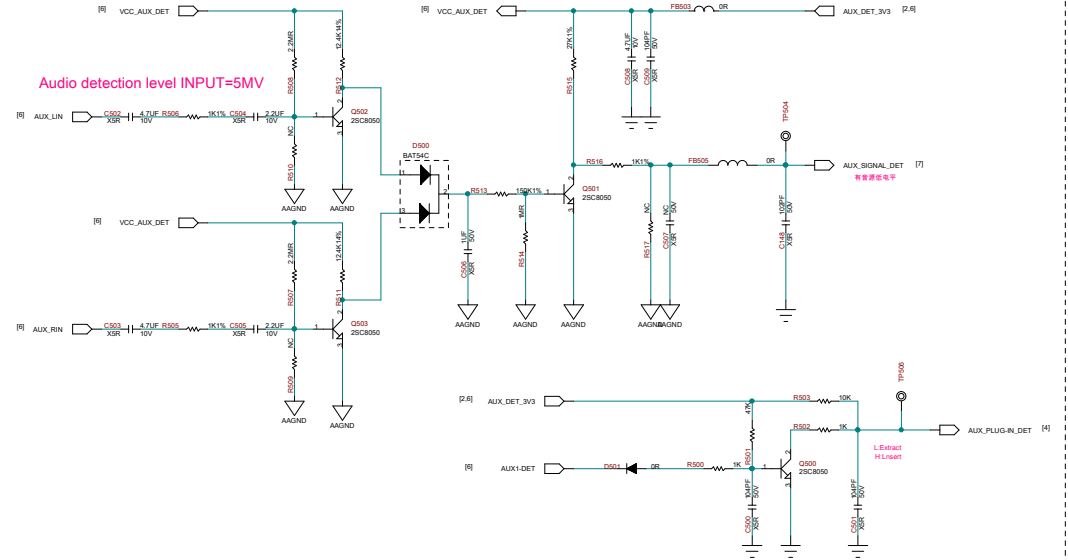
To AUX_IN JACK Board 6P/1.0mm Reverse Side FFC Cable

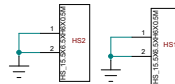
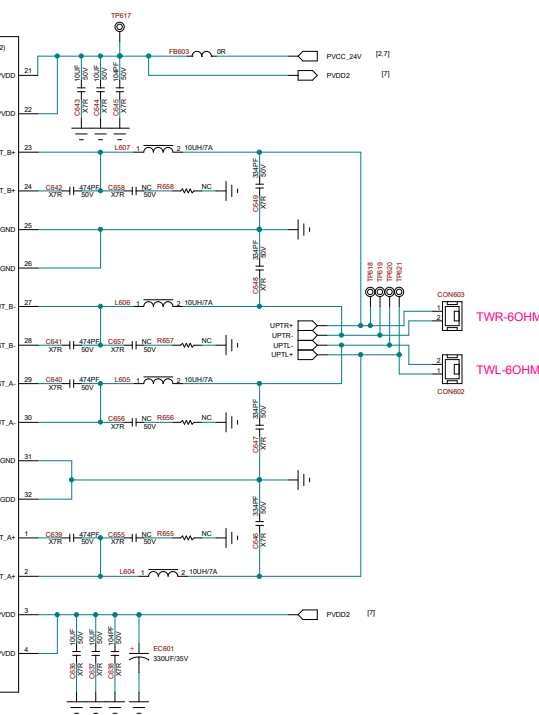
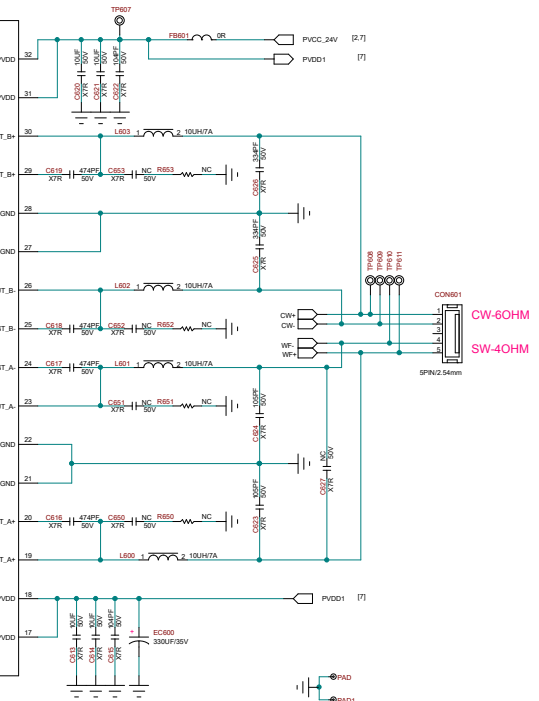


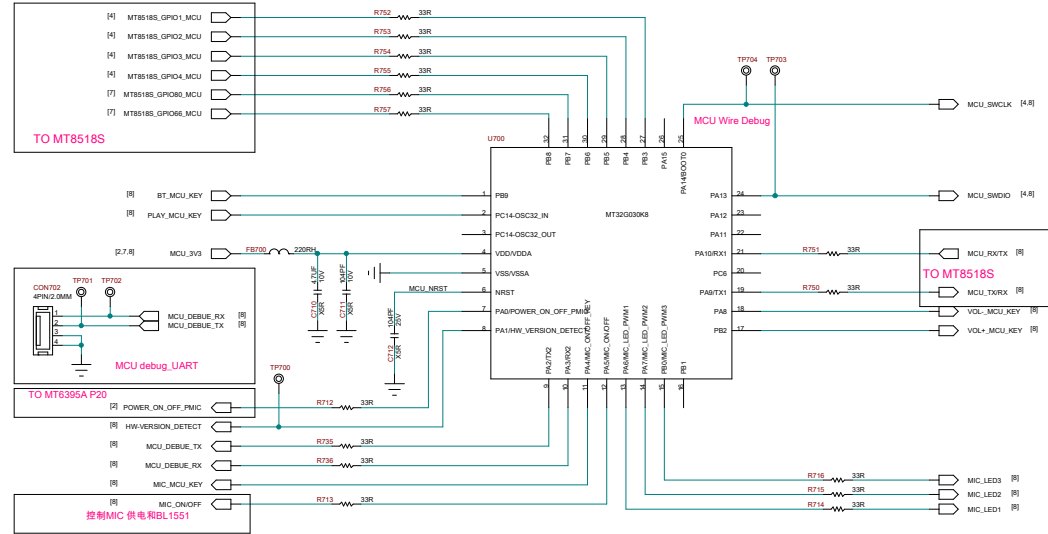
ADC Section Circuit



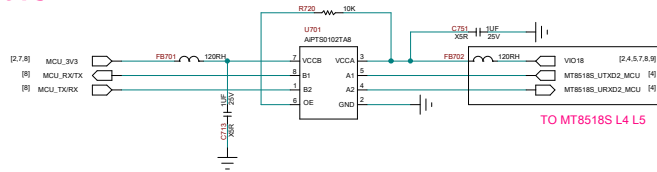
AUX_IN_DET_SYSTEM



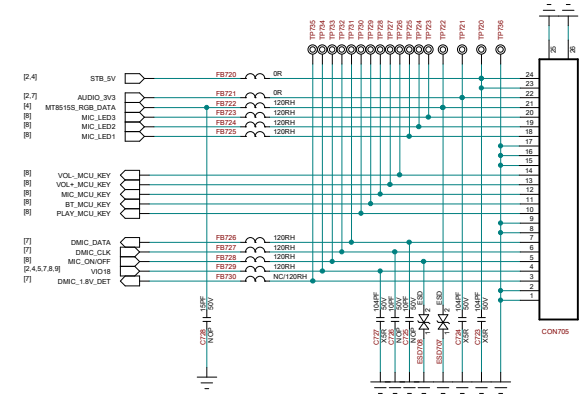
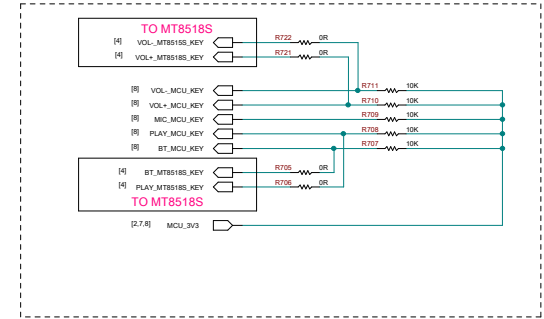
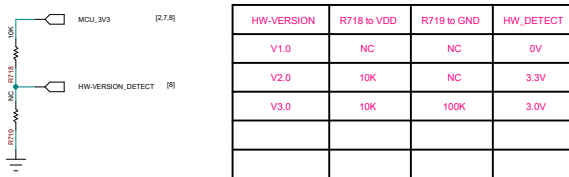


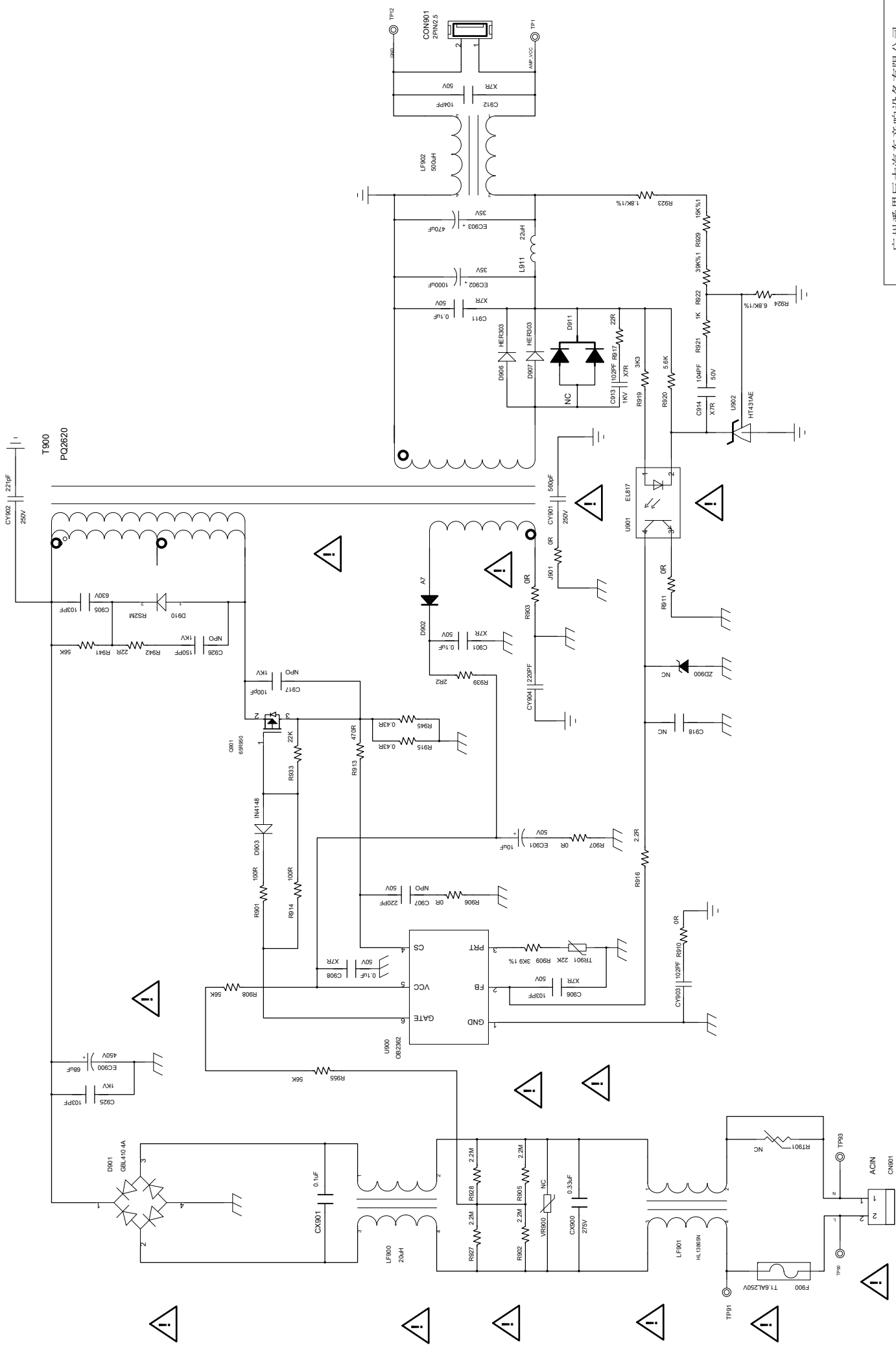


Level shift IC



MCU_HW_VERSION





广州番禺巨太汽车音响设备有限公司

Design: LIAOYIBANG

Model: JBL Baby SYSTEM

Check: SMPS

Block: SMPS

Date: 2023-4-15

Rev: 0.2

Sheet: 2 of 2

Approve: