

D

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D

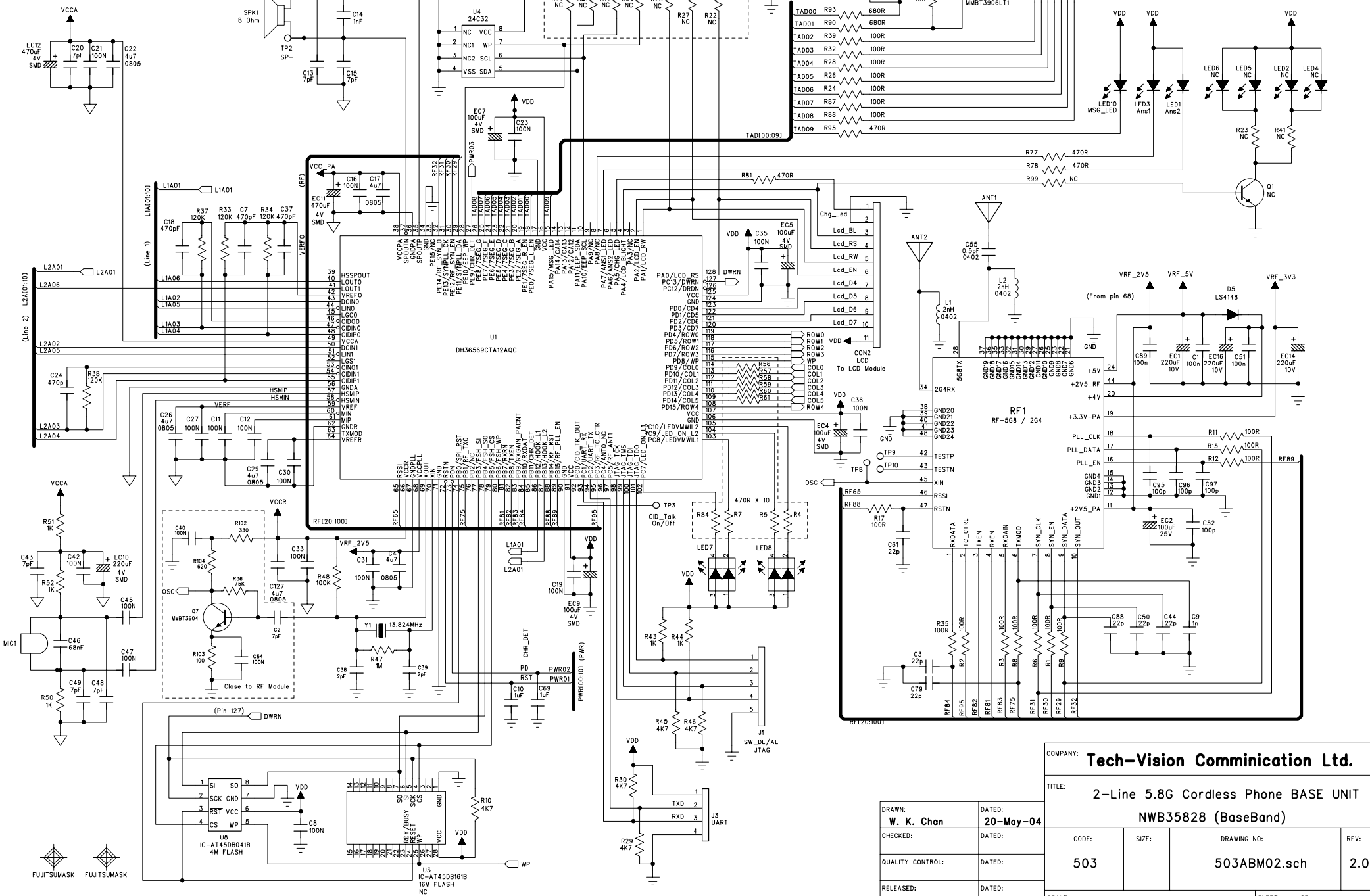
C

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Boot Atmel 16M: R21 & R14 = 4K7
R22 & 27

Boot Mask Chip: R151,R21,R14 & R20 = NC
R22,R27 & 25



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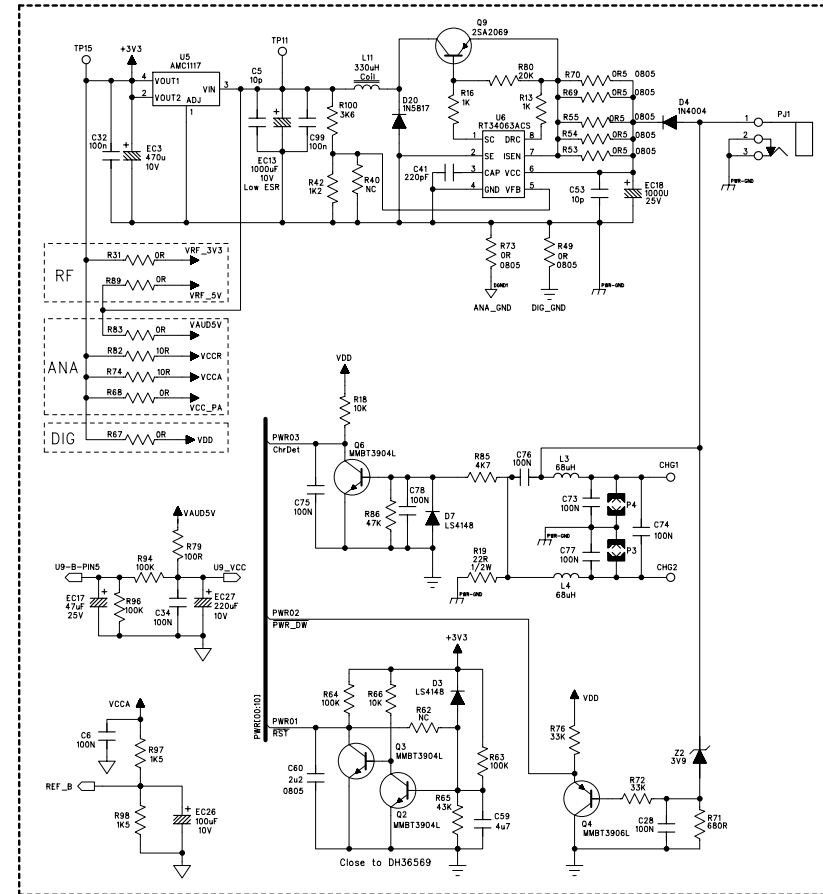
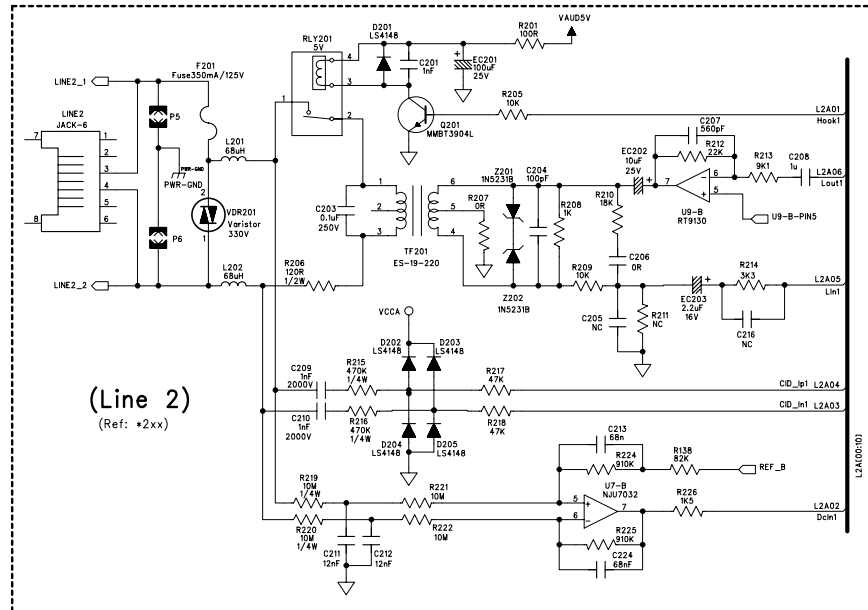
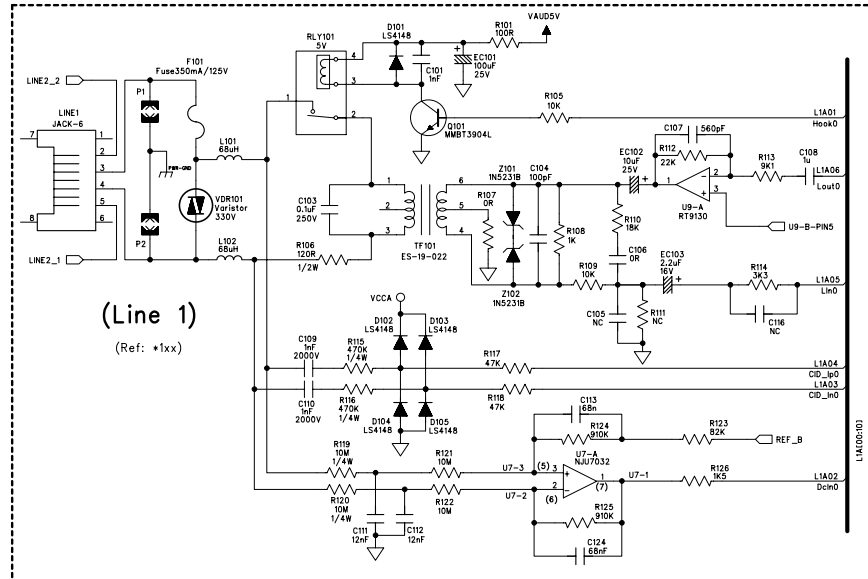
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: **Tech-Vision Communication Ltd.**

TITLE: **NWB35828 Base Line Interface**

DRAWN: **W. K. Chan**

DATED: **20-May-04**

CHECKED:

DATED:

QUALITY CONTROL:

DATED:

RELEASED:

DATED:

CODE:

503

SIZE:

503ABM02.sch

DRAWING NO:

503ABM02.sch

REV:

2.0

SCALE: SHEET: 2 OF 3

6

5

4

3

2

1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

C

B

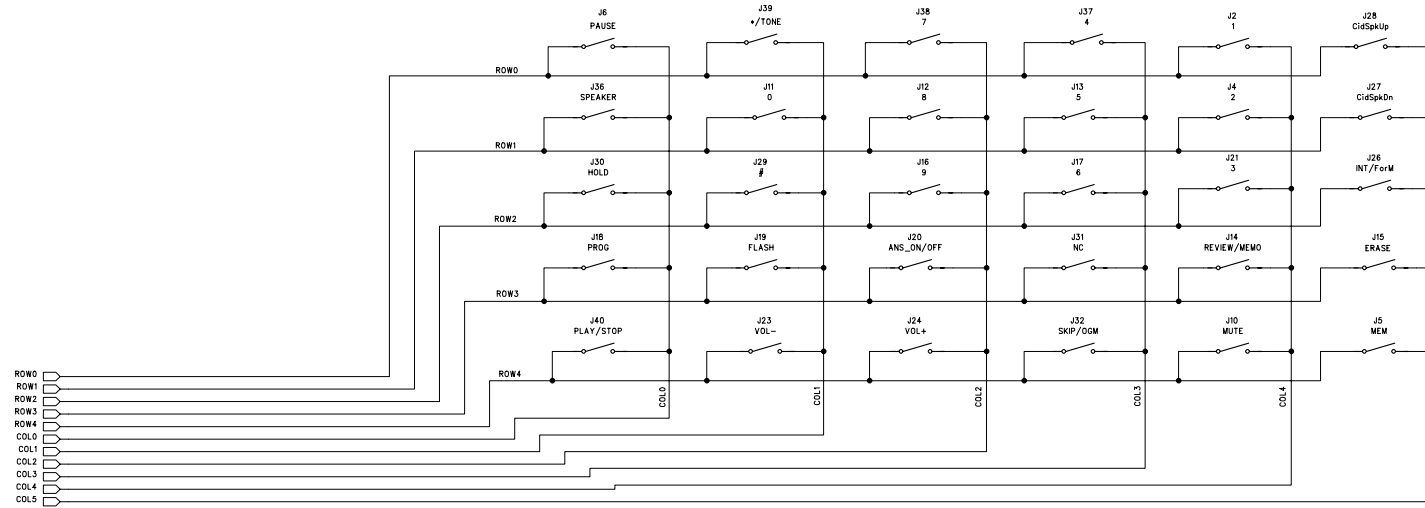
A

D

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DRAWN:	DATED:
W. K. Chan	20-May-04
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

COMPANY: Tech-Vision Communication Ltd			
TITLE: NWB35828 Base Key Assignment			
CODE:	SIZE:	DRAWING NO:	REV:
503		503ABM02.sch	02
SCALE:		SHEET: 3 OF 3	

