

Operational Principle

1. CPU,NT2012-SE65K have an 8-bit microprocessor designed and developed with low-power and high-speed CMOS technology. Operation frequency is 3.5 MHz with internal RC oscillator., This chip is responsible to control RF-Baseband Transceiver(HS6200).
2. Matrix,8*18 matrix with eight input line and eighteen output line.
3. 2.4 GHz ISM Band Transceiver/Framer,HS6200 which is a low-cost,fully integrated CMOS radio frequency(RF) transceiver block,combined with a 64-byte buffered framer block.It contains transmit,receive,VCO and PLL functions,including an on-chip channel filter and resonator. It needs an external 16MHz crystal for reference frequency;
4. ANTENNA, The antenna is typically fed from the end of the monopole section by a plated through-hole via which is in turn connected to the RF output on the 2.4GHz band.

Channel list

Channel	Frequency	Channel	Frequency
1	2402 MHz	21	2442 MHz
2	2404 MHz	22	2444 MHz
3	2406 MHz	23	2446 MHz
4	2408 MHz	24	2448 MHz
5	2410 MHz	25	2450 MHz
6	2412 MHz	26	2452 MHz
7	2414 MHz	27	2454 MHz
8	2416 MHz	28	2456 MHz
9	2418 MHz	29	2458 MHz
10	2420 MHz	30	2460 MHz
11	2422 MHz	31	2462 MHz
12	2424 MHz	32	2464 MHz
13	2426 MHz	33	2466 MHz
14	2428 MHz	34	2468 MHz
15	2430 MHz	35	2470 MHz
16	2432 MHz	36	2472 MHz
17	2434 MHz	37	2474 MHz
18	2436 MHz	38	2476 MHz
19	2438 MHz	39	2478 MHz
20	2440 MHz	40	2480 MHz