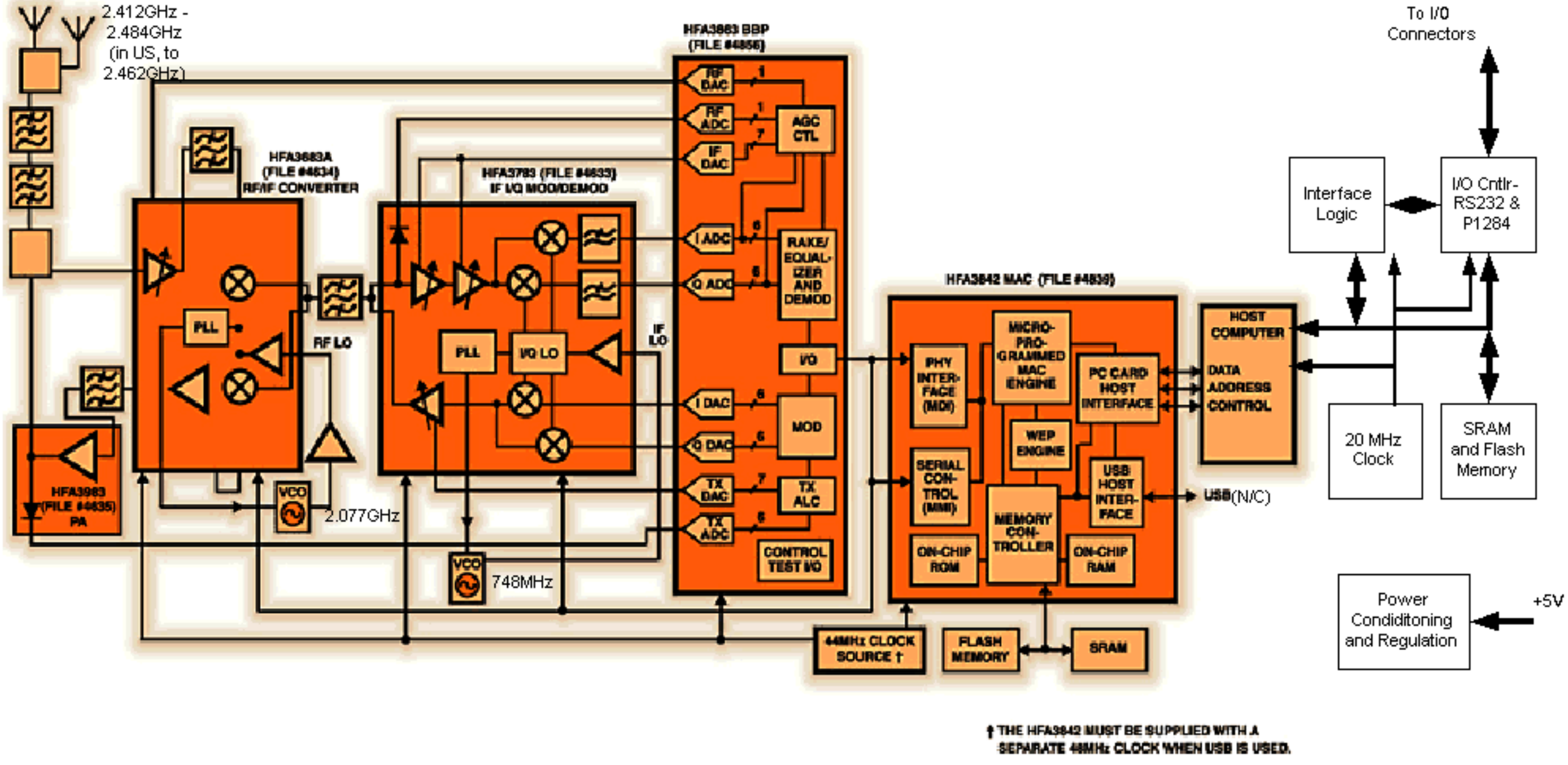
Exhibit N: Frequency Block Diagram

FCC ID: HN2EASYLAN


2.412GHz - 2.484GHz (in US, to 2.462GHz)
HFA3683A (FILE #4634) RF/IF CONVERTER
HFA3783 (FILE #4633) IF I/Q MOD/DEMOD
HFA3863 BBP (FILE #4856)
HFA3842 MAC (FILE #4839)
HFA3983 (FILE #4635) PA
PLL
RF LO
IF LO
I/Q LO
VCO
2.077GHz
748MHz
RF DAC
RF ADC
IF DAC
I ADC
Q ADC
I DAC
Q DAC
TX DAC
TX ADC
AGC CTL
RAKE/ EQUAL-IZER AND DEMOD
I/Q
MOD
TX ALC
CONTROL TEST I/O
PHY INTER-FACE (MDI)
MICRO-PRO-GRAMMED MAC ENGINE
PC CARD HOST INTERFACE
WEP ENGINE
SERIAL CON-TROL (MMI)
MEMORY CON-TROLLER
USB HOST INTER-FACE
ON-CHIP ROM
ON-CHIP RAM
HOST COMPUTER
DATA
ADDRESS
CONTROL
USB(N/C)
44MHz CLOCK SOURCE †
FLASH MEMORY
SRAM
To I/O Connectors
Interface Logic
I/O Cntlr-RS232 & P1284
20 MHz Clock
SRAM and Flash Memory
Power Condiditoning and Regulation
+5V
† THE HFA3842 MUST BE SUPPLIED WITH A SEPARATE 44MHz CLOCK WHEN USB IS USED.