

## **General Description**

BDE-BW330P1 is a 2.4-GHz Wi-Fi 6 and Bluetooth Low Energy 5.4 combo wireless module series based on Tl's 10<sup>th</sup> generation connectivity combo chip CC3301/CC3300 which is based upon proven technology and complements of TI integrated devices for connectivity portfolio. This module series is ideal for use in cost sensitive embedded applications with a Linux or RTOS host running TCP/IP, where the peak throughput requirement is 50 Mbps maximum at the IP layer. BDE-BW330P1 module series could be the best choice for bringing the efficiency of Wi-Fi 6 to embedded device applications with a small PCB footprint and highly optimized bill of materials with lower cost. The module is backward compatible with Wi-Fi 4 (802.11 b/g/n) and Wi-Fi 5 (802.11 ac).

In order to fulfil different integration requirements, BDE provides different variants. They are listed in Table 1.

**Table 1. Module Variants** Orderable Part Number | Connectivity **Antenna Options Antenna Diversity Support Operating Temperature** Wi-Fi 6 2.4-GHz SISO BDE-BW3301NP1 ANT pin Not supported -40 °C to +85 °C & BLE 5.4 Wi-Fi 6 2.4-GHz SISO BDE-BW3301UP1 -40 °C to +85 °C U.Fl. connector Not supported & BLE 5.4 Wi-Fi 6 2.4-GHz SISO BDE-BW3301AP1 -40 °C to +85 °C PCB trace antenna Not supported & BLE 5.4 Wi-Fi 6 2.4-GHz SISO BDE-BW3300NP1 ANT pin Not supported -40 °C to +85 °C & BLE 5.4 Wi-Fi 6 2.4-GHz SISO BDE-BW3300UP1 -40 °C to +85 °C U.Fl. connector Not supported & BLE 5.4 Wi-Fi 6 2.4-GHz SISO BDE-BW3300AP1 PCB trace antenna Not supported -40 °C to +85 °C & BLE 5.4 Wi-Fi 6 2.4-GHz SISO BDE-BW3301NP1-IN ANT pin Not supported -40 °C to +105 °C & RIF 5 4 Wi-Fi 6 2.4-GHz SISO BDE-BW3301UP1-IN U.FL connector -40 °C to +105 °C Not supported & BLE 5.4 Wi-Fi 6 2.4-GHz SISO BDE-BW3301AP1-IN PCB trace antenna -40 °C to +105 °C Not supported & BLE 5.4 Wi-Fi 6 2.4-GHz SISO BDE-BW3300NP1-IN ANT pin -40 °C to +105 °C Not supported & BLE 5.4 Wi-Fi 6 2.4-GHz SISO BDE-BW3300UP1-IN **U.FL** connector Not supported -40 °C to +105 °C & BLE 5.4 Wi-Fi 6 2.4-GHz SISO BDE-BW3300AP1-IN PCB trace antenna Not supported -40 °C to +105 °C & BLE 5.4 Wi-Fi 6 2.4-GHz SISO BDE-BW3301NP1-IN -40 °C to +105 °C ANT pin Not supported & BLE 5.4 Wi-Fi 6 2.4-GHz SISO **U.FL** connector Not supported -40 °C to +105 °C

BDE-BW3301UP1-IN & BLE 5.4

**Key Features** 

- Highly optimized Wi-Fi 6 and Bluetooth Low Energy 5.4 system for low cost embedded IoT applications
- Seamless integration with any processor or MCU host capable of running a TCP/IP stack
- Integrated 2.4-GHz PA for complete wireless solution with up to +20 dBm output power
- Application throughput up to 50 Mbps
- Wi-Fi 6
  - 2.4 GHz, 20 MHz, single spatial stream
  - MAC, baseband, and RF transceiver with support for IEEE 802.11 b/g/n/ax

- Target wake time (TWT), OFDMA, MU-MIMO (Downlink), Basic Service Set Coloring, and trigger frame for improved efficiency
- Hardware-based encryption and decryption supporting WPA2 and WPA3
- **Excellent interoperability**
- Support for 4 bit SDIO or SPI host interfaces
- Bluetooth Low Energy 5.4
  - LE Coded PHYs (Long Range), LE 2M PHY (High Speed) and Advertising Extension
  - Host controller interface (HCI) transport with option for UART or shared SDIO



- Enhanced Security
  - Secured host interface
  - > Firmware authentication
  - Anti-rollback protection
- Multirole support (for example, concurrent STA and AP) to connect with Wi-Fi devices on different RF channels (Wi-Fi networks)
- 3-wire or 1-wire PTA for external coexistence with additional 2.4-GHz radios (for example, Thread or Zigbee)
- Operating temperature: -40°C to +85°C or -40°C to +105°C
- Power Management
  - VDD\_1V8: 1.62 V 1.98 V
  - VDD 3V3: 2.1 V 4.2 V
- Clock Source
  - On-board 40 MHz XTAL fast clock

- External 32.768-kHz slow clock by default
- Antenna Options
  - ANT pin for external antenna
  - U.FL connector for external antenna
  - Integrated PCB trace antenna
- Package
  - LGA-64, 13.4-mm x 13.3-mm x 2-mm
  - > LGA-64, 18.4-mm x 13.3-mm x 2-mm
  - Pin to Pin Compatible with TI's WL1837MOD
  - Pin to Pin Compatible with BDE's BDE-BW2837
- Qualification and Regulatory Compliance
  - Bluetooth SIG
  - ➤ FCC
  - ➤ IC
  - ➤ TELEC
  - CE-RED

## **Applications**

- Grid Infrastructure
  - Electricity Meter
  - String Inverter
  - Micro Inverter
  - Energy Storage Power Conversion System (PCS)
  - EV Charging Infrastructure
- Building and Home Automation
  - HVAC Controller
  - HVAC Gateway
  - > Thermostat
  - Building Security Gateway
  - Garage Door System
  - ➤ IP Network Camera/ Video Doorbell
  - Wireless Security Camera
- Appliances
  - Refrigerator & Freezer
  - Oven
  - Washer & Dryer

- Residential Water Heater & Heating System
- Air Purifier & Humidifier
- Coffee Machine
- > Air Conditioner Indoor Unit
- Vacuum Robot
- Robotic Lawn Mower
- Medical
  - > Infusion Pump
  - Electronic Hospital Bed & Bed Control
  - Multiparameter Patient Monitor
  - Blood Pressure Monitor
  - CPAP Machine
  - > Telehealth Systems
  - Ultrasound Scanner
  - Ultrasound Smart Probe
  - Electric Toothbrush
- Retail Automation and Payment
- Printers



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## **References**

- 1. CC3300 resources: <a href="https://www.ti.com/product/CC3300">https://www.ti.com/product/CC3300</a>;
- 2. CC3301 resources: <a href="https://www.ti.com/product/CC3301">https://www.ti.com/product/CC3301</a>;



## 1. System Overview

### 1.1. Block Diagram

BDE-BW330P1 module series is based on the TI's 10<sup>th</sup> generation connectivity combo chip CC3301/CC3300. The module series, as seen in below diagrams, depending on different configurations, comprises of:

- 40-MHz XTAL
- Bandpass filter
- Decoupling capacitors
- U.FL connector (U.FL variants)
- PCB trace antenna (BDE-BW3301AP1)



Figure 1. The block diagram of BDE-BW3301NP1 and BDE-BW3300NP1

BDE Wi-Fi 6 & BLE Combo Module Based on CC3301/CC3300



Figure 2. The block diagram of BDE-BW3301UP1 and BDE-BW3300UP1



Figure 3. The block diagram of BDE-BW3301AP1 and BDE-BW3300AP1



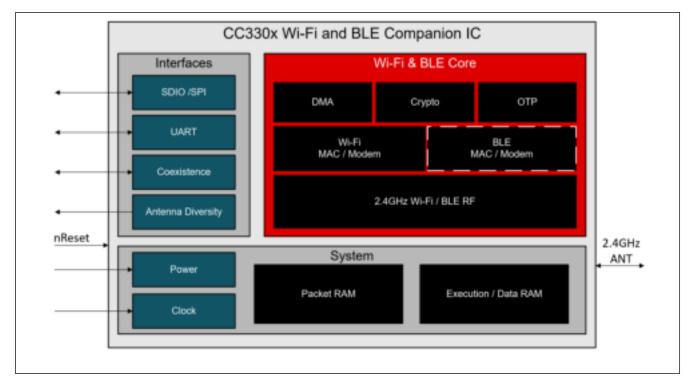


Figure 4. The block diagram of CC3301 (Adopted form CC3301 Datasheet)

### 2. Pinout Functions

### 2.1. Pin Diagram

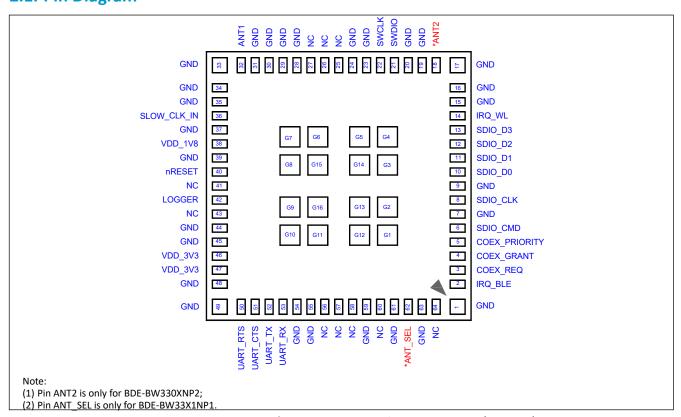


Figure 5. Pin Diagram of BDE-BW330XNP1 and BDE-BW330XNP2 (Top View)



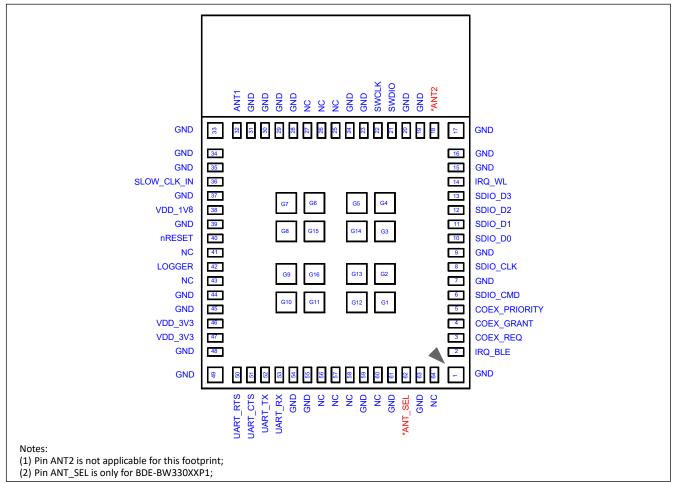


Figure 6. Pin Diagram of BDE-BW330XAP1, BDE-BW330XUP1 and BDE-BW330XUP2 (Top View)

### 2.2. Pinout Description

**Table 2. Pinout Description** 

Module Pin #	Pin Name	Туре	Voltage Level	Shutdown State (1)	State After Power-up (1)	Description
1	GND	Ground	-	-	-	Power ground
2	IRQ_BLE (2)	0	VDD_1V8	PD	PD	Interrupt request to host for BLE (in shared SDIO mode)
3	COEX_REQ	I	VDD_1V8	PU	PU	External coexistence interface – request
4	COEX_GRANT	0	VDD_1V8	PD	PD	External coexistence interface – grant
5	COEX_PRIORITY	1	VDD_1V8	PU	PU	External coexistence interface – priority
6	SDIO_CMD	1/0	VDD_1V8	Hi-Z	Hi-Z	SDIO command or SPI PICO
7	GND	Ground	-	-	-	Power ground
8	SDIO_CLK	I	VDD_1V8	Hi-Z	Hi-Z	SDIO clock or SPI clock
9	GND	Ground	-	-	-	Power ground
10	SDIO_D0	1/0	VDD_1V8	Hi-Z	Hi-Z	SDIO data D0 or SPI POCI
11	SDIO_D1	1/0	VDD_1V8	Hi-Z	Hi-Z	SDIO data D1
12	SDIO_D2	1/0	VDD_1V8	Hi-Z	Hi-Z	SDIO data D2
13	SDIO_D3	1/0	VDD_1V8	Hi-Z	PU	SDIO data D3 or SPI CS
14	IRQ_WL (2)	0	VDD_1V8	PD	PD	Interrupt request to host for WLAN
15	GND	Ground	-	-	-	Power ground
16	GND	Ground	-	-	-	Power ground
17	GND	Ground	-	-	-	Power ground
18	ANT2	RF	-	-	-	Secondary antenna for antenna diversity, only for BDE-BW330XNP2
10	NC	-	-	-	-	No connect for other variants except for BDE- BW330XNP2
19	GND	Ground	-	-	-	Power ground
20	GND	Ground	-	-	-	Power ground
21	SWDIO	1/0	VDD_1V8	PU	PU	Serial wire debug I/O



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Module Pin #	Pin Name	Туре	Voltage Level	Shutdown State (1)	State After Power-up (1)	Description
22	SWCLK	1	VDD_1V8	PD	PD	Serial wire debug clock
23	GND	Ground	-	-	-	Power ground
24	GND	Ground	-	-	-	Power ground
25	NC	-	-	-	-	No connect
26	NC	-	-	-	-	No connect
27	NC	-	-	-	-	No connect
28	GND	Ground	-	-	-	Power ground
29	GND	Ground	-	_	_	Power ground
30	GND	Ground	-	-	_	Power ground
31	GND	Ground	-	-	_	Power ground
						Bluetooth Low Energy and WLAN 2.4-GHz
32	ANT1	RF	-	-	-	antenna port
33	GND	Ground	-	-	-	Power ground
34	GND	Ground	-	-	-	Power ground
35	GND	Ground	-	-	-	Power ground
36	SLOW CLK IN	1	VDD 1V8	PD	PD	32.768-KHz RTC clock input
37	GND	Ground	-	-	-	Power ground
						1.8V power supply for SRAM, digital, analog, I/O,
38	VDD_1V8	Power	-	-	-	and programming
39	GND	Ground	_	_	_	Power ground
						Reset line for enabling or disabling device
40	nRESET	1	VDD_1V8	PU	PU	(active low)
41	NC	<u> </u>	_	<u> </u>	_	No connect
42	LOGGER (2)	0	VDD 1V8	PU	PU	Tracer (UART TX debug logger)
43	NC NC		-	-	-	No connect
44	GND	Ground	-	_	-	Power ground
45	GND	Ground	_	<del>  -</del>	-	Power ground
	GIND	Ground	-	-	-	Fower ground
46 47	VDD_3V3	Power	-	-	-	3.3V power supply for PA
48	GND	Ground	-	_	-	Power ground
49	GND	Ground	_	-	-	Power ground
50	UART RTS	O	VDD 1V8	PU	PU	UART RTS signal - flow control for BLE HCI
		10		PU	PU	
51	UART_CTS	1	VDD_1V8			UART CTS signal - flow control for BLE HCI
52	UART_TX	0	VDD_1V8	PU	PU	UART TX for BLE HCI
53	UART_RX	Constant	VDD_1V8	PU	PU	UART RX for BLE HCI
54	GND	Ground	-	-	-	Power ground
55	GND	Ground	-	-	-	Power ground
56	NC	-	-	-	-	No connect
57	NC	-	-	-	-	No connect
58	NC	ļ	-	-	-	No connect
59	GND	Ground	-	-	-	Power ground
60	NC	ļ	-	-	-	No connect
61	GND	Ground	-	-	-	Power ground
62	ANT_SEL	0	VDD_1V8	PD	PD	Antenna select control line for antenna diversity, only for BDE-BW330XNP1, BDE-BDE- BW330XUP1, BDE-BW330XAP1
	NC	-	-	-	-	No connect for BDE-BW330XNP2, BDE-BW330XUP2
63	GND	Ground	-	-	-	Power ground
64	NC	-	-	-	-	No connect
G1 – G16	GND	Ground	-	-	-	Power ground, thermal pads

#### Note:

<sup>(1)</sup> All digital I/Os are with internal PU/PD according to the "shutdown state" column when the device is in shutdown mode (with the exception of SDIO signals are Hi-Z). PU means pull-up, PD means pull-down, Hi-Z means high impedance;
(2) LOGGER, IRQ\_BLE and IRQ\_WL pins are sensed by the device during boot. They should be kept "100" state on power-up with LOGGER pin being high.



### 3. Characteristics

#### 3.1. Electrical Characteristics

### 3.1.1. Absolute Maximum Ratings

Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, so functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specification are not implied. Exposure to Absolute Maximum Rating conditions for extended periods may affect device reliability.

**Table 3. Absolute Maximum Ratings** 

Parameter	MIN	MAX	Unit
VDD_3V3	-0.5	4.2	V
VDD_1V8	-0.5	2.1	V
Operating ambient temperature	-40	105	°C
Storage temperature	-40	105	°C

### 3.1.2. ESD Ratings

**Table 4. ESD Ratings** 

Parameter	Description	Value	Unit	Note
Electrostatic discharge	Contact discharge	4000	V	As per EN 301-489
Electrostatic discharge	Air discharge	8000	V	As per EN 301-489

### 3.1.3. Recommended Operating Conditions

**Table 5. Recommended Operating Conditions** 

Parameter	MIN	TYP	MAX	Unit
VDD_3V3	3	3.3	3.6	V
VDD_1V8	1.62	1.8	1.98	V
Operating ambient temperature	-40		85	°C
Operating ambient temperature (-IN variants)	-40		105 (1)	°C

#### Note:

### 3.1.4. I/O DC Characteristics

Table 6. I/O DC Characteristics

Parameter	Description	Test Condition	MIN	TYP	MAX	Unit
$V_{IH}$	High level input voltage		0.65 x VDD_18V		VDD_1V8	V
$V_{IL}$	Low level input voltage		0		0.35 x VDD_1V8	V
V <sub>OH</sub>	High level output voltage	At 4mA	VDD_1V8 - 0.45		VDD_1V8	V
V <sub>OL</sub>	Low level output voltage	At 4mA	0		0.45	V

### 3.1.5. Power Consumption

The measurement is made with the evaluation module (EM board) BDE-BW33PN-EM at room temperature, unless otherwise noted.

<sup>(1) -</sup>IN variants module may operate at temperature of up to 105 °C. This allows the device to be used reliably in applications that may be exposed to higher ambient temperature over certain periods of the product's life. At temperatures higher than 85 °C, the WLAN/BLE performance may degrade.



**Table 7. Current Consumption – WLAN Static Modes** 

Parameter	Test Condition		Supply	TYP	Unit
	1 DSSS	TX power = 20 dBm	VDD_1V8	100	
	1 0333	1X power = 20 dBill	VDD_3V3	210	
	6 OFDM	TX power = 20 dBm	VDD_1V8	105	
	0 OFDIVI	1X power = 20 dBill	VDD_3V3	220	
	54 OFDM	TX power = 17 dBm	VDD_1V8	100	
	34 OI DIVI	1X power = 17 dBill	VDD_3V3	178	
Continuous TX (1)	HT MCS0	TX power = 20 dBm	VDD_1V8	107	
Continuous 1X	111 MC30 1X	1X power = 20 dBill	VDD_3V3	214	mA
	HT MCS7	TX power = 17 dBm	VDD_1V8	105	IIIA
	TTT IVIC37	17 power = 17 dBill	VDD_3V3	165	
	HE MCSO	TX power = 20 dBm	VDD_1V8	105	
	TIL IVICSO	1X power = 20 dBill	VDD_3V3	215	
	HE MCS7	TX power = 20 dBm	VDD_1V8	100	
	TIL IVIC3/	17 power – 20 ubili	VDD_3V3	188	
Continuous RX			VDD_1V8	62	
CONTINUOUS KX			VDD_3V3	0	

#### Note:

(1) Peak current VDD\_3V3 can hit 340mA during device calibration; Peak current VDD\_1V8 of 185mA including peripherals and internal cortex.

Table 8. Current Consumption - WLAN Use Cases

Mode	Description	TYP	Unit
DTIM = 1	System with 3.3V to Ext. DC/DC at 85% efficiency WLAN beacon reception every DTIM=1 (~102ms)	637	
DTIIVI = 1	System with 1.8V WLAN beacon reception every DTIM=1 (~102ms)	980	
DTIM = 3	System with 3.3V to Ext. DC/DC at 85% efficiency WLAN beacon reception every DTIM=1 (~102ms)	371	
DTIIVI = 3	System with 1.8V WLAN beacon reception every DTIM=1 (~102ms)	570	μΑ
DTIM = 5	System with 3.3V to Ext. DC/DC at 85% efficiency WLAN beacon reception every DTIM=1 (~102ms)	319	
ב - ווווט	System with 1.8V WLAN beacon reception every DTIM=1 (~102ms)	490	

Table 9. Current Consumption - BLE Static Modes

Parameter	Test Condition	Supply	TYP	Unit
	TX power = 0 dBm	VDD_1V8	104	Unit mA
	1x power = 0 dBill	VDD_3V3	40	
TV may duty cyclo	TV nower = 10 dDm	VDD_1V8	100	
TX, max duty cycle	TX power = 10 dBm	VDD_3V3	98	
	TV names 20 dBm	VDD_1V8	105	
	TX power = 20 dBm	VDD_3V3	220	
RX		VDD_1V8	62	
NA.		VDD_3V3	0	

**Table 10. Current Consumption – Device States** 

Mode	Description	Supply	TYP	Unit	
Shutdown	External supplies are available,	VDD_1V8	10		
Shutdown	device held in reset (nRESET is low)	VDD_3V3	2		
Sleep	Low power mode – RAM in retention	VDD_1V8	330	μΑ	
Sieep	Low power mode – KANTIII retention	VDD_3V3	2		

### 3.1.6. Fast Clock Characteristics

The fast clock running at 40-MHz for WLAN/BLE functions is included in the module. The specification is shown in below table.

Table 11. 40-MHz Crystal Oscillator (HFXT) Characteristics

Parameter	Test Condition	MIN	TYP	MAX	Unit
Crystal frequency			40		MHz
ESR, Equivalent series resistance				20	Ω
Frequency tolerance	T <sub>A</sub> : 25°C	-10		+10	ppm
Frequency stability	T <sub>A</sub> : -40°C ~ 85°C/105°C <sup>(1)</sup>	-30		+30	ppm
C <sub>L</sub> , Crystal load capacitance			8		pF



#### Note:

(1) -IN variants can support up to 105 °C.

### 3.1.7. External Slow Clock Requirements

The slow clock running at 32.768-KHz for low power modes is not included in the module. The slow clock can be generated internally or externally. The external slow clock requirements are listed in below table.

Table 12. Externa	ll 32.768-KHz Slow (	Clock Requirements
-------------------	----------------------	--------------------

Parameter	Description	MIN	TYP	MAX	Unit
Crystal frequency	Square wave		32768		Hz
Frequency accuracy	Initial + temperature + aging	-250		+250	ppm
Input duty cycle		30	50	70	%
Rise and fall time	10% to 90% (rise) and 90% to 10% (fall) of digital signal level			100	ns
Input low level		0		0.35 x VDD_1V8	V
Input high level		0.65 x VDD_1V8		1.95	V
Input impedance		1			MΩ
Input capacitance				5	pF

### 3.1.8. Power Supply Sequencing

For proper operation of the module, perform the recommended power-up sequencing as follows:

- 1. VDD\_3V3 and VDD\_1V8 must be available before nRESET is released;
- 2. For an external slow clock, ensure that the clock is stable before nRESET is deasserted (high);
- 3. The nRESET pin should be held low for at least 10 us after stabilization of the external power supplies.

### 3.1.9. SDIO Timing Characteristics

SDIO is the main host interfaces for WLAN, and it supports a maximum clock rate of 52-MHz. The module also supports shared SDIO interface for both BLE and WLAN.

The timing diagram for default speed and high speed SDIO are as follows:

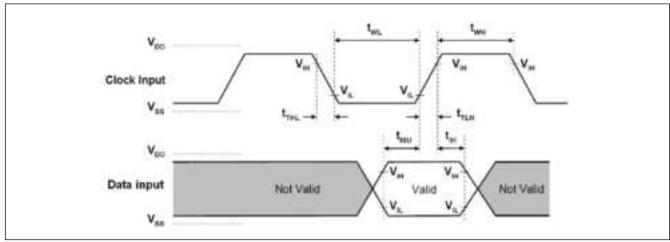


Figure 7. SDIO Default Input Timing



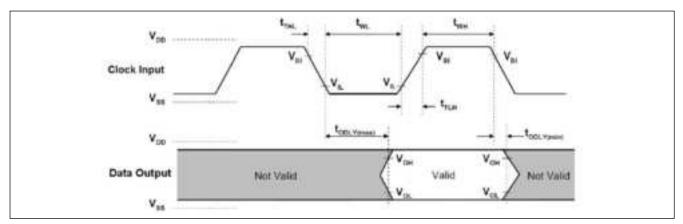


Figure 8. SDIO Default Output Timing

**Table 13. SDIO Timing Parameters - Default Speed** 

Parameter	Description	MIN	MAX	Unit
f <sub>clock</sub>	Clock frequency, CLK		26	MHz
t <sub>High</sub>	High period	10		
$t_{Low}$	Low period	10		
t <sub>TLH</sub>	Rise time, CLK		10	
t <sub>THL</sub>	Fall time, CLK		10	ns
t <sub>ISU</sub>	Setup time, input valid before CLK ↑	5		
t <sub>IH</sub>	Hold time, input valid after CLK ↑	5		
todly	Delay time, CLK ↓ to output valid	2	14	
C <sub>L</sub>	Capacitive load on outputs	15	40	pF

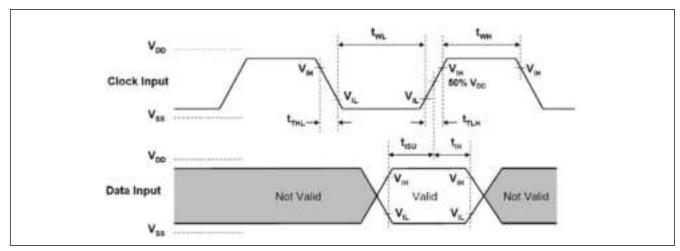


Figure 9. SDIO High Speed Input Timing



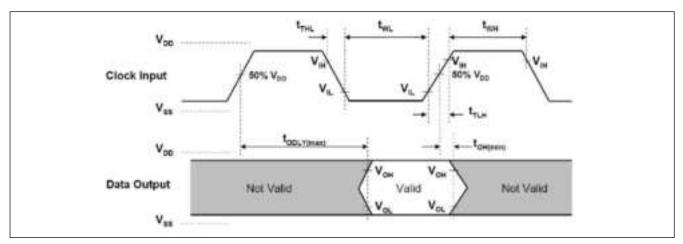


Figure 10. SDIO High Speed Output Timing

**Table 14. SDIO Timing Parameters - High Speed** 

Parameter	Description	MIN	MAX	Unit
f <sub>clock</sub>	Clock frequency, CLK		52	MHz
t <sub>High</sub>	High period	7		
t <sub>Low</sub>	Low period	7		
t <sub>TLH</sub>	Rise time, CLK		3	
t <sub>THL</sub>	Fall time, CLK		3	ns
t <sub>ISU</sub>	Setup time, input valid before CLK 1	6		
t <sub>IH</sub>	Hold time, input valid after CLK ↑	2		
t <sub>ODLY</sub>	Delay time, CLK ↓ to output valid	2	14	
C <sub>L</sub>	Capacitive load on outputs	15	40	pF

### 3.1.10. SPI Timing Characteristics

SPI is another host interface for WLAN. The module also supports shared SPI interface for both BLE and WLAN.

The timing diagram for SIP is as follows:

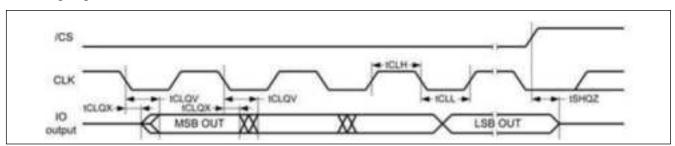


Figure 11. SDIO Default Input Timing

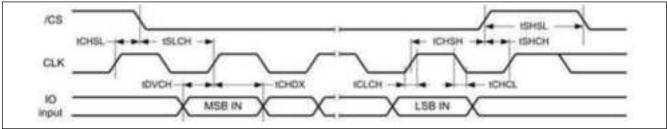


Figure 12. SPI Output Timing

**Table 15. SPI Timing Parameters** 

Parameter	Description	MIN	MAX	Unit
$f_{clock}$	Clock frequency, CLK		26	MHz
t <sub>High</sub>	High period	10		ns



Parameter	Description	MIN	MAX	Unit
t <sub>Low</sub>	Low period	10		
t <sub>TLH</sub>	Rise time, CLK		3	
t <sub>THL</sub>	Fall time, CLK		3	
t <sub>CSU</sub>	CS setup time, CS valid before CLK	3		
t <sub>ISU</sub>	PICO, input valid before CLK ↑	3		
t <sub>iH</sub>	PICO Hold time, input valid after CLK 1	3		
t <sub>Dr</sub> , t <sub>Df</sub> - Active	Delay time, CLK ↑/↓ to output valid	2	10	
t <sub>Dr</sub> , t <sub>Df</sub> - Sleep	Delay time, CLK $\uparrow / \downarrow$ to output valid		12	
$C_L$	Capacitive load on outputs	15	40	pF

### 3.1.11. UART 4-Wire Interface

UART is the main host interface for BLE, which supports host controller interface (HCI) transport layer.

#### **Table 16. UART Timing Parameters**

Parameter	Description	MIN	TYP	MAX	Unit
Baud rate	Clock frequency, CLK	37.5		4364	kbps
Baud rate accuracy per byte	Receive/Transmit	-2.5		+1.5	%
Baud rate accuracy per bit	Receive/Transmit	-12.5		+12.5	%
CTS low to TX_DATA on		0	2		ms
CTS high to TX_DATA off	Hardware flow control			1	Byte
CTS high pulse width		1			bit
RTS low to RX_DATA on		0	2		ms
RTS high to RX_DATA off	Interrupt set to 1/4 FIFO			16	Byte

### 3.2. RF Characteristics

### 3.2.1. WLAN Performance: 2.4-GHz Receiver Characteristics

#### Table 17. WLAN Performance: 2.4-GHz Receiver Characteristics

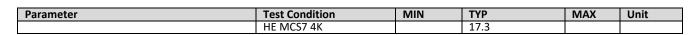
Parameter	Test Condition	MIN	TYP	MAX	Unit	
Operational frequency range		2412		2472	MHz	
	1 DSSS		-98			
	2 DSSS		-95			
	11 DSSS		-90			
Consiste the ON DED for 11h rotes 100/	6 OFDM		-93			
Sensitivity: 8% PER for 11b rates, 10%	54 OFDM		-75		dBm	
PER for 11g/n/ax rates	HT MCS0 MM 4K		-93			
	HT MCS7 MM 4K		-72			
	HE MCSO 4K		-92			
	HE MCS7 4K		-72			
	1 DSSS		0			
Maximum input level: 8% PER for 11b	6 OFDM, HT MCSO, HE MCSO		0		dBm	
rates, 10% PER for 11g/n/ax rates	54 OFDM, HT MCS7, HE		-9		ивт	
	MCS7		-9			
	1 DSSS		45			
	2 DSSS		39			
	11 DSSS		20			
Adjacent channel rejection	6 OFDM		3		dп	
Adjacent channel rejection	54 OFDM		20		dB	
	HT MCS0 MM 4K		3			
	HT MCS7 MM 4K		16			
	HE MCSO 4K		-1			
RSSI accuracy	-90 dBm to -30 dBm	-3		3	dB	

### 3.2.2. WLAN Performance: 2.4-GHz Transmitter Characteristics

Table 18. WLAN Performance: 2.4-GHz Transmitter Power

Parameter	Test Condition	MIN	TYP	MAX	Unit
Operational frequency range		2412		2472	MHz
	1 DSSS		20.5		
	6 OFDM		20.2		
Output nower at VDD 2V2 - 2.2 V	54 OFDM		17.4		dBm
Output power at VDD_3V3 = 3.3 V	HT MCS0 MM 4K		20.2		UBIII
	HT MCS7 MM 4K		17.4		
	HE MCS0 4K		20.2		





#### Note:

(1) The output power is measured at frequency 2437MHz.

### 3.2.3. BLE Performance: Receiver Characteristics

#### Table 19. BLE Performance: 2.4-GHz Receiver Characteristics

_	Table 19. BLE Performance: 2.4-GHz Receiver Characte		1			
Parameter	Test Condition	MIN	TYP	MAX	Unit	
BLE 125Kbps (LE Coded) Receiv		1	1		1	
Receiver sensitivity	PER <30.8%		-102		dBm	
Receiver saturation	PER <30.8%		0			
Co-channel rejection (1)	Wanted signal at -79 dBm, modulated interferer in channel		10			
Selectivity, ±1 MHz (1)	Wanted signal at -79 dBm, modulated interferer at ±1 MHz		0 / 0 (2)			
Selectivity, ±2 MHz (1)	Wanted signal at -79 dBm, modulated interferer at ±2 MHz		-37 / -30 <sup>(2)</sup>		dB	
Selectivity, ±3 MHz (1)	Wanted signal at -79 dBm, modulated interferer at ±3 MHz		-39 / -36 <sup>(2)</sup>		ub.	
Selectivity, ±4 MHz <sup>(1)</sup>	Wanted signal at -79 dBm, modulated interferer at ±4MHz		-45 / -41 <sup>(2)</sup>			
RSSI accuracy	-90 dBm to -20 dBm	-4		4		
BLE 500Kbps (LE Coded) Receiv						
Receiver sensitivity	PER <30.8%		-99		dBm	
Receiver saturation	PER <30.8%		0		abiii	
Co-channel rejection (1)	Wanted signal at -72 dBm, modulated interferer in channel		10			
Selectivity, ±1 MHz (1)	Wanted signal at -72 dBm, modulated interferer at ±1 MHz		0/0(2)			
Selectivity, ±2 MHz (1)	Wanted signal at -72 dBm, modulated interferer at ±2 MHz		-35 / -25 <sup>(2)</sup>		dB	
Selectivity, ±3 MHz (1)	Wanted signal at -72 dBm, modulated interferer at ±3 MHz		-40 / -37 <sup>(2)</sup>		uв	
Selectivity, ±4 MHz (1)	Wanted signal at -72 dBm, modulated interferer at ±4MHz		-45 / -40 <sup>(2)</sup>			
RSSI accuracy	-90 dBm to -20 dBm	-4		4		
BLE 1Mbps (LE 1M) Receiver Ch	naracteristics					
Receiver sensitivity	PER <30.8%, 37-byte packets		-99			
Receiver sensitivity	PER <30.8%, 255-byte packets		-98		dBm	
Receiver saturation	PER <30.8%		0			
Co-channel rejection (1)	Wanted signal at -67 dBm, modulated interferer in channel		10			
Selectivity, ±1 MHz (1)	Wanted signal at -67 dBm, modulated interferer at ±1 MHz		0 / 0 (2)			
Selectivity, ±2 MHz (1)	Wanted signal at -67 dBm, modulated interferer at ±2 MHz		-35 / -28 <sup>(2)</sup>		dB	
Selectivity, ±3 MHz (1)	Wanted signal at -67 dBm, modulated interferer at ±3 MHz		-38 / -32 <sup>(2)</sup>			
Selectivity, ±4 MHz (1)	Wanted signal at -67 dBm, modulated interferer at ±4MHz		-45 / -40 <sup>(2)</sup>			
Out-of-band blocking	30 MHz to 2000 MHz, wanted signal at -67 dBm		-23			
Out-of-band blocking	2003 MHz to 2399 MHz, wanted signal at -67 dBm		-30			
Out-of-band blocking	2484 MHz to 2997 MHz, wanted signal at -67 dBm		-30		dB	
Out-of-band blocking	3000 MHz to 6 GHz, wanted signal at -67 dBm		-21		dBm	
-	Wanted signal at 2402 MHz, -64 dBm, two interferers at		40			
Intermodulation	2405 and 2408 MHz respectively, at the given power level		-40			
RSSI accuracy	-90 dBm to -20 dBm	-4		4	dB	
BLE 2Mbps (LE 2M) Receiver Ch	naracteristics				1	
Receiver sensitivity	PER <30.8%, 37-byte packets		-95		I.D.	
Receiver saturation	PER <30.8%		0		dBm	
Co-channel rejection (1)	Wanted signal at -67 dBm, modulated interferer in channel		10			
Selectivity, ±2 MHz (1)	Wanted signal at -67 dBm, modulated interferer at ±1 MHz		0 / 0 (2)			
Selectivity, ±4 MHz (1)	Wanted signal at -67 dBm, modulated interferer at ±2 MHz		-35 / -28 <sup>(2)</sup>			
Selectivity, ±6 MHz <sup>(1)</sup>	Wanted signal at -67 dBm, modulated interferer at ±3 MHz		-35 / -28 <sup>(2)</sup>		- dB	
Alternate channel rejection,	-					
±8 MHz <sup>(1)</sup>	Wanted signal at -67 dBm, modulated interferer at ±8MHz		-37 / -32 <sup>(2)</sup>			
Out-of-band blocking	30 MHz to 2000 MHz, wanted signal at -67 dBm		-23			
Out-of-band blocking	2003 MHz to 2399 MHz, wanted signal at -67 dBm		-30			
Out-of-band blocking	, ,					
Out-of-band blocking	3000 MHz to 6 GHz, wanted signal at -67 dBm		-21		— dBm	
Intermodulation	Wanted signal at 2402 MHz, -64 dBm, two interferers at 2405 and 2408 MHz respectively, at the given power level		-44			
RSSI accuracy	-90 dBm to -20 dBm	-4		4	dB	
nooi decuidey	-30 UBIII 10 -20 UBIII	-4	i	4	ub	

#### Note:

<sup>(1)</sup> Numbers given as C/I dB; (2) X / Y, where X is +N MHz and Y is -N M;



### 3.2.4. BLE Performance: Transmitter Characteristics

**Table 20. BLE Performance: Transmitter Characteristics** 

Parameter	Test Condition	MIN	TYP Peak Power	TYP Average Power	MAX	Unit
Operational frequency range		2402	20		2480	MHz
Output power	Highest setting		20			dBm

#### Note:

(1) The output power is measured at frequency 2440MHz.

## 4. Mechanical Specifications

### 4.1. Module Dimensions

The module dimensions ae shown in following figures:

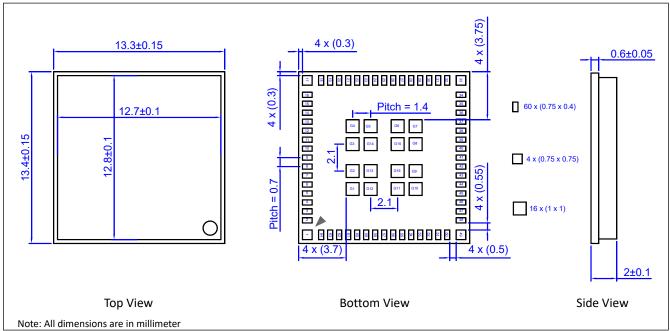


Figure 13. Mechanical Drawing of BDE-BW330XNP1 and BDE-BW330XNP2



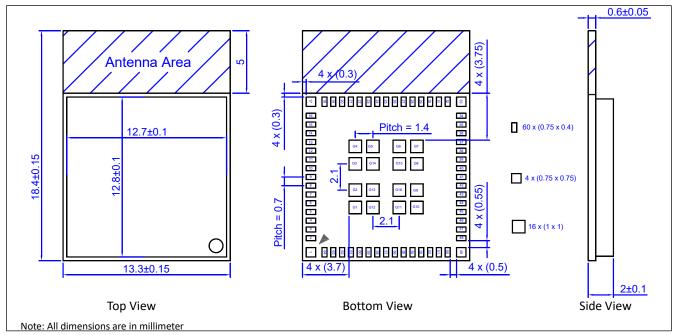
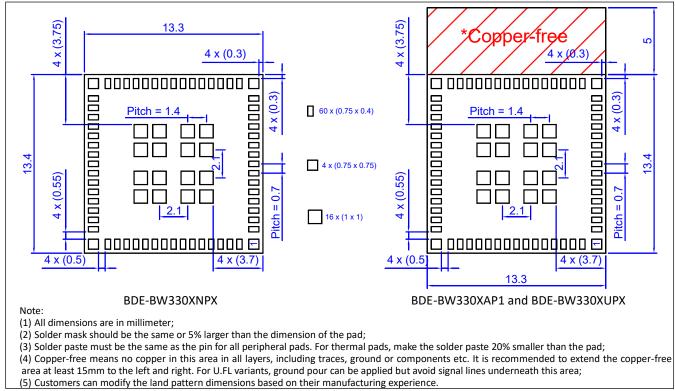


Figure 14. Mechanical Drawing of BDE-BW330XAP1, BDE-BW330XUP1 and BDE-BW330XUP2

### **4.2. PCB Footprints**

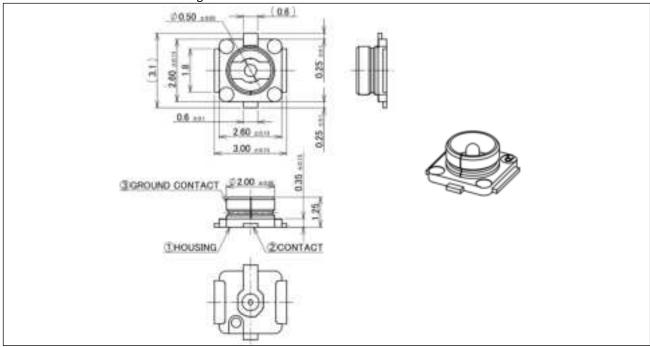


**Figure 15. Recommended Footprint Drawings** 

### 4.3. U.FL Connector Specification

The drawing and specification of the U.FL connector utilized in the module is as below for reference.

The dimension unit in below drawing is millimeter.



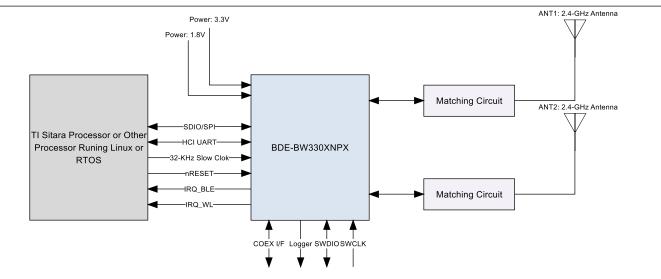
INMATING FORCE (INITIAL / AFTER TEST)	INITIAL: 5 N MIN. INITIAL: 4 N MIN. AFTER TEST: 2 N MIN. AFTER TEST: 2 N MIN.		
DURABILITY	30 CYCLES		
DELECTRIC WITHSTANDING VOLTAGE	200 V AC, 1 MINUTE	200 V AC, 1 MINUTE	
NSULATION RESISTANCE	INITIAL: 500 Mohm MIN. / AFTER TEST: 100 Mohm MIN	N.	
GROUND CONTACT RESISTANCE	INITIAL: 20 mohm MAX. / AFTER TEST: ⊴R 100 mohm MAX.		
MAIN CONTACT RESISTANCE	INITIAL: 20 mohm MAX. / AFTER TEST: ∠R 20 mohm MAX.		
/SWR	RECEPTAGLE: 1.3 MAX. AT 0.1~3 GHz, 1.4 MAX. AT 3~6 GHz, 1.8 M	AX. AT 6~9 GHz	
OPERATING TEMPERATURE	233~363K (-40°C~+90°C)		
RATING FREQUENCY	DC~9GHz		
RATING VOLTAGE	60 V AC (R.M.S)		

Figure 16. U.FL Connector Drawing and Specification



## 5. Integration Guideline

#### 5.1. System Diagram



#### Note:

- (1) Pin IRQ\_BLE, IRQ\_WL and Logger are assigned as the SoP mode pins, the default SoP mode is "001", which is IRQ\_BLE pin being pulled down, IRQ\_WL pin being pulled down and Logger pin being pulled up by default;
- (2) SWDIO and SWCLK is for RF testing using TI's SimpleLink Wi-Fi ToolBox, recommended to be exposed to the host board;
- (3) ANT2 is only for antenna diversity variants;
- (4) COEX I/F is the coexistence interface. Contact BDE for software support;
- (5) The two antennas ANT1 and ANT2 should be physically placed apart, at least one quarter to one wavelength which is 31.25mm to 125mm, and they should be oriented at least 45 degree to 145 degree to each other;
- (6) Contact BDE for antenna diversity software support;
- (7) For BDE-BW330XAP1 which is with integrated PCB trace antenna, the external antenna is not needed.

Figure 17. High-Level System Block Diagram

#### 5.2. Module Placement

The placement of the module in the base board is critical in your design. Improper placement can lead to poor antenna performance. BDE recommend following below practical placement to achieve acceptable antenna performance.

Any form of proximity to the metal or other material will change/degrade the antenna performance. Keep the antenna area as far as possible to the metal material in any direction. If metal materials cannot be avoided in your design for example the design with metal enclosure, we recommended keep the antenna area at least 40mm distance to the enclosure in all directions. Customers should verify the communication range with the mock-up or real product prototype on their own.



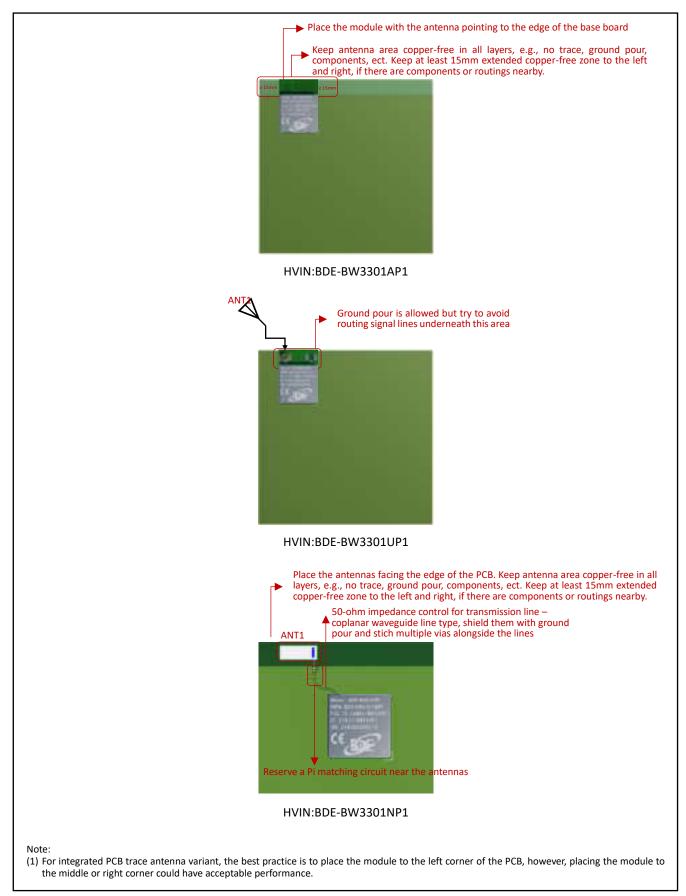


Figure 18. Recommended Module Placement



### 5.3. Reference Design

For reference schematic and layout, please refer to the design files of BDE-33PN-EM and BDE-33PA-EM.

### 5.4. Other Design Considerations

**Table 21. Other Design Considerations** 

	Table 21. Other Design Considerations
	Thermal
1	The proximity of ground vias must be close to each ground pad of the module.
2	Signal traces must not be run underneath the module on the layer where the module is mounted.
3	Have a complete ground pour in layer 2 for thermal dissipation.
4	Have a solid ground plane and ground vias under the module for stable system and thermal dissipation.
5	Increase the ground pour in the first layer and have all of the traces from the first layer on the inner layers, if possible.
6	Signal traces can be run on a third layer under the solid ground layer, which is below the module mounting layer.
	RF Trace and Antenna Routing
7	The RF trace antenna feed must be as short as possible beyond the ground reference. At this point, the trace starts to radiate.
8	The RF trace bends must be gradual with an approximate maximum bend of 45° with trace mitered. RF traces must not have
0	sharp corners.
9	RF traces must have via stitching on the ground plane beside the RF trace on both sides.
10	RF traces must have constant impedance (50-ohm Coplanar or microstrip transmission line).
11	For best results, the RF trace ground layer must be the ground layer immediately below the RF trace. The ground layer must
	be solid.
12	There must be no traces or ground under the antenna section.
13	RF traces must be as short as possible. The antenna, RF traces, and modules must be on the edge of the PCB product. The
15	proximity of the antenna to the enclosure and the enclosure material must also be considered.
14	BDE recommends using double-shielded coaxial RF cable to connect with the U.FL connector with antenna if the U.FL variants
	are selected.
15	Do not place or run the RF cable right above or below the module.
16	If there are some other radios besides this module in the system, try to place them apart as far as possible. And ensure
10	there is at least 25 dB isolation between the antenna port of every radio.
	Supply and Interface
17	The power trace for VDD 3V3 must be at least 40-mil wide.
18	The VDD 1V8 trace must be at least 18-mil wide.
19	Make VDD 3V3 and VDD 1V8 traces as wide as possible to ensure reduced inductance and trace resistance.
20	If possible, shield 3V3 and 1V8 traces with ground above, below, and beside the traces.
	SDIO signals traces (CLK, CMD, D0, 01, 02, and 03) must be routed in parallel to each other and as short as possible (less
	than 12 cm). In addition, every trace length must be the same as the others. There should be enough space between traces-
21	greater than 1.5 times the trace width or ground-to ensure signal quality, especially for the SDIO_CLK trace. Remember to
	keep these traces away from the other digital or analog signal traces. It is recommended adding ground shielding around
	these buses.
22	SDIO and digital clock signals are a source of noise. Keep the traces of these signals as short as possible. If possible, maintain
~~	a clearance around them.

## 6. Handling Instructions

The module is the surface mount module with LGA footprint. It is designed to conform to the major manufacturing guidelines, including the commercial, industrial manufacturing process.

In this section, we will cover the basic shipping information, including the module markings, packaging, labeling, ect. And also, the instructions on how to handle the module in terms of storage, assembly and so on.

### 6.1. Module Marking

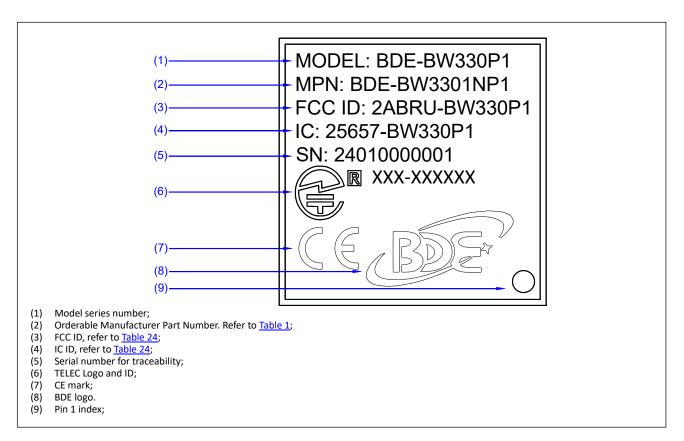


Figure 19. Module Marking

### 6.2. Packaging Information

## 6.2.1. Tape and Reel Package Information

Anti-static coefficient of  $10^{6-11}\Omega$ .

(9)



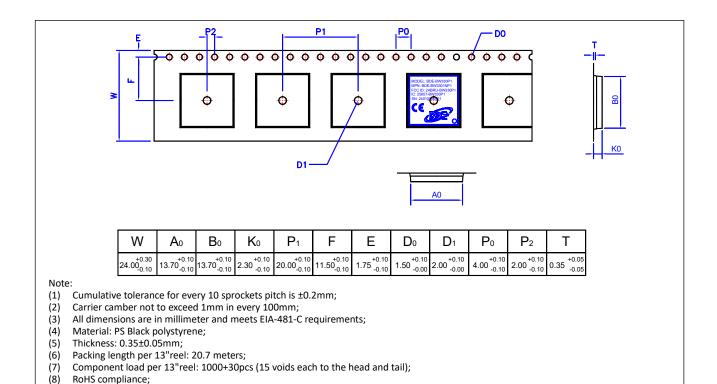


Figure 20. Carrier Tape Drawing for BDE-BW330XNPX variants

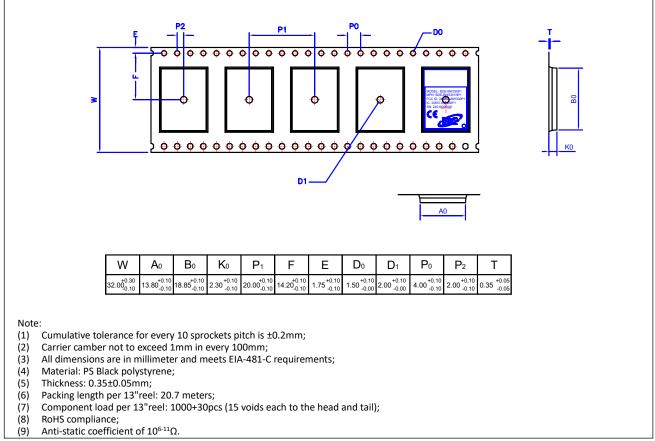


Figure 21. Carrier Tape Drawing for BDE-BW330XUPX and BDE-BW330XAP1 variants



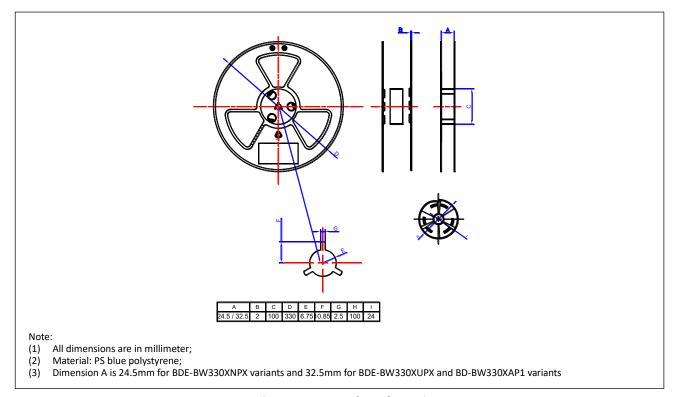


Figure 22. 13-INnch Reel Drawing

### 6.2.2. Carton Information and Labeling

TBD

### 6.3. Assembly Instruction

### 6.3.1. Moisture Sensitive Level

The MSL (Moisture Sensitive Level) of the module is MSL-3. Handling guidelines are listed as below:

- (1) The floor life for MSL-3 device is 168 hours in ambient environment 30°C/60%RH. Before assembly, make sure to check if the modules are packaged with desiccate and humidity indicator card;
- (2) After the bag is opened, make sure to mount the modules within 168 hours at factory conditions (< 30°C/60% RH) or stored at <10% RH. Repackage is needed with new desiccate and humidity indicator card if the modules are not mounted before exceeding floor life;
- (3) If the card reads >10%, or the modules have been exposed for over 168 hours, the modules need to be baked before mounted. Recommended baking condition is 125°C for 8 hours.

### 6.3.2. Reflow Profile



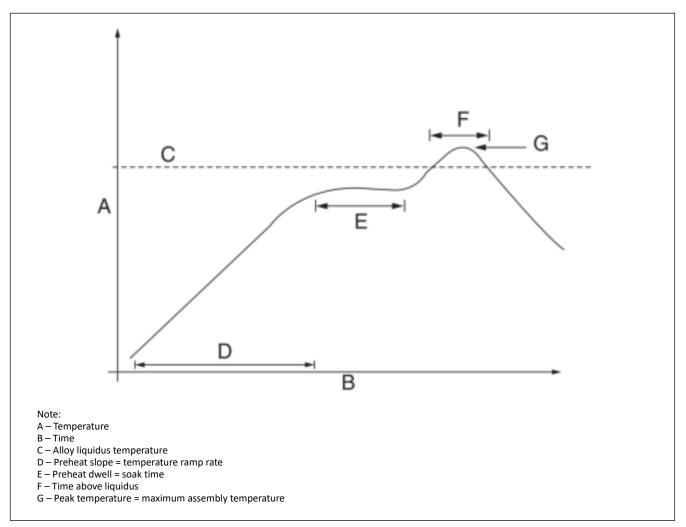


Figure 23. Thermal Profile Schematic

Table 22. Reflow Profile Parameters (1) (3)

Item	Temperature Range	Ramp Rate / Time
D, preheat zone	30°C ~ 175°C	2°C ~ 4°C per second
E, soak zone	150°C ~ 200°C	60 ~ 120 seconds
C, Alloy liquidus temperature	217°C ~ 220°C	-
F, reflow zone	230°C ~ 245°C	60 ~ 90 seconds
G, target maximum reflow temperature	250°C	-
Absolute peak temperature (2)	260°C	-

#### Note:

- (1) This is for Pb-free (SAC 305) paste. Different pastes require different profiles for optimum performance, so it is important to consult the paste manufacturer before developing the solder profile;
- (2) Exceed the absolute peak temperature for certain period, e.g. 20s might damage the device or affect the reliability;
- (3) It is recommended that the modules do not go through the reflow process more than one time.

### 6.3.3. Other Consideration

(1) Ultrasonic cleaning process is discouraged for the modules as the process might damage the module permanently, especially for the crystal oscillator in the module.



(2) Conformal coating is not allowed to this module. It will impact the reliability of the module once the coating flooded into the shield.

### 7. Certification

#### 7.1. Bluetooth Qualification

#### 7.1.1. Bluetooth Qualification Information

The module series is listed on the Bluetooth SIG website as a qualified End Product, referencing a Controller and Host Subsystem combination. The detail information can be found in below table.

**Table 23. Bluetooth Qualification Information** 

Declaration ID	Reference QDID		
D067225	Controller Subsystem	229129	
D067335	Host Subsystem	TBD	

#### 7.1.2. Bluetooth Qualification Process

Below Bluetooth qualification process is provided for customers when they are listing their end product referencing BDE module.

- (1) Go to <a href="https://launchstudio.bluetooth.com/">https://launchstudio.bluetooth.com/</a> and log in;
- (2) Select Start the Bluetooth Qualification Process with No Required Testing;
- (3) Project Basics:
  - (a) Enter your project name, it can be the product name or the product series name;
  - Enter QDID that the product reference, in this case the QDID is 229129 for controller subsystem and .TBD for host subsystem.
- (4) Product Declaration:
  - (a) Select the listing date. You can select a date that you want your product listed and go public, although the qualification will complete immediately after your submission.
  - Add every product that integrated with this module. You can add a series of individual product models that use the same design/module without any modification.
- (5) Declaration ID:
  - (a) Select a DID. If you don't have one, you need to purchase a DID for your product by clicking Pay Declaration Fee.
- (6) Review and Submit:
  - (a) Review all information that you have entered and make sure no mistakes;
  - (b) Tick all check boxes if you confirmed above information and add your name to the signature page;
  - (c) Click Signature Confirmed Complete Project & Submit Product(s) for Qualification.
- (7) The qualification will be done immediately and your product will be listed to the Bluetooth SIG website as per your required listed date in step (4).

For more information about listing your product to Bluetooth SIG, please visit below webpage: https://www.bluetooth.com/develop-with-bluetooth/qualification-listing/

### 7.2. Regulatory Compliance

The module is certified for FCC, IC/ISED and ETSI/CE as listed in below table. More regions can be cover by request.

**Table 24. Certification Information** 

Regulatory Body / Region	ID	MPN
FCC (USA)	2ABRU-BW330P1	BDE-BW3301NP1 BDE-BW3301UP1
IC/ISED (Canada)	25657-BW330P1	BDE-BW3301AP1

Datashee

Regulatory Body / Region	ID	MPN
TELEC (Japan)	XXX-XXXXXX	BDE-BW3300NP1 BDE-BW3300UP1
ETSI/CE (Europe)	NA	BDE-BW3300AP1 BDE-BW3301NP1-IN BDE-BW3301UP1-IN BDE-BW3301AP1-IN BDE-BW3300NP1-IN BDE-BW3300UP1-IN BDE-BW3300AP1-IN

#### 7.2.1. Certified Antennas

The module series has been tested and certified with three antennas, where BDE-BW330XAP1 variants utilize an integrated PCB trace antenna and BDE-BW330XUPX variants utilize an external whip antenna through U.FL connector and BDE-BW330XNPX utilize a ceramic chip antenna utilized in the EM board through the dedicated ANT pin of the module.

The characteristic of the three antennas is listed in below table.

Table 25. Certified Antenna List

Manufacturer	Manufacturer Part Number	Antenna Type	Frequency Range (MHz)	Gain (dBi)	Note
BDE	BDE-ANT-BW33PA	PCB trace antenna	2400 - 2450	-2.2	Integrated
BDE	BDE-W25-17010-HRP	Whip antenna	2400 - 2500	2.7	External
BDE	BDE-FPC25-4017-120F1	FPC antenna	2400 - 2500	1.5	External
Laird	001-0015	FPC antenna	2400 - 2500	2.0	External
Ethertronics	M830520	Ceramic chip antenna	2400 - 2500	1.0	External

Customers are encouraged to use the certified antennas in the case of external antenna options to reduce certification testing effort and risk of failing. If customer want to choose another antenna that fits their product, there are some scenarios that need to be considered.

If the external antenna is of the same antenna type and of equal or less gain compared to the ones listed in above table, and with similar in-band and out-of-band characteristic, then the antenna can be used with the module in USA and Canada where modular approval is applicable, as long as the spot-check testing of the new antenna with host is performed to verified that it will not change the performance. However, in countries such as EU countries applying the ETSI standards where the modular approval is not applicable, the radiated emissions are always tested with the end product with any antennas.

If the external antenna is of a different type or with non-similar in-band and out-of-band characteristic, but still has equal gain or less gain compared to the above listed antennas. The new antenna can be added to the existing modular grant/certificate by filing a permissive change, C2PC (Class II Permissive Change) in case of FCC and ISED. The radiated emission testing is needed, but re-certification is not required.

In the case of the external antenna with higher gain than the peak gain listed in above table are very likely to require a full new end product certification. However, we recommended that you consult with your certification house to understand the correct approaches for your product case by case.

For the case where customer choose the certified antenna with BDE-BW330XNPX through the dedicated ANT pin of the module, the customer must copy the design exactly as the one that tested in the certification to comply with the requirement.

#### 7.2.2. FCC Caution

This device complies with part 15 of the FCC rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursua nt to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates uses and can radiate radio frequency energy a nd, if not installed and used in accordance with the instructions, may cause harmful interference to radio com munications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turn ing the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help important announcement Important Note:

#### **Radiation Exposure Statement**

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20cm between the radiator and your body.

This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter. Country Code selection feature to be disabled for products marketed to the US/Canada.

This device is intended only for OEM integrators under the following conditions:

- 1.The antenna must be installed such that 20 cm is maintained between the antenna and users, and
- 2. The transmitter module may not be co-located with any other transmitter or antenna,

3.For all products market in US, OEM has to limit the operation channels in CH1 to CH11 for 2.4G band by supplied firmware programming tool. OEM shall not supply any tool or info to the end-user regarding to Regulatory Domain change. (if modular only test Channel 1-11)

As long as the three conditions above are met, further transmitter testing will not be required. However, the OEM integrator is still responsible for testing their end-product for any additional compliance requirements required with this module installed.

#### **Important Note:**

In the event that these conditions cannot be met (for example certain laptop configurations or co-location with another transmitter), then the FCC authorization is no longer considered valid and the FCC ID cannot be used on the final product. In these circumstances, the OEM integrator will be responsible for re-evaluating the end product (including the transmitter) and obtaining a separate FCC authorization.

#### **End Product Labeling**

The final end product must be labeled in a visible area with the following" Contains FCC ID: 2ABRU-BW330P1 "

#### **Manual Information to the End User**

The OEM integrator has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module.

The end user manual shall include all required regulatory information/warning as show in this manual.

Integration instructions for host product manufacturers according to KDB 996369 D03 OEM Manual v01r01

### 2.2List of applicable FCC rules

CFR 47 FCC PART 15 SUBPART C has been investigated. It is applicable to the modular transmitter

#### 2.3Specific operational use conditions

This module is stand-alone modular. If the end product will involve the Multiple simultaneously transmitting condition or different operational conditions for a stand-alone modular transmitter in a host, host manufacturer have to consult with module manufacturer for the installation method in end system.

#### 2.4Limited module procedures

Not applicable

#### 2.5Trace antenna designs

Not applicable

#### 2.6RF exposure considerations

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20cm between the radiator & your body.

#### 2.7 Antennas

This radio transmitter **FCC ID:2ABRU-BW330P1** has been approved by Federal Communications Commission to operate with the antenna types listed below, with the maximum permissible gain indicated. Antenna types not included in this list that have a gain greater than the maximum gain indicated for any type listed are strictly prohibited for use with this device.

Antenna No.	Model No. of antenna:	Type of antenna:	Gain of the antenna (Max.)	Frequency range(MHz)
PCB antenna	BDE-ANT-BW33PA	External	2.7dBi	2400 - 2450
Whip antenna	BDE-W25-17010- HRP	Internal	-2.2dBi	2400 - 2450
FPC antenna2	BDE-FPC25-4017- 120F1	Internal	1.5dBi	2400 - 2450
FPC antenna1	001-0015	Internal	2.0dBi	2400 - 2450
Ceramic chip antenna	M830520	Internal	1.0dBi	2400 - 2450

#### 2.8 Label and compliance information

The final end product must be labeled in a visible area with the following" Contains FCC ID:2ABRU-BW330P1".

#### 2.9 Information on test modes and additional testing requirements

Host manufacturer is strongly recommended to confirm compliance with FCC requirements for the transmitter when the module is installed in the host.

#### 2.10 Additional testing, Part 15 Subpart B disclaimer

Host manufacturer is responsible for compliance of the host system with module installed with all other applicable requirements for the system such as Part 15 B.

#### 2.11 Note EMI Considerations

Host manufacture is recommended to use D04 Module Integration Guide recommending as "best practice" RF design engineering testing and evaluation in case non-linear interactions generate additional non-compliant limits due to module placement to host components or properties.

#### 2.12 How to make changes

This module is stand-alone modular. If the end product will involve the Multiple simultaneously transmitting condition or different operational conditions for a stand-alone modular transmitter in a host, host manufacturer have to consult with module manufacturer for the installation method in end system. According to the KDB 996369 D02 Q&A Q12, that a host manufacture only needs to do an evaluation (i.e., no C2PC required when no emission exceeds the limit of any individual device (including unintentional radiators) as a composite. The host manufacturer must fix any failure.

#### 7.2.3. ISED Statement

-English: This device complies with Industry Canada license - exempt RSS standard(s). Operation is subject to the following two conditions: (1) This device may not cause interference, a nd (2) This device must accept any interference, including interference that may cause undesired operation of the device.

The digital apparatus complies with Canadian CAN ICES - 3 (B)/NMB - 3(B).

- French: Le présentappareilestconforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitationestautorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareildoit accepter tout brouillageradi oélectriquesubi, mêmesi le brouillageest susceptible d'encompromettre le fonctionnement.

l'appareil numérique du ciem conforme canadien peut - 3 (b) / nmb - 3 (b).

This device meets the exemption from the routine evaluation limits in section 6.3 of RSS 102 and compliance with RSS 102 RF exposure, users can obtain Canadian information on RF exposure and compliance.

cet appareil est conforme à l'exemption des limites d'évaluation courante dans la section 6.3 du cnr - 102 et conformité avec rss 102 de l'exposition aux rf, les utilisateurs peuvent obtenir des données canadiennes sur l'exposition aux champs rf et la conformité.

This equipment complies with Canada radiation exposure limits set forth for an uncontrolled environment.

Cet équipement est conforme Canada limites d'exposition aux radiations dans un environnement non contrôlé.

This equipment should be installed and operated with minimum distance 20cm between the radiator & your body. Cet équipementdoit être installé et utilisé à une distance minimale de 20 cm entre le radiateur et votre corps.

#### ISED Modular Usage Statement

NOTE 1: When the ISED certification number is not visible when the module is installed inside another device, then the outside of the device into which the module is installed must also display a label referring to the enclosed module. This exterior label can use the

wording "Contains transmitter module IC: 25657-BW330P1" or "Contains IC: 25657-BW330P1".

NOTE 1: Lorsque le numéro de certification ISED n'est pas visible lorsque le module est installé dans un autre appareil, l'ext érieur de l'appareil dans lequel le module est installé doit également afficher une étiquette faisant référence au module inclus. Cette étiquetteextérieure peut être libellée Contient le module émetteur IC: 25657-BW330P1 ou Contient IC: 25657-BW330P1.



# 8. Ordering Information

**Table 26. Ordering Information** 

Table 26. Ordering Information					
Orderable Part Number	Description	Size (mm)	Core Chip	Shipping Form	MOQ
BDE-BW3301NP1	Wi-Fi 6 2.4-GHz SISO & BLE 5.4, Single Antenna Port with ANT Pin, -40 °C to +85 °C	13.4 x 13.3 x 2	CC3301	Tape & Reel	1K
BDE-BW3301UP1	Wi-Fi 6 2.4-GHz SISO & BLE 5.4, Single Antenna Port with U.FL Connector, -40 °C to +85 °C	18.4 x 13.3 x 2	CC3301	Tape & Reel	1K
BDE-BW3301AP1	Wi-Fi 6 2.4-GHz SISO & BLE 5.4, Single Antenna Port with Integrated PCB Trace Antenna, -40 °C to +85 °C	18.4 x 13.3 x 2	CC3301	Tape & Reel	1K
BDE-BW3300NP1	Wi-Fi 6 2.4-GHz SISO & BLE 5.4, Single Antenna Port with ANT Pin, -40 °C to +85 °C	13.4 x 13.3 x 2	CC3300	Tape & Reel	1K
BDE-BW3300UP1	Wi-Fi 6 2.4-GHz SISO & BLE 5.4, Single Antenna Port with U.FL Connector, -40 °C to +85 °C	18.4 x 13.3 x 2	CC3300	Tape & Reel	1K
BDE-BW3300AP1	Wi-Fi 6 2.4-GHz SISO & BLE 5.4, Single Antenna Port with Integrated PCB Trace Antenna, -40 °C to +85 °C	18.4 x 13.3 x 2	CC3300	Tape & Reel	1K
BDE-BW3301NP1-IN	Wi-Fi 6 2.4-GHz SISO & BLE 5.4, Single Antenna Port with ANT Pin, -40 °C to +105 °C	13.4 x 13.3 x 2	CC3301	Tape & Reel	1K
BDE-BW3301UP1-IN	Wi-Fi 6 2.4-GHz SISO & BLE 5.4, Single Antenna Port with U.FL Connector, -40 °C to +105 °C	18.4 x 13.3 x 2	CC3301	Tape & Reel	1K
BDE-BW3301AP1-IN	Wi-Fi 6 2.4-GHz SISO & BLE 5.4, Single Antenna Port with Integrated PCB Trace Antenna, -40 °C to +105 °C	18.4 x 13.3 x 2	CC3301	Tape & Reel	1K
BDE-BW3300NP1-IN	Wi-Fi 6 2.4-GHz SISO & BLE 5.4, Single Antenna Port with ANT Pin, -40 °C to +105 °C	13.4 x 13.3 x 2	CC3300	Tape & Reel	1K
BDE-BW3300UP1-IN	Wi-Fi 6 2.4-GHz SISO & BLE 5.4, Single Antenna Port with U.FL Connector, -40 °C to +105 °C	18.4 x 13.3 x 2	CC3300	Tape & Reel	1K
BDE-BW3300AP1-IN	Wi-Fi 6 2.4-GHz SISO & BLE 5.4, Single Antenna Port with Integrated PCB Trace Antenna, -40 °C to +105 °C	18.4 x 13.3 x 2	CC3300	Tape & Reel	1K

## 9. Revision History

**Table 27. Ordering Information** 

Revision	Date	Description		
V0.1	16-Dec-2022	Preliminary, draft		
V0.2	13-Feb-2023	Updated pinout, added reference design		
V0.3	29-Mar-2023	Added more information		
V0.4	14-Jul-2023	Corrected some editorial mistakes, updated reference design		
V0.5	30-Jan-2024	Added detailed information		
V0.6	20-Mar-2024	Updated some data, corrected some mistakes		

#### Note:

The latest datasheet can be found with this <u>Link</u>.

## **Important Notice and Disclaimer**

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