

Datasheet

F1 Smart Module

August 2024 V1.3





Introduction

The F1 Smart Module (order part number SGW3501) is a compact OEM module equipped with BLE, Wi-Fi, LoRa(WAN), and LTE CAT-M1/NB1/NB2 to support various connectivity needs. Running on a MicroPython-programmable microcontroller with a no-barrier entry into the SG Wireless Ctrl. Cloud Platform, the module enables truly limitless IoT application development with multi-network creation flexibility and rapid scaling capacity.

The F1 Smart Module has four variants with various connectivity options, under each variant, there are two subvariants with advanced security element option.

- Multi-connectivity:
 - Wi-Fi 802.11b/g/n (2.4GHz)
 - Bluetooth BLE 5.0
 - Cellular LTE-CAT M1/NB1/NB2
 - Semtech LoRa(WAN) 868MHz/915MHz
- Powerful Espressif ESP32 S3 CPU
- MicroPython programmable with 27 IOs on module pads
- SMT-friendly semi-hole pins at module edges
- Operating temperature: 0°C to 85°C
- Advanced Security IC NXP SE050 (for "s" suffix models)
- Compact size-to-functionality ratio: 42.6mm x 17.6mm x 3.6mm

Order Part Number	Description
SGW3531 F1s Smart Module: BLE, Wi-Fi, LoRa, LTE & Secure element	
SGW3501 F1 Smart Module: BLE, Wi-Fi, LoRa, LTE	
SGW3431 F1/Cs Cellular Module: BLE, Wi-Fi, LTE & Secure element	
SGW3401 F1/C LoRa Module: BLE, Wi-Fi, LTE	
SGW3231	F1/Ls LoRa Module: BLE, Wi-Fi, LoRa & Secure element
SGW3201	F1/L LoRa Module: BLE, Wi-Fi, LoRa
SGW3131 F1/Ws Wi-Fi BLE Module: BLE, Wi-Fi & Secure element	
SGW3101	F1/W Wi-Fi BLE Module: BLE, Wi-Fi

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1. General Features

a. Feature Specifications

CPU
Xtensa [®] dual-core 32-bit LX7 microprocessor, up to 240Mhz
 On-chip 384KB ROM and 512KB SRAM, on-board 8MB PSRAM and 16MB Flash
• Deep Sleep Mode: 10µA
Wi-Fi/BLE
Espressif ESP32-S3 on-chip RF frontend
 Wi-Fi: IEEE 802.11b/g/n (2.4GHz band); Data Rate: 1M up to 54Mbps (MCS7); Max Tx Power: 20dBm
BLE: Bluetooth LE 5.0, Bluetooth mesh; Data Rate: 125kbps to 2Mbps; Max Tx Power: 20dBm
LTE
 Sequans Monarch2 GM02S for CAT-M1, CAT-NB1 and CAT-NB2 support
LTE CAT-M1/NB1/NB2 transmit power up to +23dBm
PTCRB and GCF 1.3 3GPP release 13 compliant; Operator Approval: Verizon, AT&T, T-Mobile, Vodafone, Orange
LoRa
Semtech SX1262 RF transceiver, 868/915MHz LPWAN Module
TX Power: Up to +22dBm; Sensitivity: -127dBm

• LoRaWAN stack – Class A and Class C Device

b. Block Diagram

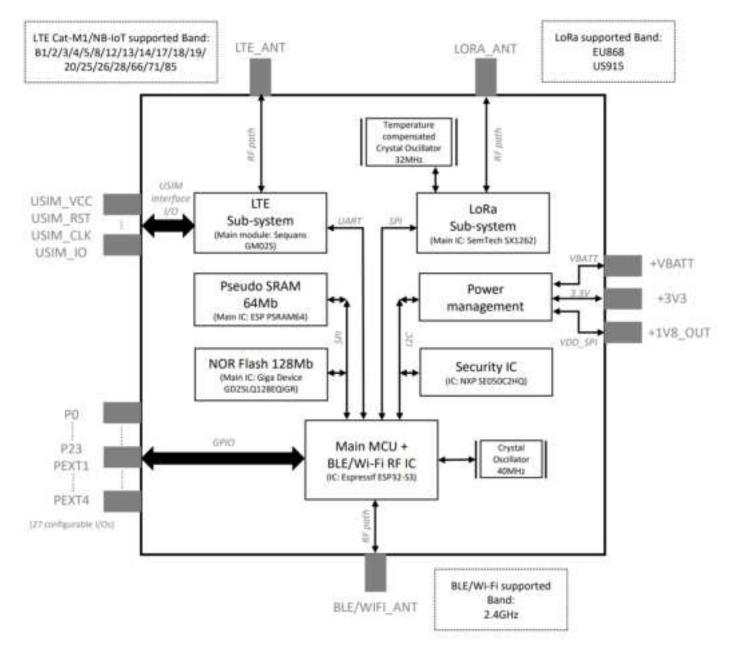


Figure 1: F1 Smart Module Block Diagram

2. Electrical Specifications

a. Absolute Rating and Operating Conditions

Table 1: Absolute Rating and Operating Conditions Specifications

Symbol	Parameter	Min	Тур	Мах	Unit
Absolute Ratir	ng				
+VBATT	Supply voltage to Sequans GM02S LTE module		5.0	5.8	V
+3V3	Supply voltage to Espressif ESP32-S3 and module main circuit	3.0	3.3	3.6	V
+1V8_OUT*	SPI supply voltage (output) of SPI flash and PSRAM for decoupling capacitor connection		1.8	2.3	V
T(OPR)	Operating temperature	-40		85	°C
Operating Con	iditions				
+VBATT	Supply voltage to Sequans GM02S LTE module	2.5	5.0	5.5	V
+3V3	Supply voltage to Espressif ESP32-S3 and module main circuit	3.2	3.3	3.4	V
+1V8_OUT*	SPI supply voltage (output) of SPI flash and PSRAM for decoupling capacitor connection		1.8	1.9	V
CPU IO (3.3V p	ower domain, VDD=3.3V)				
VIH	Input high voltage for GPIOs	0.75 x VDD		VDD + 0.3	V
VIL	Input low voltage for GPIOs	-0.3		0.25 x VDD	V
VOH	Input high voltage for GPIOs	0.8 x VDD			V
VOL	Input low voltage for GPIOs			0.1 x VDD	V
Radio IO (1.8V p	power domain)	-	,		
VIH	Input high voltage for GPIOs	1.26		1.8	V
VIL	Input low voltage for GPIOs	0		0.54	V
VOH	OH Input high voltage for GPIOs			1.8	V
VOL	Input low voltage for GPIOs	0		0.36	V

* The +1V8_OUT pin is to connect an external capacitor to the module internal SPI flash and PSRAM for a more robust VDD_SPI supply. This pin should not be connected to any external circuits that may draw more than 20mA. Voltage of this pin will vary in module light sleep mode and approach zero in module deep sleep mode.

b. Wi-Fi

Standard: 802.11b/g/n (2.4GHz ONLY) – 1T1R

Table 2: Wi-Fi Specifications

Parameter	Description	Min	Тур	Max	Unit
General					
Freq. (EU)	Operating frequency (EU)	2.402		2.482	GHz
Ch. (EU)	Channel (EU)	1		13	
Freq. (US)	Operating frequency (US)	2.402		2.472	GHz
Ch. (US)	Channel (US)	1		11	
Power max. (EU/US)	Maximum power (EU/US)			20	dBm
Тх					
Tx Power @B – 1Mbps	Tx power at B mode with data rate 1Mbps		18	20	dBm
EVM (Peak) @B – 1Mbps	EVM(Peak) at B mode with data rate 1Mbps			8	%
Freq. Err. @B – 1Mbps	Frequency error at B mode with data rate 1Mbps	-40	0	40	kHz
Tx Power @G – 54Mbps	here a service of the		16	20	dBm
EVM (RMS) @G – 54Mbps	EVM(RMS) at G mode with data rate 54Mbps			-25	dB
Freq. Err. @G – 54Mbps	Frequency error at G mode with data rate 54Mbps	-40	0	40	kHz
Tx Power @N20 – MCS7	Tx power at N mode with data rate MCS7 and 20MHz bandwidth		15	20	dBm
EVM (RMS) @N20 – MCS7	EVM rms at N mode with data rate MCS7 and 20MHz bandwidth			-27	dB
Freq. Err. @N20 – MCS7	Frequency error @ N mode with data rate MCS7 and 20MHz bandwidth	-40	0	40	kHz
Tx Power @B – 1Mbps	Tx power at B mode with data rate 1Mbps		18	20	dBm
Rx					
Rx Sens. @B – 1Mbps	Tx power at B mode with data rate 1Mbps		-92.0	-82.0	dBm
Rx Sens. @G – 54Mbps	Tx power at G mode with data rate 54Mbps		-76.5	-66.0	dBm
Rx Sens. @N20 – MCS7	Tx power at N mode with data rate MCS7 and 20MHz bandwidth		-71.4	-64.0	dBm

c. Bluetooth

Standard: BLE 5.0 – 1T1R

Table 3: Bluetooth Specifications

Parameter	Description	Min	Тур	Мах	Unit
General					
Freq.	Operating frequency	2.4000		2.4835	GHz
Ch.	Channel	0		39	
Power max.	Maximum power			20	dBm
Тх					
Tx Power @Ch.37 – 1 Mbps			17	20	dBm
Freq. Err. @Ch.37 – 1Mbps	Frequency error at channel 37 (freq.=2402MHz) with data rate 1Mbps	-50	0	50	%
Tx Power @Ch.38 – 1Mbps			17	20	kHz
Freq. Err. @Ch.38 – 1Mbps			0	50	dBm
Tx Power @Ch.39 – 1Mbps	Tx power at channel 39 (freq.=2480MHz) with data rate 1Mbps		17	20	dBm
Freq. Err. @Ch.39 – 1Mbps	Frequency error at channel 39 (freq.=2480MHz) with data rate 1Mbps	-50	0	50	kHz
Rx			• •		•
Rx Sens. @Ch.38 – 2Mbps	Tx power at channel 38 (freq.=2426MHz) with data rate 2Mbps		-93.5		dBm
Rx Sens. @Ch.38 – 1Mbps			-97.5	-70.0	dBm
Rx Sens. @Ch.38 – 500kbps	Tx power at channel 38 (freq.=2426MHz) with data rate 500kbps		-100.0		dBm

d. LTE

Standard: CAT-M1, CAT-NB1, CAT-NB2

Table 4: LTE Frequency Bands (in MHz)

Band No.	Duplex Type	Uplink Frequency (MHz)	Uplink Bandwidth (MHz)	Downlink Frequency (MHz)	Downlink Bandwidth (MHz)	For LTE-M	For NB-loT
1	FDD	1920 – 1980	60	2110 – 2170	60	~	~
2	FDD	1850 – 1910	60	1930 – 1990	60	~	~
3	FDD	1710 – 1785	75	1805 – 1880	75	~	~
4	FDD	1710 – 1755	45	2110 – 2155	45	~	~
5	FDD	824 - 849	25	869 - 894	25	~	~
8	FDD	880 – 915	35	925 – 960	35	~	~
12	FDD	699 – 716	17	729 – 746	17	~	~
13	FDD	777 – 787	10	746 – 756	10	~	~
14	FDD	788 – 798	10	758 – 768	10	~	~
17	FDD	704 – 716	12	734 – 746	12	×	~
18	FDD	815 – 830	15	860 - 875	15	~	~
19	FDD	830 - 845	15	875 – 890	15	~	~
20	FDD	832 - 862	30	791 – 821	30	~	~
25	FDD	1850 – 1915	65	1930 – 1995	65	~	~
26	FDD	814 - 849	35	859 - 894	35	~	~
28	FDD	703 – 748	45	758 – 803	45	~	~
66	FDD	1710 – 1780	70	2110 – 2200	90	~	~
85	FDD	698 – 716	18	728 – 746	18	~	~

Table 5: LTE Specifications

Parameter	Description	Min	Тур	Мах	Unit	
General						
Power max.	max. Maximum power			23	dBm	
Тх						
Tx power @Band 8 (900MHz GSM)	Tx power at Band 8 (900MHz GSM)		22	23	dBm	
Tx power @Band 2 (1900MHz PCS)	Tx power at Band 2 (1900MHz PCS)		22	23	dBm	
Rx						
Rx sens. @Band 8 (900MHz GSM)	Rx sensitivity at Band 8 (900MHz GSM)		-103	-100	dBm	
Rx sens. @Band 2 (1900MHz PCS)	Rx sensitivity at Band 2 (1900MHz PCS)		-103	-100	dBm	

e. LoRa

Mode: LoRa RAW mode and LoRa WAN mode LoRaWAN Node Type: Class Type A, Class Type C Frequency Band: EU868, US915

Table 6: LoRa Specifications

Parameter	Description	Min	Тур	Мах	Unit
General					
Freq. (EU)	Frequency band (EU)	863		870	GHz
Freq. (US)	Frequency band (US)	902		928	GHz
Power max. (EU)	Maximum power (EU)			15	dBm
Power max. (US)	Maximum power (US)			22	dBm
Тх					
Tx power (Tx tone) @866.4MHZ [EU868 band]	Tx power (Tx tone) at 866.4MHz		14	15	dBm
Tx power (Tx tone) @918.2MHZ [US915 band]	Tx power (Tx tone) at 918.2MHz		21	22	%
Rx					
Rx Sens. @freq=866.4MHz, BW=500kHz, SF=12	Rx sensitivity at 866.4MHz, 500kHz bandwidth and SF=12		-127		dBm
Rx Sens. @freq=866.4MHz, BW=500kHz, SF=12	Rx sensitivity at 866.4MHz, 500kHz bandwidth and SF=12		-127		dBm

3. Module Interface

a. Power Management

Table 7: Power Consumption by Mode of Operation

Mode of Operation	Min	Тур	Мах	Unit
Idle (no radio but MicroPython is running)		30		mA
Light sleep (wake up or restart is required for MicroPython to run)		800		μA
Deep sleep (wake up or restart is required for MicroPython to run)		10		μΑ

b. Memory Allocation

Module OS firmware, OTA and user space sizes:

- Module OS firmware: 2,560Kb
- OTA1 space: 2,560Kb
- OTA2 space: 2,560Kb
- User space: 8Mb

4. Mechanical Data

a. Mechanical Specification

All pins have a pin width of 0.7mm with the exception of pin VBATT (pin #A4) with pin width 1.0mm.

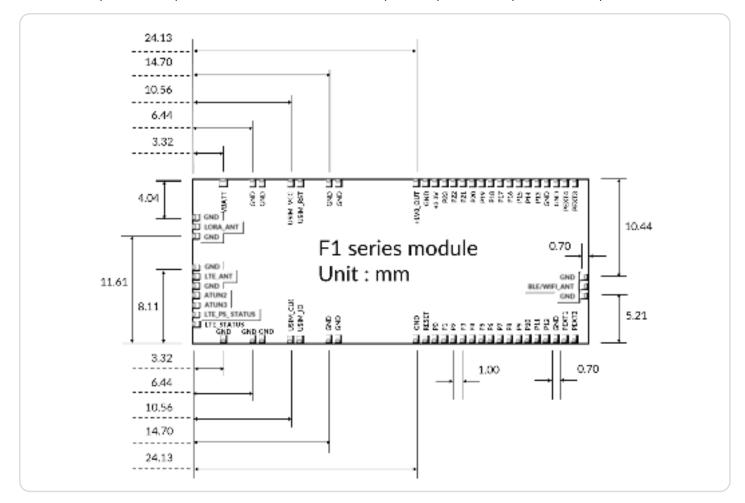


Figure 2: F1 Smart Module Pin Dimensions, Pin Pitches and Pin Locations (Top View)

b. Module Pin-out

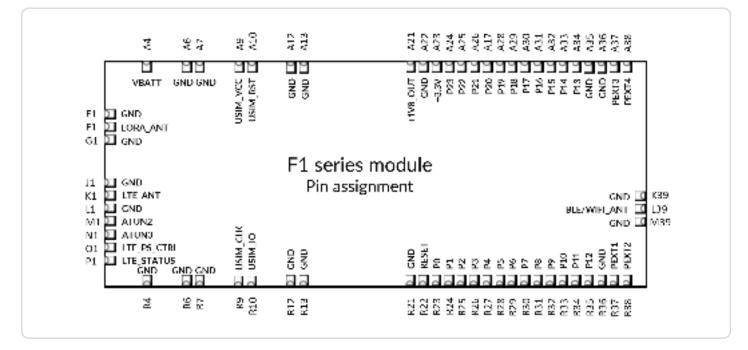


Figure 3: F1 Smart Module Pin-out (Top View)

Table 8:	: F1 S	Smart	Module	Pin-out
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Pin Number	Pin Name	MCU Pin	LTE Module Pin	Туре	Description
R4	GND			Power	Ground signal
R6	GND			Power	Ground signal
R7	GND			Power	Ground signal
R9	USIM_CLK		SIM0_CLK	Analog I/O	USIM interface I/O to GM02S
R10	USIM_IO		SIM0_IO	Digital I/O	USIM interface I/O to GM02S
R12	GND			Power	Ground signal
R13	GND			Power	Ground signal
R21	GND			Power	Ground signal
R22	RESET	CHIP_PU		Analog I/O	Reset pin to ESP32-S3 for module reset
R23	P0	UORXD		Analog I/O	UART0 RXD to ESP32-S3
R24	P1	UOTXD		Analog I/O	UART0 TXD to ESP32-S3
R25	P2	GPIO0		Analog I/O or Digital I/O	Analog I/O or Digital I/O to ESP32-S3
R26	P3	GPIO4		Analog I/O or Digital I/O	Analog I/O or Digital I/O to ESP32-S3
R27	P4	MTDO		Digital I/O	Digital I/O to ESP32-S3
R28	P5	GPIO5		Analog I/O or Digital I/O	Analog I/O or Digital I/O to ESP32-S3
*R29	P6	GPIO6			Reserved – Leave floating, do not connect

R31P8GPI046Digital I/ODigital I/O<	R30	P7	GPIO3		Analog I/O or Digital I/O	Analog I/O or Digital I/O to ESP32-S3	
R32P9GPI045Digital I/ODigital I/ODigital I/O to ESP32-S3R33P10MTCKDigital I/ODigital I/ODigital I/O to ESP32-S3R34P11GPI011Analog I/O or Digital I/O to ESP32-S3Analog I/O or Digital I/O to ESP32-S3R35P12GPI021Analog I/O or Digital I/O to ESP32-S3R36GNDPowerGround signalR37PEXT1GPI011Analog I/O or 	R31	P8	GPIO46	1	-	Digital I/O to ESP32-S3	
R34 P11 GPI01 Analog (// O r Digital I/O to ESP32-S3 R35 P12 GPI021 Analog (// O r Digital I/O to ESP32-S3 R36 GND Power Ground signal R37 PEXT1 GPI01 Analog (// O r Digital I/O to ESP32-S3 R38 PEXT2 GPI012 Analog (// O r Digital I/O to ESP32-S3 M39 GND Power Ground signal L39 BLE/WIFLANT RF I/O RF Interface to ESP32-S3 for BLE and/or WI-F1 interface K39 GND Power Ground signal A38 PEXT4 GPI014 Power Ground signal A38 PEXT3 GPI013 Analog I/O or Digital I/O to ESP32-S3 A37 PEXT3 GPI013 Analog I/O or Digital I/O to ESP32-S3 A38 PEXT4 GPI020 Analog I/O or Digital I/O to ESP32-S3 A37 PEXT3 GPI020 Analog I/O or Digital I/O to ESP32-S3 A38 OND Power Ground signal A33 P14 GPI020 Analog I/O or Digital I/O to ESP32-S3 A33 P14 GPI019 Analog I/O or Digital I/O to ESP32-S	R32	P9	GPIO45				
R35P12GPI021Digital //OAnalog //O or Digital //O to ESP32-S3R36GNDPowerGround signalR37PEXT1GPI01Analog //O orAnalog //O or Digital //O to ESP32-S3R38PEXT2GPI012Analog //O orAnalog //O or Digital //O to ESP32-S3M39GNDPowerGround signalL39BLE/WIFLANTPOWERFri Interface to ESP32-S3 for BLE and/or WI-Fi interfaceK39GNDPowerGround signalA38PEXT2GPI013PowerA38PEXT4GPI014PowerA38PEXT3GPI013PowerA39GNDPowerGround signalA31PEXT3GPI013PowerA33PEXT3GPI014PowerA34P13GPI020Analog //O orA35GNDPowerGround signalA34P13GPI020Analog //O orA33P14GPI019PowerJ33P15GPI038Digital //OA33P18GPI010Digital //OA34P19GPI016A35GPI017Analog //O orA36P18GPI019A37P20GPI038A38P14GPI019A39P16GPI038A31P16GPI041Digital //OAnalog //O or Digital //O to ESP32-S3A33P17GPI02A33P18GPI016A28P19GPI016 <td>R33</td> <td>P10</td> <td>МТСК</td> <td></td> <td>Digital I/O</td> <td>Digital I/O to ESP32-S3</td>	R33	P10	МТСК		Digital I/O	Digital I/O to ESP32-S3	
R36GNDDigital I/OControl signalR37PEXT1GPI01Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3R38PEXT2GPI012Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3R38PEXT2GPI012Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3M39GNDPowerGround signalL39BLE/WIFLANTPowerGround signalA38PEXT4GPI014PowerGround signalA38PEXT3GPI013Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A37PEXT3GPI013Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A36GNDPowerGround signalA37PEXT3GPI013Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A38P13GPI020Analog I/O or Digital I/OInalog I/O or Digital I/O to ESP32-S3A33P14GPI019Analog I/O or Digital I/OInalog I/O or Digital I/O to ESP32-S3A33P15GPI038Digital I/OAnalog I/O or Digital I/O to ESP32-S3A30P17GPI01Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A28P19GPI016Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A26P21GPI018Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A27P20GPI018Analog I/O or Digital I/O	R34	P11	GPIO11		Analog I/O or Digital I/O	Analog I/O or Digital I/O to ESP32-S3	
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Image: state of the state of	R36	GND			Power	Ground signal	
M39GNDOrPowerGround signalL39BLE/WIFLANTRFRFI/ORF interface to ESP32-S3 for BLE and/or Wi-FiK39GNDPowerGround signalA38PEXT4GPI014PowerGround signalA38PEXT3GPI013Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A37PEXT3GPI013Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A36GNDPowerGround signalA35GNDPowerGround signalA36GNDPowerGround signalA37P13GPI020Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A33P14GPI019Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A33P14GPI018Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A31P16GPI038Digital I/ODigital I/OA32P17GPI02Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A33P14GPI010Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A34P18GPI010Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A34P19GPI016Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A26P21GPI016Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A27P20GPI018Analog I/	R37	PEXT1	GPIO1			Analog I/O or Digital I/O to ESP32-S3	
L39BLE/WIFLANTImage: Second sec	R38	PEXT2	GPIO12			Analog I/O or Digital I/O to ESP32-S3	
InterfaceK39GNDImage: constraint of the second signalA38PEXT4GPI014Analog I/O or Digital I/O to ESP32-S3A37PEXT3GPI013Analog I/O or Digital I/O to ESP32-S3A36GNDImage: constraint of the second signalAnalog I/O or Digital I/O to ESP32-S3A36GNDImage: constraint of the second signalGround signalA35GNDImage: constraint of the second signalGround signalA36GNDImage: constraint of the second signalAnalog I/O or Digital I/O to ESP32-S3A37P13GPI020Analog I/O or Digital I/O to ESP32-S3A33P14GPI019Analog I/O or Digital I/O to ESP32-S3A31P16GPI041Digital I/ODigital I/O to ESP32-S3A31P16GPI010Analog I/O or Digital I/O to ESP32-S3A33P17GPI02Analog I/O or Digital I/O to ESP32-S3A30P17GPI02Analog I/O or Digital I/O to ESP32-S3A33P18GPI010Analog I/O or Digital I/O to ESP32-S3A29P18GPI016Analog I/O or Digital I/O to ESP32-S3A27P20GPI016Analog I/O or Digital I/O to ESP32-S3A26P21GPI018Analog I/O or Digital I/O to ESP32-S3A27P22GPI018Analog I/O or Digital I/O to ESP32-S3A28P21GPI018Analog I/O or Digital I/O to ESP32-S3A26P21GPI018Analog I/O or Digital I/O to ESP32-S3A27P20GPI018Analog I/O	M39	GND			Power	Ground signal	
A38PEXT4GPI014Analog I/O or Digital I/O to ESP32-S3A37PEXT3GPI013Analog I/O or Digital I/O to ESP32-S3A36GNDPowerGround signalA35GNDPowerGround signalA34P13GPI020Analog I/O or Digital I/O to ESP32-S3A33P14GPI019Analog I/O or Digital I/O to ESP32-S3A33P14GPI019Analog I/O or Digital I/O to ESP32-S3A33P15GPI038Digital I/OA32P15GPI041Digital I/OA33P16GPI041Digital I/OA30P17GPI02Analog I/O or Digital I/O to ESP32-S3A31P16GPI010Analog I/O or Digital I/O to ESP32-S3A29P18GPI010Analog I/O or Digital I/OA28P19GPI015Analog I/O or Digital I/OA28P19GPI016Analog I/O or Digital I/OA26P21GPI017Analog I/O or Digital I/OA28P23GPI042Digital I/OA24P23GPI042Analog I/O or Digital I/OA25P22GPI018Analog I/O or Digital I/OA26P21GPI04Digital I/OA27P20GPI04Digital I/OA28P23GPI042A29P23GPI042A26P21GPI04A27P20GPI04A28P21A29QPI04A29P20A20QPI04A20 <td>L39</td> <td>BLE/WIFI_ANT</td> <td></td> <td></td> <td>RF I/O</td> <td></td>	L39	BLE/WIFI_ANT			RF I/O		
LandLa	K39	GND			Power	Ground signal	
A36GNDImage: Control of the state of	A38	PEXT4	GPIO14			Analog I/O or Digital I/O to ESP32-S3	
A35GNDConstraintPowerGround signalA34P13GPI020Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3 (USB OTG D+)A33P14GPI019Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3 (USB OTG D-)A32P15GPI038Digital I/ODigital I/O to ESP32-S3A31P16GPI041Digital I/ODigital I/O to ESP32-S3A30P17GPI02Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A29P18GPI010Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A28P19GPI015Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A26P21GPI017Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A25P22GPI018Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A23+3.3VVDD3P3_RTC VDDAVDD3P3_RTC VDDAPowerVoltage supply to ESP32-S3 and module main circuit	A37	PEXT3	GPIO13			Analog I/O or Digital I/O to ESP32-S3	
A34P13GPI020Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3 /USB OTG D+A33P14GPI019Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3 /USB OTG D-A32P15GPI038Digital I/ODigital I/OA31P16GPI041Digital I/ODigital I/O to ESP32-S3A30P17GPI02Analog I/O or Digital I/ODigital I/O to ESP32-S3A29P18GPI010Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A28P19GPI015Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A26P21GPI017Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A26P21GPI018Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A24P23GPI042Digital I/OAnalog I/O or Digital I/O to ESP32-S3A23+3.3VVDD3P3_RTC VDDAPowerVoltage supply to ESP32-S3 and module main circuit	A36	GND			Power	Ground signal	
A33P14GPI019Digital //O//USB OTG D+A33P14GPI019Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A32P15GPI038Digital I/ODigital I/OA31P16GPI041Digital I/ODigital I/OA33P17GPI02Analog I/O or Digital I/ODigital I/O to ESP32-S3A30P17GPI02Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A29P18GPI010Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A28P19GPI015Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A26P21GPI016Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A24P23GPI042Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A23+3.3VVDD3P3_RTC VDDAPowerVoltage supply to ESP32-S3 and module main circuit	A35	GND			Power	Ground signal	
A32P15GPI038Digital I/O/USB OTG D-A31P16GPI041Digital I/ODigital I/O to ESP32-S3A30P17GPI02Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A29P18GPI010Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A28P19GPI015Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A27P20GPI016Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A26P21GPI017Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A25P22GPI018Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A23+3.3VVDD3P3_CPU VDD3P3_VDDAPowerVoltage supply to ESP32-S3 and module main circuit	A34	P13	GPIO20				
A31P16GPIO41Digital I/ODigital I/O to ESP32-S3A30P17GPIO2Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A29P18GPIO10Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A28P19GPIO15Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A27P20GPIO16Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A26P21GPIO17Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A25P22GPIO18Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A24P23GPIO42Digital I/ODigital I/OA23+3.3VVDD3P3_CPU VDDAPowerVoltage supply to ESP32-S3 and module main circuit	A33	P14	GPIO19				
A30P17GPIO2Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A29P18GPIO10Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A28P19GPIO15Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A27P20GPIO16Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A26P21GPIO17Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A25P22GPIO18Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A24P23GPIO42Digital I/ODigital I/OA23+3.3VVDD3P3_RTC VDDAPowerVoltage supply to ESP32-S3 and module main circuit	A32	P15	GPIO38		Digital I/O	Digital I/O to ESP32-S3	
A29P18GPIO10Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A28P19GPIO15Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A27P20GPIO16Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A26P21GPIO17Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A25P22GPIO18Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A24P23GPIO42Digital I/ODigital I/OA23+3.3VVDD3P3_CPU VDDAPowerVoltage supply to ESP32-S3 and module main circuit	A31	P16	GPIO41		Digital I/O	Digital I/O to ESP32-S3	
A28P19GPIO15Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A27P20GPIO16Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A26P21GPIO17Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A25P22GPIO18Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A24P23GPIO42Digital I/ODigital I/OA23+3.3VVDD3P3_CPU VDDAPowerVoltage supply to ESP32-S3 and module main circuit	A30	P17	GPIO2			Analog I/O or Digital I/O to ESP32-S3	
A27P20GPI016Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A26P21GPI017Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A25P22GPI018Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A24P23GPI042Digital I/ODigital I/OA23+3.3VVDD3P3_CPU VDDAPowerVoltage supply to ESP32-S3 and module main circuit	A29	P18	GPIO10			Analog I/O or Digital I/O to ESP32-S3	
A26P21GPI017Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A25P22GPI018Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A24P23GPI042Digital I/ODigital I/OA23+3.3VVDD3P3_CPU VDD3P3_RTC VDDAPowerVoltage supply to ESP32-S3 and module main circuit	A28	P19	GPIO15			Analog I/O or Digital I/O to ESP32-S3	
A25P22GPIO18Analog I/O or Digital I/OAnalog I/O or Digital I/O to ESP32-S3A24P23GPIO42Digital I/ODigital I/OA23+3.3VVDD3P3_CPU VDD3P3_RTC VDDAPowerVoltage supply to ESP32-S3 and module main circuit	A27	P20	GPIO16			Analog I/O or Digital I/O to ESP32-S3	
A24 P23 GPIO42 Digital I/O Digital I/O A23 +3.3V VDD3P3_CPU VDD3P3_RTC VDD3P3 VDDA Power Voltage supply to ESP32-S3 and module main circuit	A26	P21	GPIO17			Analog I/O or Digital I/O to ESP32-S3	
A23 +3.3V VDD3P3_CPU VDD3P3_RTC VDD3P3 VDDA Power Voltage supply to ESP32-S3 and module main circuit	A25	P22	GPIO18			Analog I/O or Digital I/O to ESP32-S3	
VDD3P3_RTC VDD3P3 VDDA	A24	P23	GPIO42		Digital I/O	Digital I/O to ESP32-S3	
A22 GND Power Ground signal	A23	+3.3V	VDD3P3_RTC VDD3P3		Power		
	A22	GND			Power	Ground signal	

A21	+1.8V_OUT	VDD_SPI		Power	Voltage supply VDD_SPI to ESP32-S3 for SPI flash and PSRAM
A13	GND			Power	Ground signal
A12	GND			Power	Ground signal
A10	USIM_RST		SIM0_RSTN	Digital I/O	USIM interface I/O to GM02S
A9	USIM_VCC		SIM0_VCC	Power	USIM voltage supply to GM02S
A7	GND			Power	Ground signal
A6	GND			Power	Ground signal
A4	+VBATT		VBAT	Power	Voltage supply to GM02S
E1	GND			Power	Ground signal
F1	LORA_ANT			RF I/O	RF interface to SX1262 for LoRa interface
G1	GND			Power	Ground signal
J1	GND			Power	Ground signal
K1	LTE_ANT		LTE_ANT	RF I/O	RF interface to GM02S for LTE CAT-M1/CAT- NB1/CAT-NB2 interface
L1	GND			Power	Ground signal
M1	ATUN2		GPIO34/ ANT_TUNE0	Analog I/O	ANT_TUNE I/O to GM02S
N1	ATUN3		GPIO35/ ANT_TUNE1	Analog I/O	ANT_TUNE I/O to GM02S
01	LTE_PS_CTRL		GPIO2/ PS_STATUS	Digital I/O	Power saving status I/O from GM02S
P1	LTE_STATUS		GPIO1/ STATUS_LED	Digital I/O	LTE status I/O from GM02S

c. Recommended PCB Landing Pattern

All pins have a pin width of 0.7mm with the exception of pin VBATT (pin #A4) with pin width 1.0mm.

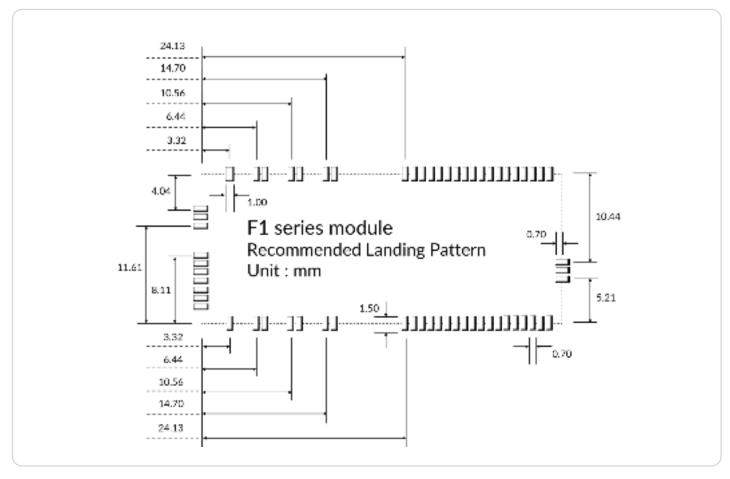


Figure 4: F1 Smart Module Recommended PCB Landing Pattern (Top View)

d. Recommended Basic Circuit

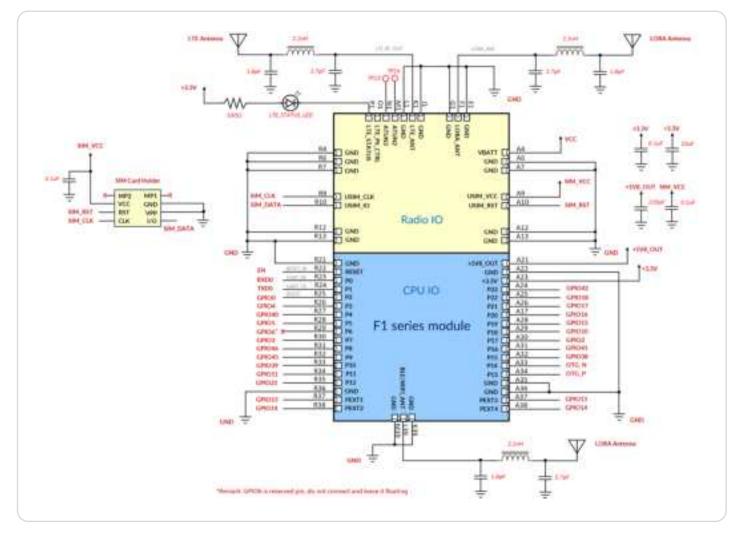
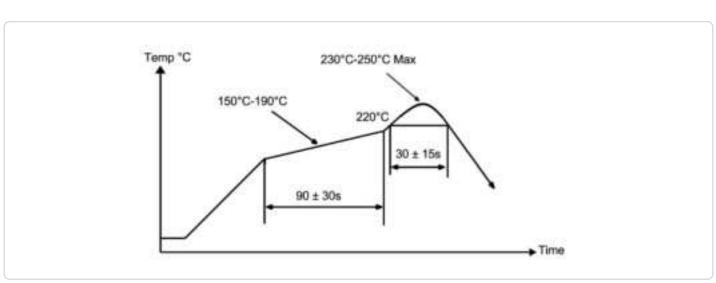


Figure 5: F1 Smart Module Recommended Basic Circuit



e. Recommended Soldering Profile



5. MicroPython Application Development on F1

a. Device Programming via UART

- By default, the F1 Smart Module runs an interactive python REPL (Read-Eval-Print-Loop) on UARTO which is connected to P0 (RX) and P1 (TX) running at 115200 baud.
- The Module can be connected via a development board or any USB UART adapter. Code can be run via the REPL and the SG Wireless CtrlR. Visual Studio Code plug-in can also be used to upload code to the board.

b. Module-supported Libraries

Table 9: F1 Smart Module Supported Libraries

Library	Min
Python Standard Libraries*	array, aysncio, binascii, builtins, cmath, collections, errno, gc, gzip, hashlib, heapq, io, json, math, os, platform, random, re, select, socket, ssl, struct, sys, time, zlib, _thread
MicroPython-specific Libraries*	Bluetooth, btree, cryptolib, deflate, framebuf, machine, micropython, neopixel, network, uctypes, esp, esp32
	lte: Ready-to-use LTE CAT-M1/NB1/NB2 library lora: Ready-to-use LoRa RAW and full stack LoRa WAN device Class A, Class C library ctrl: Ready-to-use Ctrl Cloud Platform client library

* MicroPython documentation library with API function calls (<u>https://docs.micropython.org/en/latest/library/</u>).

* SG Wireless F1 Smart Module documentation library with API function calls.

c. MicroPython Capability – REPL (Read-Eval-Print Loop)

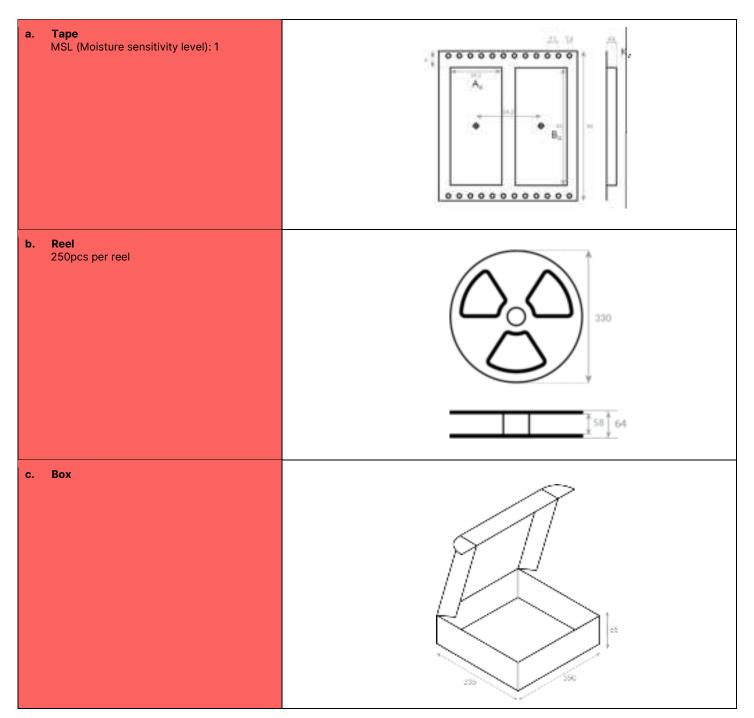
The MicroPython-ready F1 Smart Module carries the REPL shell that that can execute codes in realtime, as well as enable section-by-section of code execution through a copy-and-paste function, both of which facilitate real-time debugging and instant application code prototyping.

III. COM10 - Tera Term VT		×
Eile Edit Setup Control Window Help		
help() Welcome to HicroPython on the ESP321		
For generic online doce places visit <u>http://doce.sicrenthin.org/</u>		
For access to the hardware use the 'machine' module:		
<pre>import machine pint2 = machine.Pin(12, machine.Pin.00T) pint2,value(1) pint3 = machine.Pin(13, machine.Pin.1N, machine.Pin.POLL_UP) print(pint3,value()) i2c = machine.I2C(scl-machine.Pin(21), sda-machine.Pin(22)) i2c.scan() i2c.scan() i2c.read(ron1addr, b*1234*) i2c.read(ron1addr, 4)</pre>		
Basic HiFi configurations		
import network sta_if = network.HERMInetwork.STR_DF); sta_if.activelTrue) sta_if.scan[] = Scan for available access paints sta_if.connect("GR_name)", "(password)") = Connect to an SP sta_if.isconnected() = Check for successful connection		
Control commands: CTRL-R — on a blank line, enter raw REPL node CTRL-B — on a blank line, enter normal REPL node CTRL-C — interrupt a running program CTRL-C — on a blank line, do a soft reset of the board CTRL-E — on a blank line, enter paste node CTRL-F — do hand reset in safeboot mode		
For further help on a specific object, type help[obj] For a list of available modules, type help["modules"] >>>		

Figure 7: F1 Smart Module Code Sample in REPL Shell

6. Product Packaging

Modules are packed in tape-and-reel packaging and shipped out in carton boxes.



7. Certification

a. CE Statements

EU Declaration of Conformity (DOC)

Hereby, SG Wireless Limited declares that the F1 Smart Module series is in compliance with Radio Equipment Directive (RED) 2014/53/EU.

The full text of the EU declaration of conformity is available at the following Internet address: https://docs.sgwireless.com

RF exposure statement

RF exposure information: The Maximum Permissible Exposure (MPE) level has been calculated based on a distance of d=20cm between the device and the human body. To maintain compliance with RF exposure requirement, use product that maintain a 20cm distance between the device and human body.

CE marking and labeling

By complying to CE standard, all modules are laser printed with "CE" marking and part number at surface of the module shield can; and manufacturer information is printed with label at shipping box/packages.

CE marking on module:





DOM: N













SG Confidential

<complex-block>

Manufacturer information at shipping package/box:

b. FCC Statements

This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

(1) This device may not cause harmful interference.

(2) This device must accept any interference received, including interference that may cause undesired operation.

CAUTION: Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.

- Increase the separation between the equipment and receiver.

- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.

- Consult the dealer or an experienced radio/TV technician for help.

FCC Radiation Exposure Statement:

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance 20cm between the radiator & your body.

OEM INTEGRATION INSTRUCTIONS:

This device is intended only for OEM integrators under the following conditions:

The module must be installed in the host equipment such that 20 cm is maintained between the antenna and users, and the transmitter module may not be co-located with any other transmitter or antenna. The module shall be only used with the internal on-board antenna that has been originally tested and certified with this module. External antennas are not supported. As long as these 3 conditions above are met, further transmitter test will not be required.

However, the OEM integrator is still responsible for testing their end-product for any additional compliance requirements required with this module installed (for example, digital device emissions, PC peripheral requirements, etc.). The end-product may need Verification testing, Declaration of Conformity testing, a Permissive Class II Change or new Certification. Please involve a FCC certification specialist in order to determine what will be exactly applicable for the end-product.

Validity of using the module certification:

In the event that these conditions cannot be met (for example certain laptop configurations or colocation with another transmitter), then the FCC authorization for this module in combination with the host equipment is no longer considered valid and the FCC ID of the module cannot be used on the final product. In these circumstances, the OEM integrator will be responsible for re-evaluating the end product (including the transmitter) and obtaining a separate FCC authorization. In such cases, please involve a FCC certification specialist in order to determine if a Permissive Class II Change or new Certification is required.

Upgrade Firmware:

The software provided for firmware upgrade will not be capable to affect any RF parameters as certified for the FCC for this module, in order to prevent compliance issues.

End product labeling:

This transmitter module is authorized only for use in device where the antenna may be installed such that 20 cm may be maintained between the antenna and users. The final end product must be labeled in a visible area with the following: "Contains FCC ID: 2AS9406" (for F1), "Contains FCC ID: 2AS9407" (for F1/C), "Contains FCC ID: 2AS9408" (for F1/L), "Contains FCC ID: 2AS9409" (for F1/W), "Contains FCC ID: 2AS9410" (for F1s), "Contains FCC ID: 2AS9411" (for F1/Cs), "Contains FCC ID: 2AS9412" (for F1/Ls), "Contains FCC ID: 2AS9413" (for F1W/s).

Information that must be placed in the end user manual:

The OEM integrator has to be aware not to provide information to the end user regarding how to install or remove this RF module in the user's manual of the end product which integrates this module. The end user manual shall include all required regulatory information/warning as show in this manual.

"CAUTION : Exposure to Radio Frequency Radiation.

Antenna shall be mounted in such a manner to minimize the potential for human contact during normal operation. The antenna should not be contacted during operation to avoid the possibility of exceeding the FCC radio frequency exposure limit.

Orderable part number	Model no.	BLE/Wi-Fi	LTE Cat-M1/ NB-IoT	LoRa(WAN)	Security element
SGW3531	F1s	√	✓	✓	✓
SGW3501	F1	√	✓	✓	×
SGW3431	F1/Cs	✓	✓	×	✓
SGW3401	F1/C	✓	✓	×	×
SGW3231	F1/Ls	✓	×	✓	✓
SGW3201	F1/L	✓	×	✓	×
SGW3131	F1/Ws	✓	×	×	✓
SGW3101	F1/W	✓	×	×	×

8. Orderable part number/Model no. comparison table

9. Revision History

Version	Released Date	Description
1.0	Feb 7, 2024	Initial document release
1.1	Mar 6, 2024	Branding revised with updates: Introduction: Operating temperature updated Section 4: Pin number updated (A38 and A37) Section 5b: Link to MicroPython documentation library updated Section 6a: MSL updated
1.2	Jul 7, 2024	Adding following section: Section 7: Certification Section 8: Part number/Model no. comparison table
1.3	Aug 13, 2024	Updating following section: Section 7b: FCC Statements

10.Contact

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