

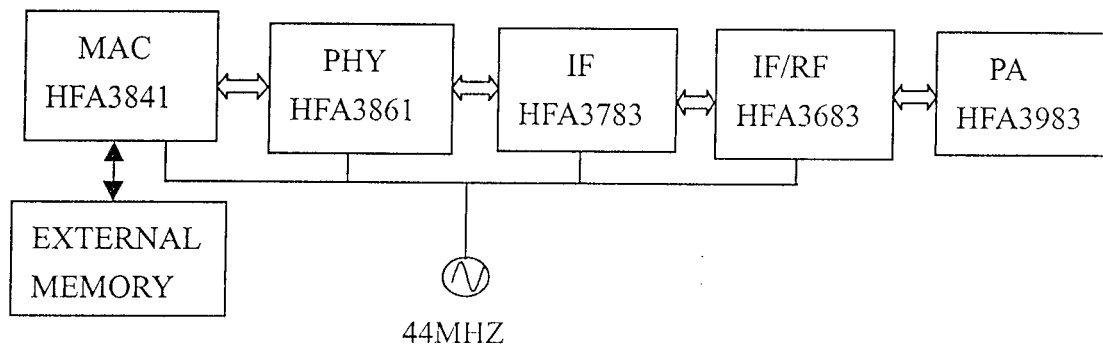
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Block Diagram

11 CW DSS TX/RX

2400 ~ 2483.5 MHz

Part 15.247



- **MAC Controller (HFA3841)**
 - ⇒ To support MAC protocol of IEEE 802.11 standards
 - ⇒ To support the interface of PC Card standard (PCMCIA V2.1)
- **External Memory**
 - ⇒ Volatile memory: 128k*8*2 SRAM
 - ⇒ Non-volatile memory: 128k*8 Flash
- **PHY Transceiver (HFA3861)**
 - ⇒ Direct Sequence Spread Spectrum Baseband Processor
- **IF (HFA3783)**
 - ⇒ I/Q Channel Modulation and Demodulation
- **IF/RF (HFA3683)**
 - ⇒ LNA and Mixer
- **PA (HFA3983)**