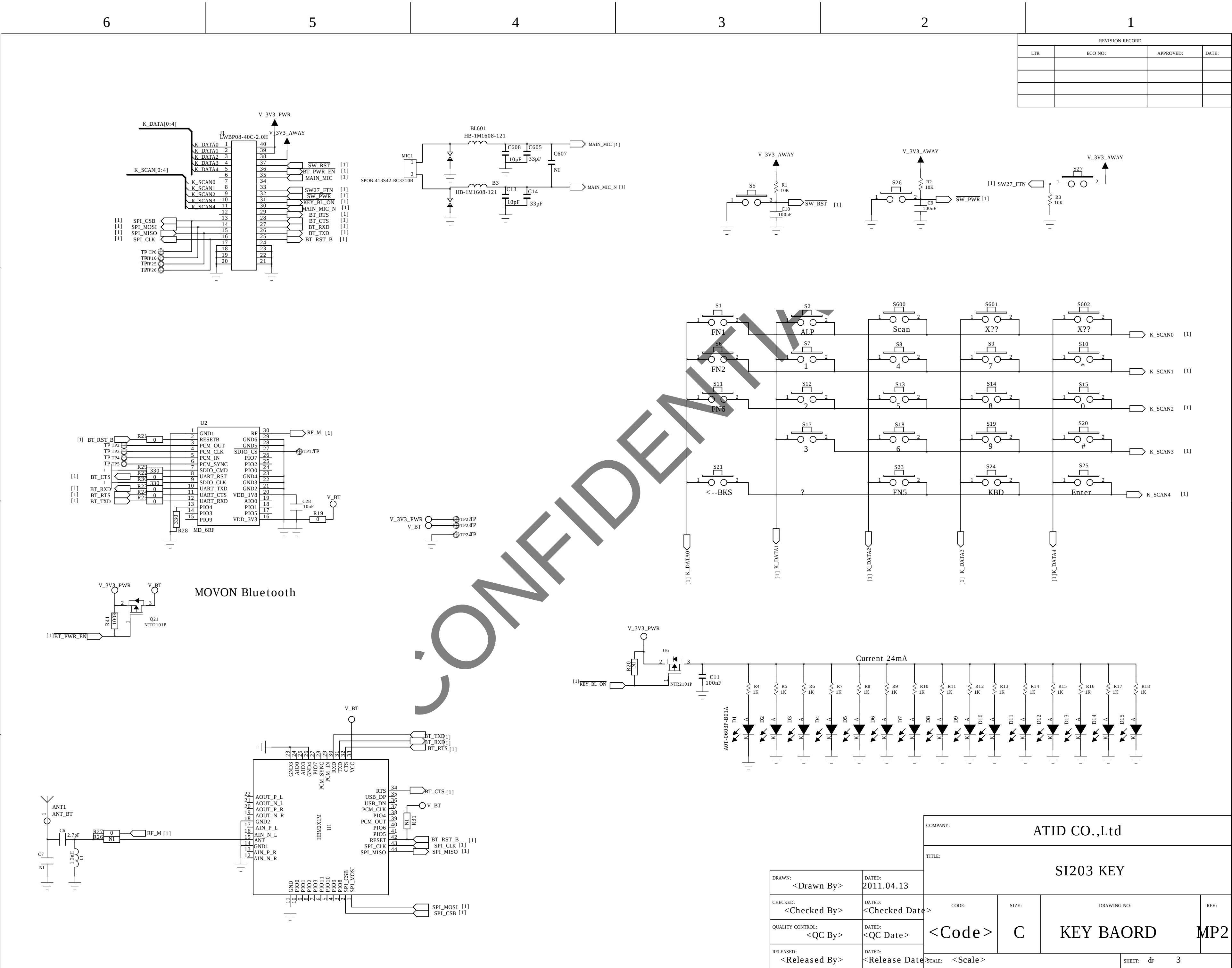
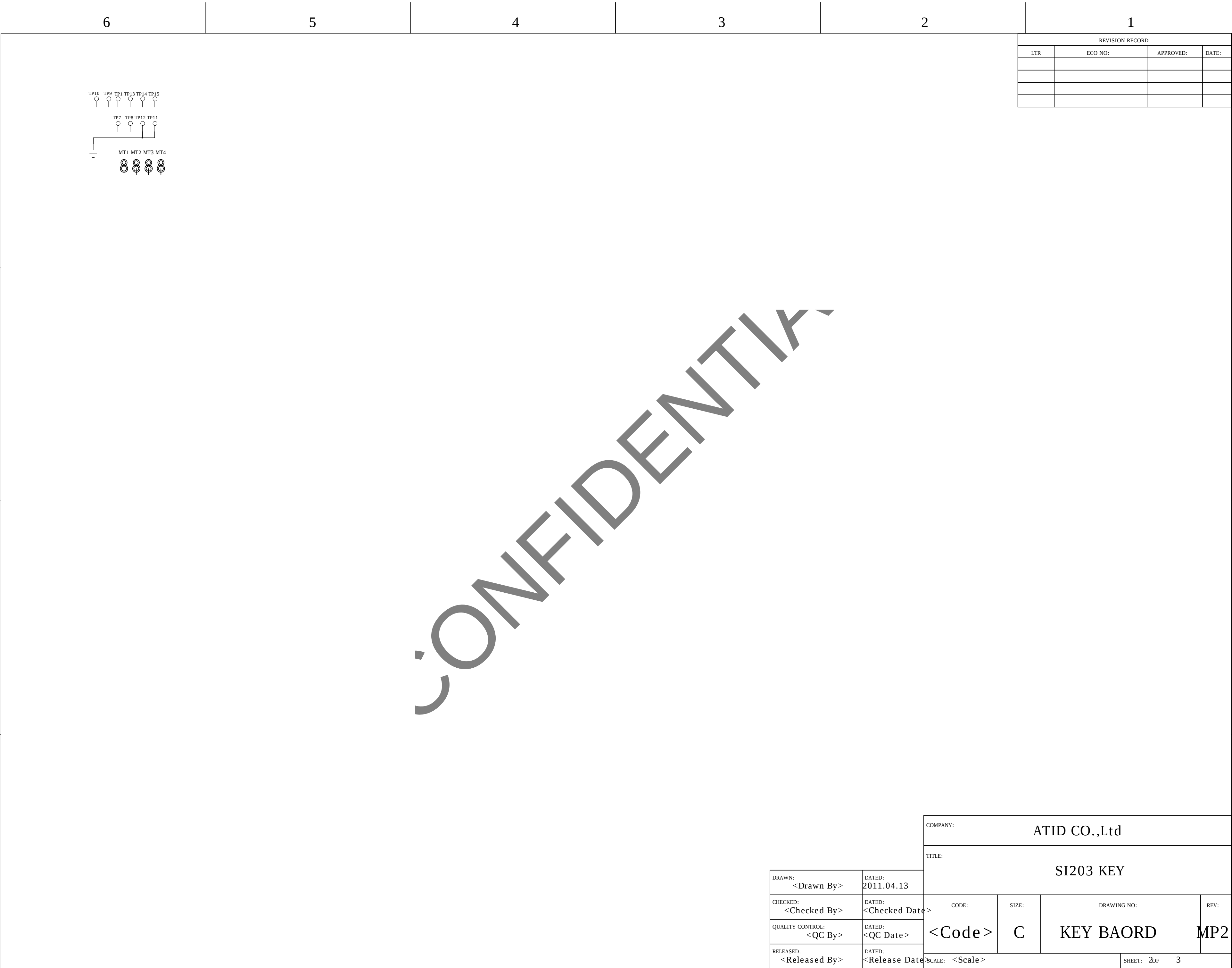




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COMPANY: ATID CO.,Ltd			
TITLE: SI203 KEY			
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CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO: KEY BAORD	REV: MP2
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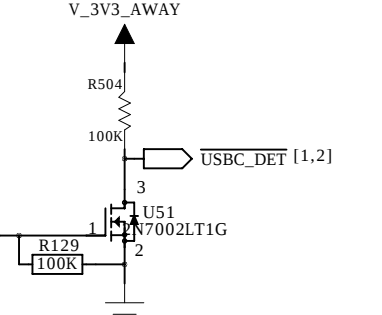


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COMPANY:		ATID CO.,Ltd	
TITLE:		SI203-WCDMA/AME2000	
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Phone IO 1.8V-2.7V



The schematic diagram illustrates the I/O interface for the SP3222EBCY-L module. It shows the connection of the module's pins to external components, including a microSD card and a phone interface.

MicroSD CARD Interface:

- V_3V3_PWR:** Connected to the module's V_3V3_PWR pin.
- V_3V3_SYS:** Connected to the module's V_3V3_SYS pin.
- DATA0-DATA3:** Connected to the module's DATA0-DATA3 pins.
- CMD:** Connected to the module's CMD pin.
- CLK:** Connected to the module's CLK pin.
- GND0:** Connected to the module's GND0 pin.

Phone Interface:

- PHONE_RXD:** Connected to the module's PHONE_RXD pin.
- PHONE_TXD:** Connected to the module's PHONE_TXD pin.
- PHONE_DCD:** Connected to the module's PHONE_DCD pin.
- PHONE_DT:** Connected to the module's PHONE_DT pin.
- PHONE_PSN:** Connected to the module's PHONE_PSN pin.
- PHONE_RST:** Connected to the module's PHONE_RST pin.
- PHONE_CS:** Connected to the module's PHONE_CS pin.

Other Components:

- TS3USB30ERSWK:** A USB-to-UART bridge chip.
- TS3USB30ERSWR:** A USB-to-UART bridge chip.
- Capacitors:** Various capacitors (e.g., 100nF, 100pF, 100k) are used for decoupling and timing.
- Resistors:** Various resistors (e.g., 10k, 100k, 1k) are used for pull-up and pull-down.

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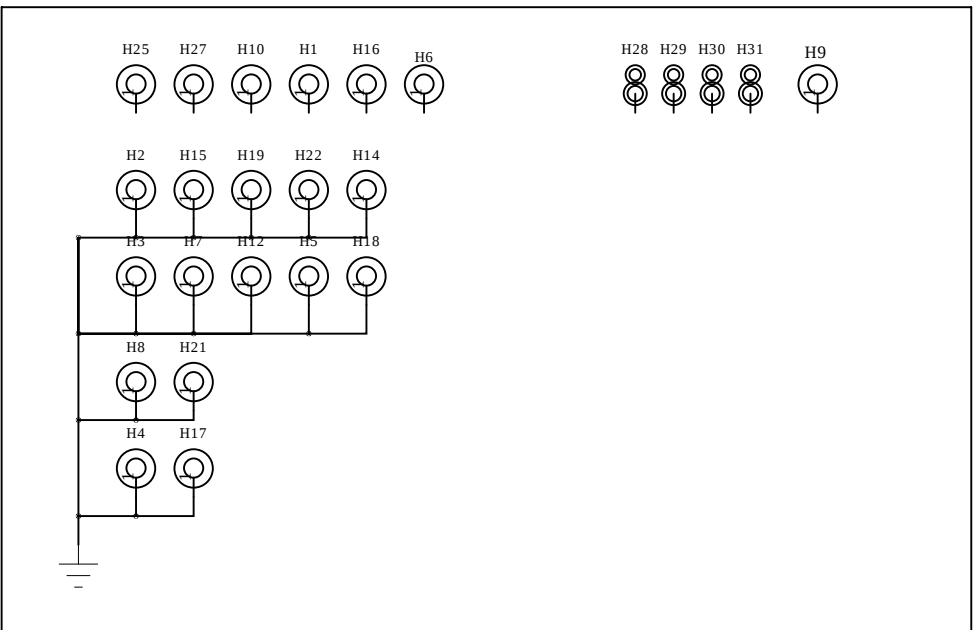
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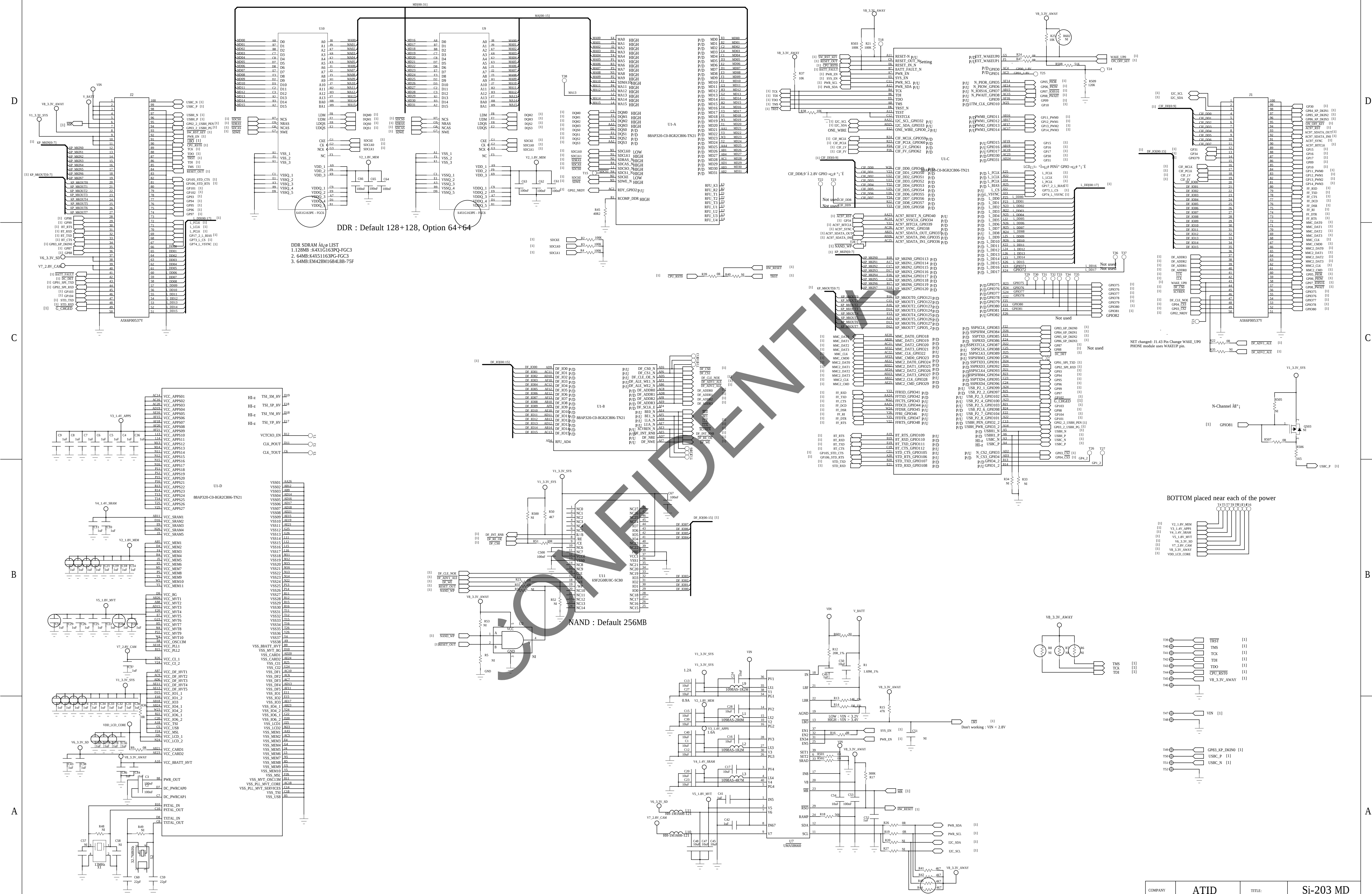
HF INTERFACE



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CONFIDENTIAL

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COMPANY	ATID	TITLE	Si-203 MD
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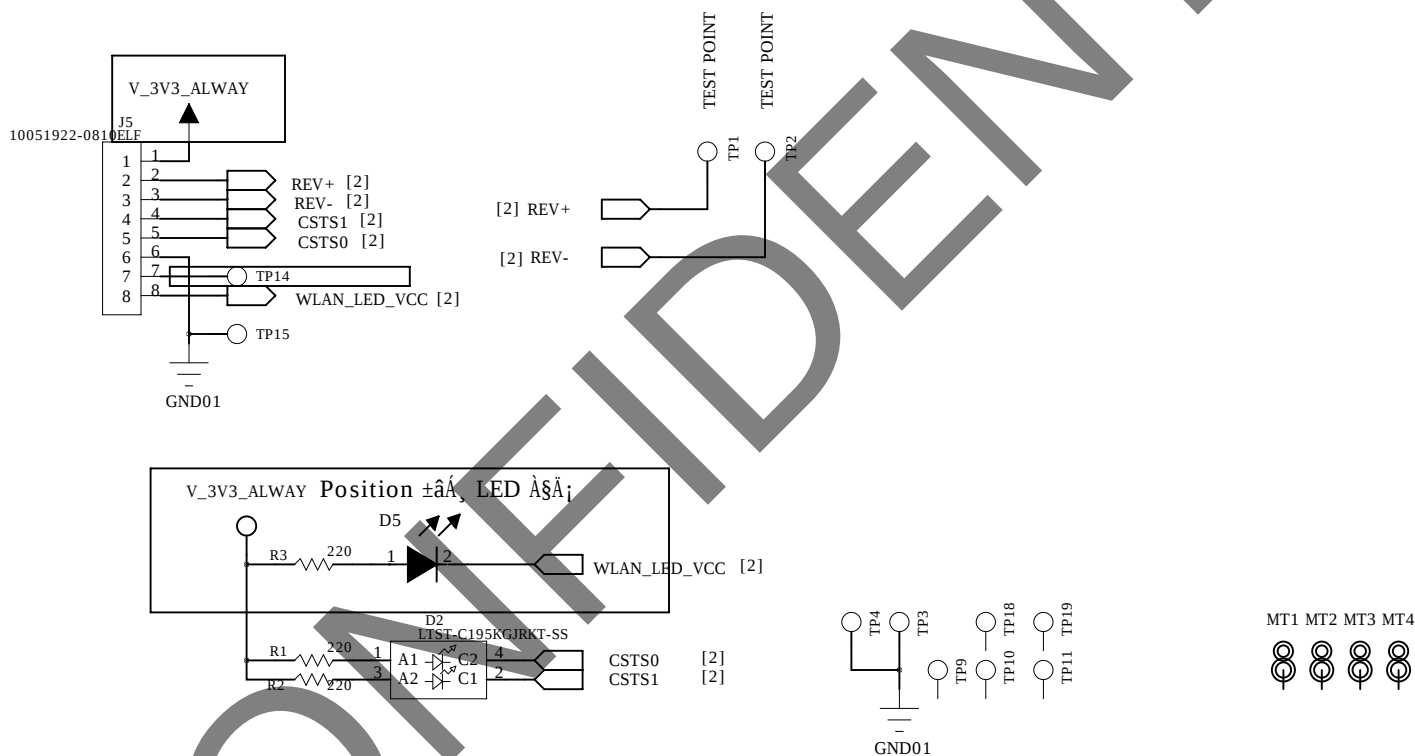
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REVISION RECORD			
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