



11AC20SISO_Ant1_5785



11AC20SISO_Ant2_5785



11AC20SISO_Ant1_5825



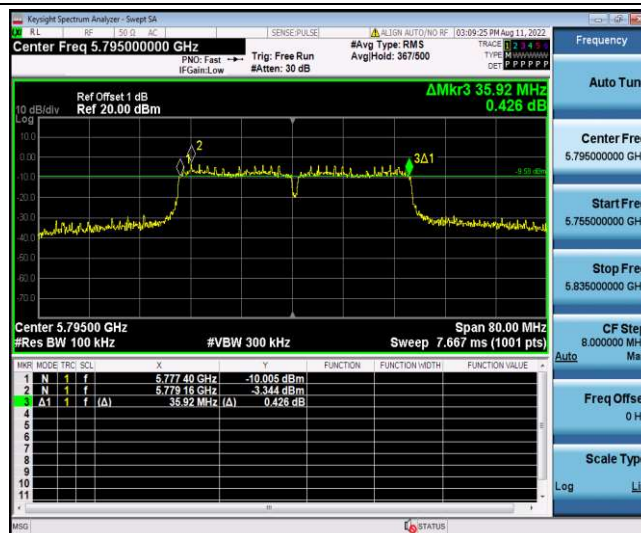
11AC20SISO_Ant2_5825



11AC40SISO_Ant1_5755



11AC40SISO_Ant2_5755



11AC40SISO_Ant1_5795



11AC40SISO_Ant2_5795



11AC80SISO_Ant1_5775



11AC80SISO_Ant2_5775

Appendix C: Maximum conducted output power

Test Result

TestMode	Antenna	Channel	Result[dBm]	Limit[dBm]	Verdict
11A	Ant1	5180	13.28	≤23.98	PASS
	Ant2	5180	13.24	≤23.98	PASS
	Ant1	5200	12.83	≤23.98	PASS
	Ant2	5200	13.13	≤23.98	PASS
	Ant1	5240	13.71	≤23.98	PASS
	Ant2	5240	13.86	≤23.98	PASS
	Ant1	5745	12.26	≤30	PASS
	Ant2	5745	12.23	≤30	PASS
	Ant1	5785	11.34	≤30	PASS
	Ant2	5785	12.52	≤30	PASS
	Ant1	5825	12.35	≤30	PASS
	Ant2	5825	11.16	≤30	PASS
11N20SISO	Ant1	5180	13.39	≤23.98	PASS
	Ant2	5180	12.63	≤23.98	PASS
	Ant1	5200	13.71	≤23.98	PASS
	Ant2	5200	12.95	≤23.98	PASS
	Ant1	5240	13.93	≤23.98	PASS
	Ant2	5240	12.36	≤23.98	PASS
	Ant1	5745	12.46	≤30	PASS
	Ant2	5745	12.35	≤30	PASS
	Ant1	5785	11.64	≤30	PASS
	Ant2	5785	11.70	≤30	PASS
	Ant1	5825	12.50	≤30	PASS
	Ant2	5825	12.50	≤30	PASS
11N40SISO	Ant1	5190	13.39	≤23.98	PASS
	Ant2	5190	12.59	≤23.98	PASS
	Ant1	5230	13.92	≤23.98	PASS
	Ant2	5230	13.20	≤23.98	PASS
	Ant1	5755	12.23	≤30	PASS
	Ant2	5755	12.17	≤30	PASS
	Ant1	5795	11.34	≤30	PASS
	Ant2	5795	12.54	≤30	PASS
11AC20SISO	Ant1	5180	13.46	≤23.98	PASS
	Ant2	5180	12.42	≤23.98	PASS
	Ant1	5200	13.63	≤23.98	PASS
	Ant2	5200	13.04	≤23.98	PASS
	Ant1	5240	12.86	≤23.98	PASS
	Ant2	5240	13.28	≤23.98	PASS

	Ant1	5745	11.32	≤30	PASS
	Ant2	5745	12.31	≤30	PASS
	Ant1	5785	10.46	≤30	PASS
	Ant2	5785	10.55	≤30	PASS
	Ant1	5825	9.99	≤30	PASS
	Ant2	5825	10.16	≤30	PASS
11AC40SISO	Ant1	5190	13.36	≤23.98	PASS
	Ant2	5190	12.40	≤23.98	PASS
	Ant1	5230	13.89	≤23.98	PASS
	Ant2	5230	12.27	≤23.98	PASS
	Ant1	5755	11.12	≤30	PASS
	Ant2	5755	11.02	≤30	PASS
	Ant1	5795	10.34	≤30	PASS
	Ant2	5795	10.41	≤30	PASS
11AC80SISO	Ant1	5210	13.78	≤23.98	PASS
	Ant2	5210	11.08	≤23.98	PASS
	Ant1	5775	12.56	≤30	PASS
	Ant2	5775	12.54	≤30	PASS

Note: 1. The Duty Cycle Factor was compensated in the spectrum analyzer Ref offset.

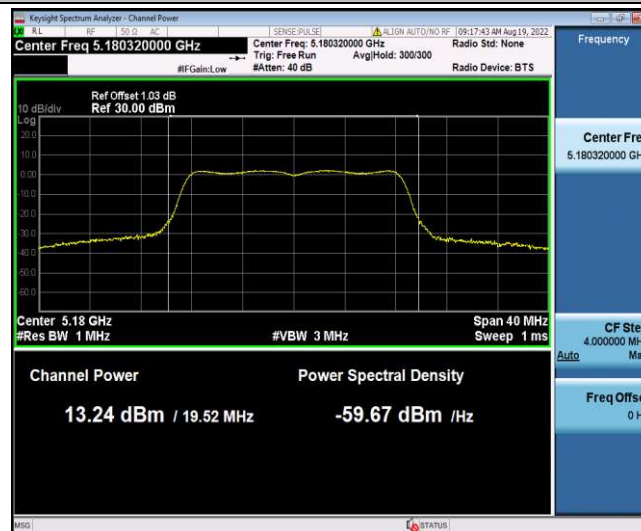
2. Spectrum analyzer Ref offset=Cable Loss(1dB) + Duty Cycle Factor.

3. Duty Cycle Factor=10Log(1/X), where X is the Duty Cycle.

Test Graphs



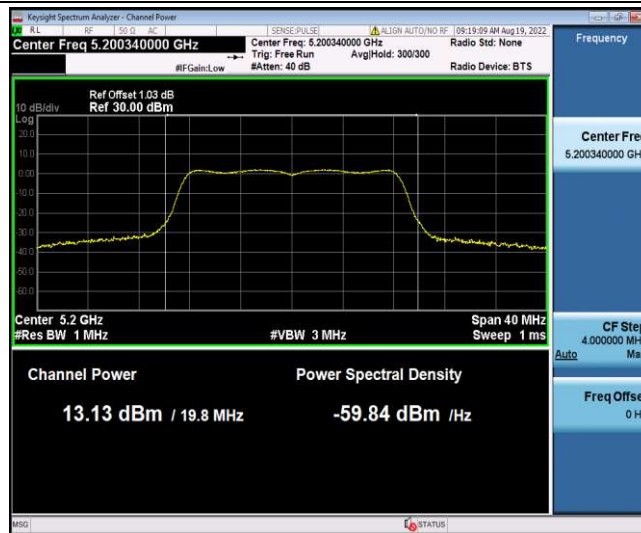
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11A_Ant2_5180



11A_Ant1_5200



11A_Ant2_5200



11A_Ant1_5240



11A_Ant2_5240



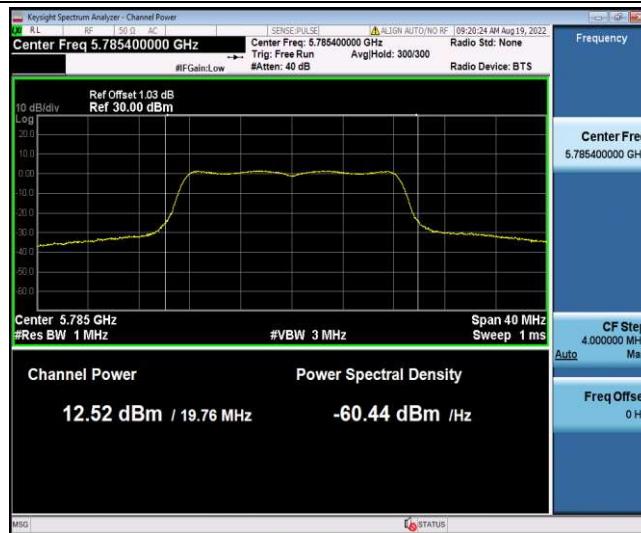
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11A_Ant2_5745



11A_Ant1_5785



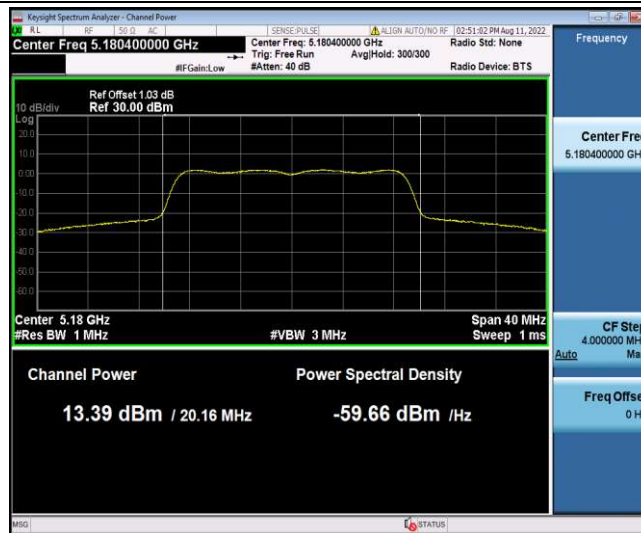
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11A_Ant1_5825



11A_Ant2_5825



11N20SISO_Ant1_5180



11N20SISO_Ant2_5180



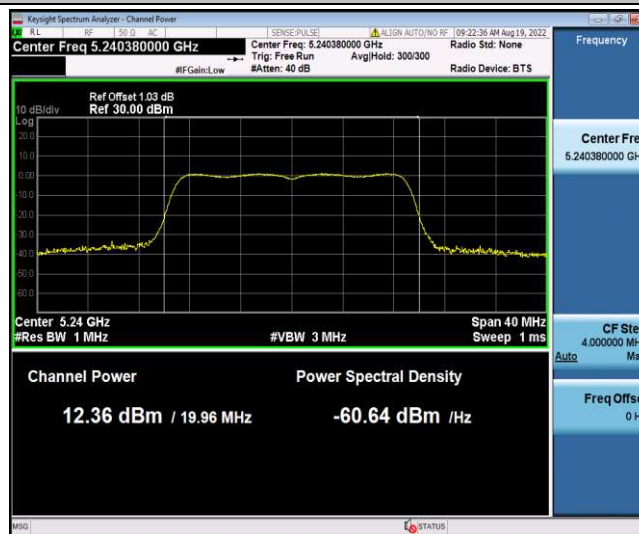
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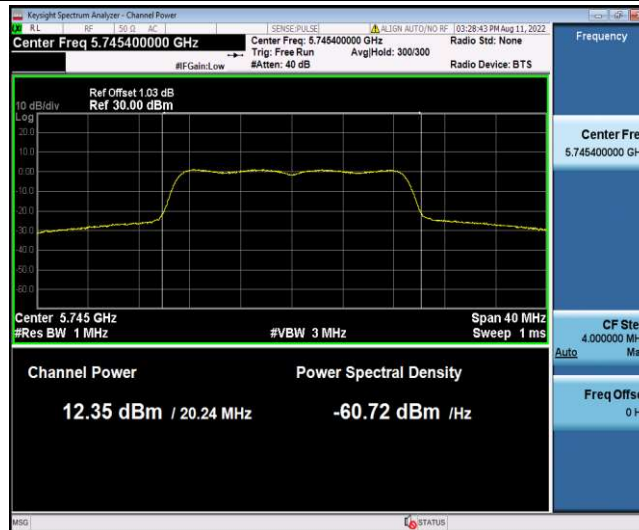
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11N20SISO_Ant1_5745



11N20SISO_Ant2_5745



11N20SISO_Ant1_5785



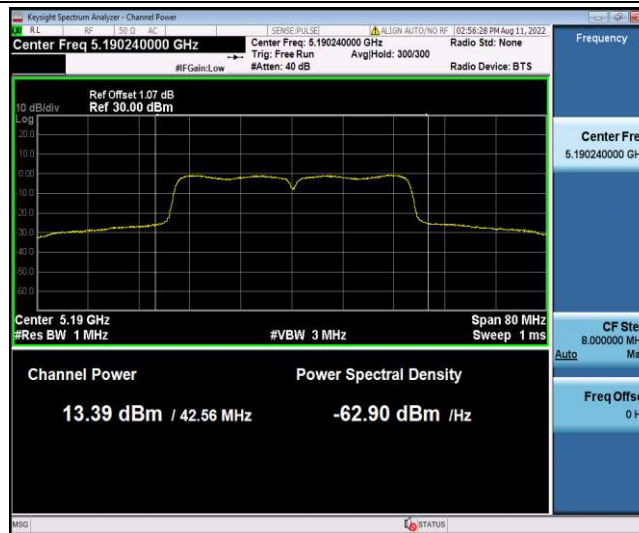
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11N20SISO_Ant1_5825



11N20SISO_Ant2_5825



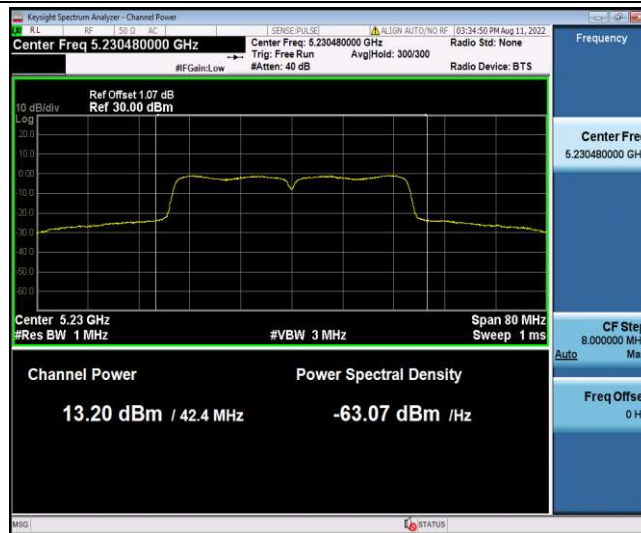
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11N40SISO_Ant2_5190



11N40SISO_Ant1_5230



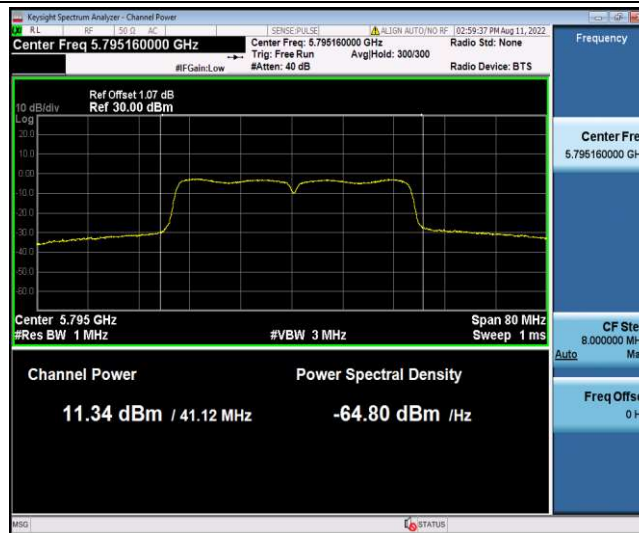
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11N40SISO_Ant1_5755



11N40SISO_Ant2_5755



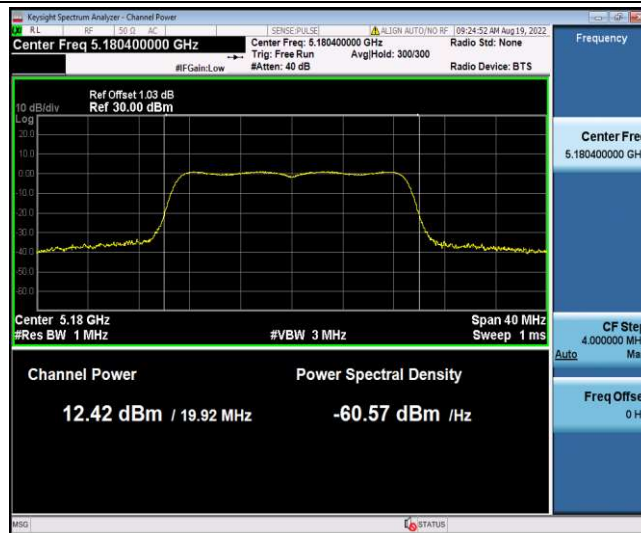
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11N40SISO_Ant2_5795



11AC20SISO_Ant1_5180



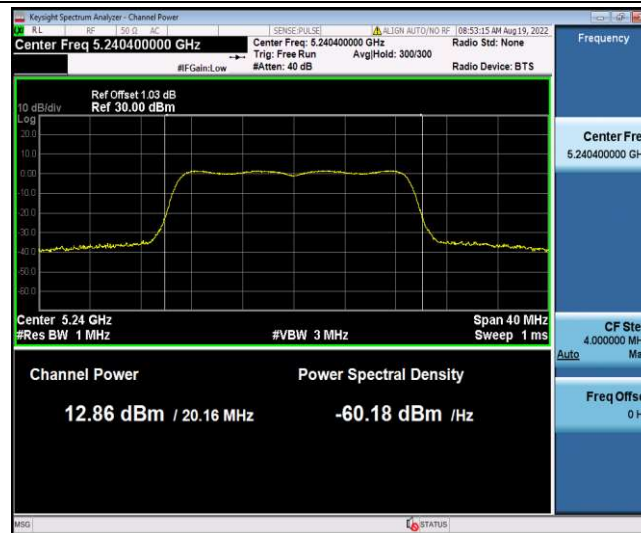
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11AC20SISO_Ant1_5200



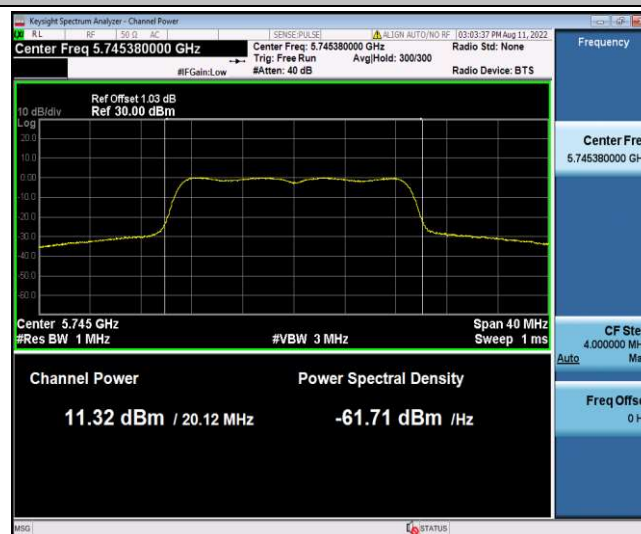
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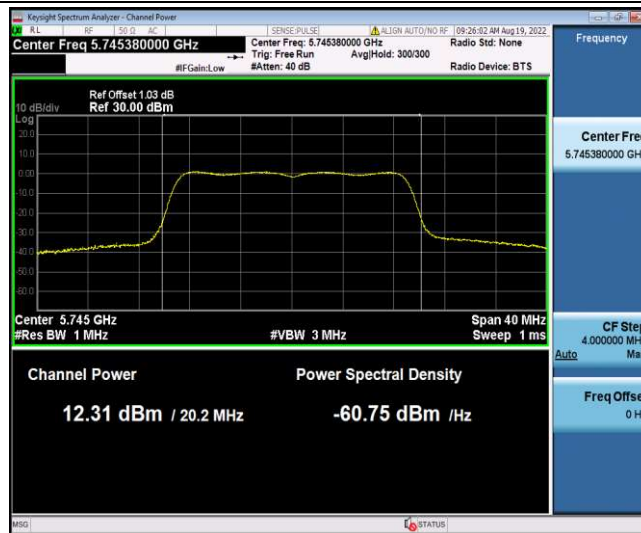
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11AC20SISO_Ant2_5240



11AC20SISO_Ant1_5745



11AC20SISO_Ant2_5745



11AC20SISO_Ant1_5785



11AC20SISO_Ant2_5785



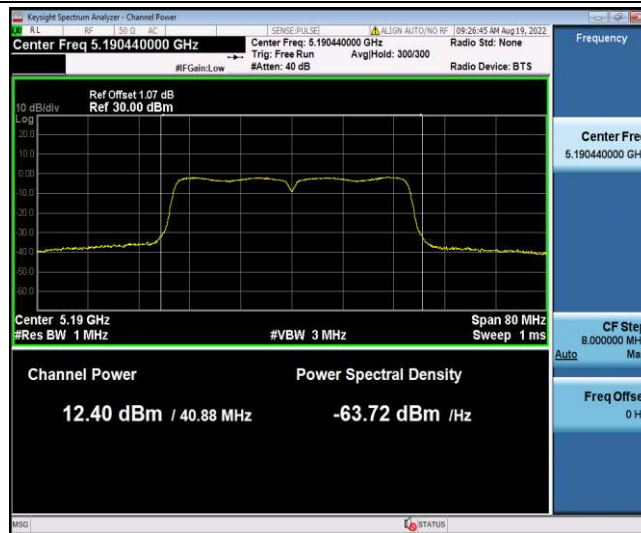
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11AC20SISO_Ant2_5825



11AC40SISO_Ant1_5190



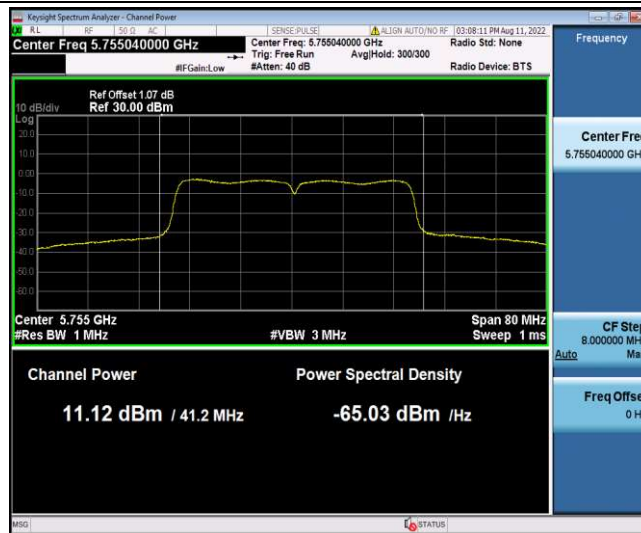
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11AC40SISO_Ant1_5230



11AC40SISO_Ant2_5230



11AC40SISO_Ant1_5755



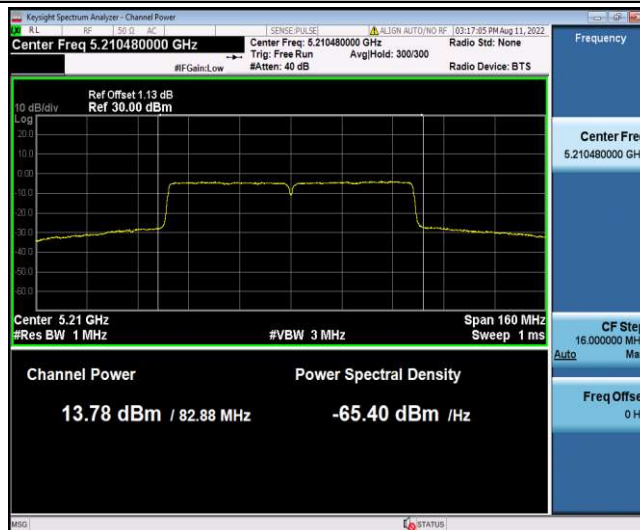
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11AC40SISO_Ant1_5795



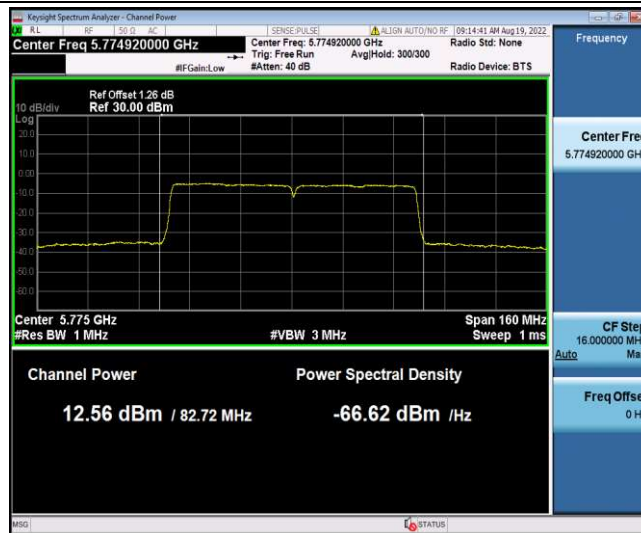
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11AC80SISO_Ant1_5210



11AC80SISO_Ant2_5210



11AC80SISO_Ant1_5775



11AC80SISO_Ant2_5775

Appendix D: Maximum power spectral density

Test Result

TestMode	Antenna	Channel	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant1	5180	2.3	≤11	PASS
	Ant2	5180	2.37	≤11	PASS
	Ant1	5200	2.55	≤11	PASS
	Ant2	5200	2.81	≤11	PASS
	Ant1	5240	2.85	≤11	PASS
	Ant2	5240	3.08	≤11	PASS
	Ant1	5745	-1.28	≤30	PASS
	Ant2	5745	-1.39	≤30	PASS
	Ant1	5785	-2.41	≤30	PASS
	Ant2	5785	-2.08	≤30	PASS
	Ant1	5825	-2.76	≤30	PASS
	Ant2	5825	-2.13	≤30	PASS
11N20SISO	Ant1	5180	2.2	≤11	PASS
	Ant2	5180	1.54	≤11	PASS
	Ant1	5200	2.59	≤11	PASS
	Ant2	5200	1.8	≤11	PASS
	Ant1	5240	2.69	≤11	PASS
	Ant2	5240	2.48	≤11	PASS
	Ant1	5745	-1.35	≤30	PASS
	Ant2	5745	-1.37	≤30	PASS
	Ant1	5785	-2.44	≤30	PASS
	Ant2	5785	-2	≤30	PASS
	Ant1	5825	-2.78	≤30	PASS
	Ant2	5825	-1.98	≤30	PASS
11N40SISO	Ant1	5190	-0.51	≤11	PASS
	Ant2	5190	-1.2	≤11	PASS
	Ant1	5230	0.08	≤11	PASS
	Ant2	5230	-0.77	≤11	PASS
	Ant1	5755	-4.34	≤30	PASS
	Ant2	5755	-4.36	≤30	PASS
	Ant1	5795	-5.23	≤30	PASS
	Ant2	5795	-5.05	≤30	PASS
11AC20SISO	Ant1	5180	2.4	≤11	PASS
	Ant2	5180	1.58	≤11	PASS
	Ant1	5200	2.67	≤11	PASS
	Ant2	5200	1.84	≤11	PASS
	Ant1	5240	2.72	≤11	PASS
	Ant2	5240	2.31	≤11	PASS

	Ant1	5745	-2.37	≤30	PASS
	Ant2	5745	-2.64	≤30	PASS
	Ant1	5785	-3.3	≤30	PASS
	Ant2	5785	-3.22	≤30	PASS
	Ant1	5825	-3.39	≤30	PASS
	Ant2	5825	-3.46	≤30	PASS
11AC40SISO	Ant1	5190	-0.62	≤11	PASS
	Ant2	5190	-1.24	≤11	PASS
	Ant1	5230	-0.02	≤11	PASS
	Ant2	5230	-0.85	≤11	PASS
	Ant1	5755	-5.26	≤30	PASS
	Ant2	5755	-5.29	≤30	PASS
	Ant1	5795	-6.34	≤30	PASS
	Ant2	5795	-6.22	≤30	PASS
11AC80SISO	Ant1	5210	-3.69	≤11	PASS
	Ant2	5210	-4.5	≤11	PASS
	Ant1	5775	-8.27	≤30	PASS
	Ant2	5775	-8.58	≤30	PASS

Note: 1. The Duty Cycle Factor was compensated in the spectrum analyzer Ref offset.

2. Spectrum analyzer Ref offset=Cable Loss(1dB) + Duty Cycle Factor.

3. Duty Cycle Factor=10Log(1/X), where X is the Duty Cycle.

Test Graphs



11A_Ant1_5180



11A_Ant2_5180



11A_Ant1_5200



11A_Ant2_5200



11A_Ant1_5240



11A_Ant2_5240



11A_Ant1_5745



11A_Ant2_5745



11A_Ant1_5785



11A_Ant2_5785



11A_Ant1_5825



11A_Ant2_5825



11N20SISO_Ant1_5180



11N20SISO_Ant2_5180



11N20SISO_Ant1_5200



11N20SISO_Ant2_5200



11N20SISO_Ant1_5240



11N20SISO_Ant2_5240



11N20SISO_Ant1_5745



11N20SISO_Ant2_5745



11N20SISO_Ant1_5785



11N20SISO_Ant2_5785



11N20SISO_Ant1_5825



11N20SISO_Ant2_5825



11N40SISO_Ant1_5190



11N40SISO_Ant2_5190



11N40SISO_Ant1_5230



11N40SISO_Ant2_5230



11N40SISO_Ant1_5755



11N40SISO_Ant2_5755