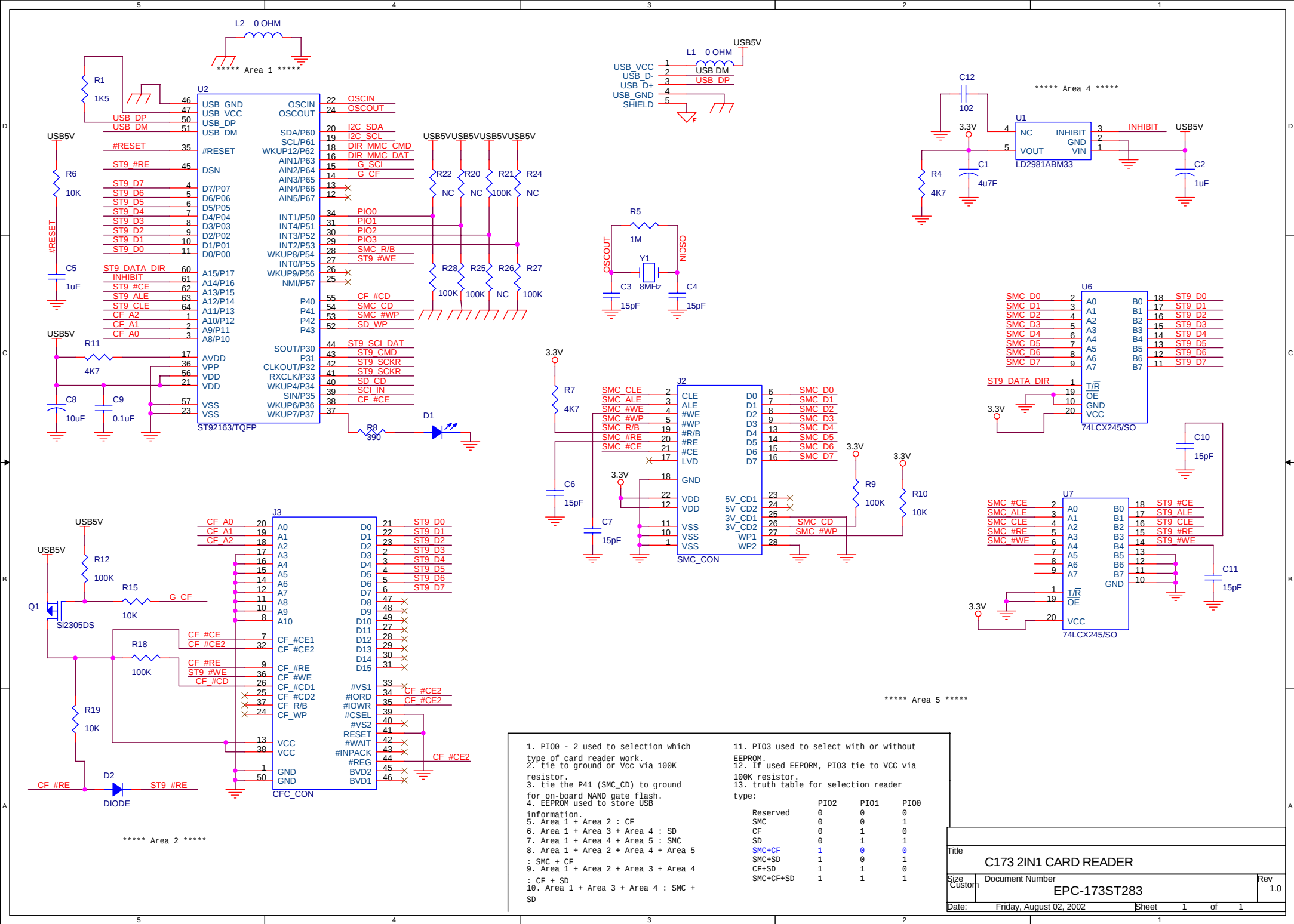
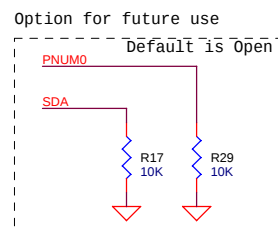
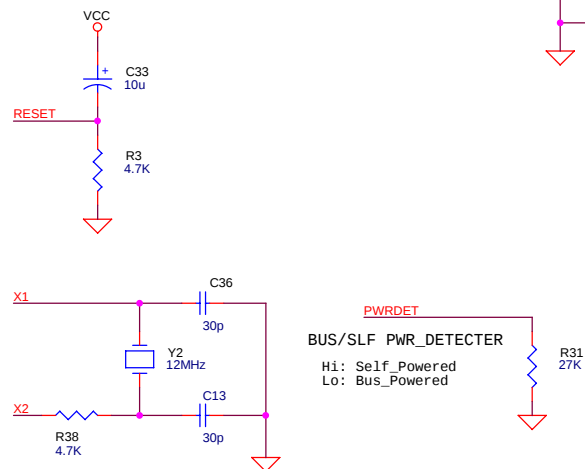
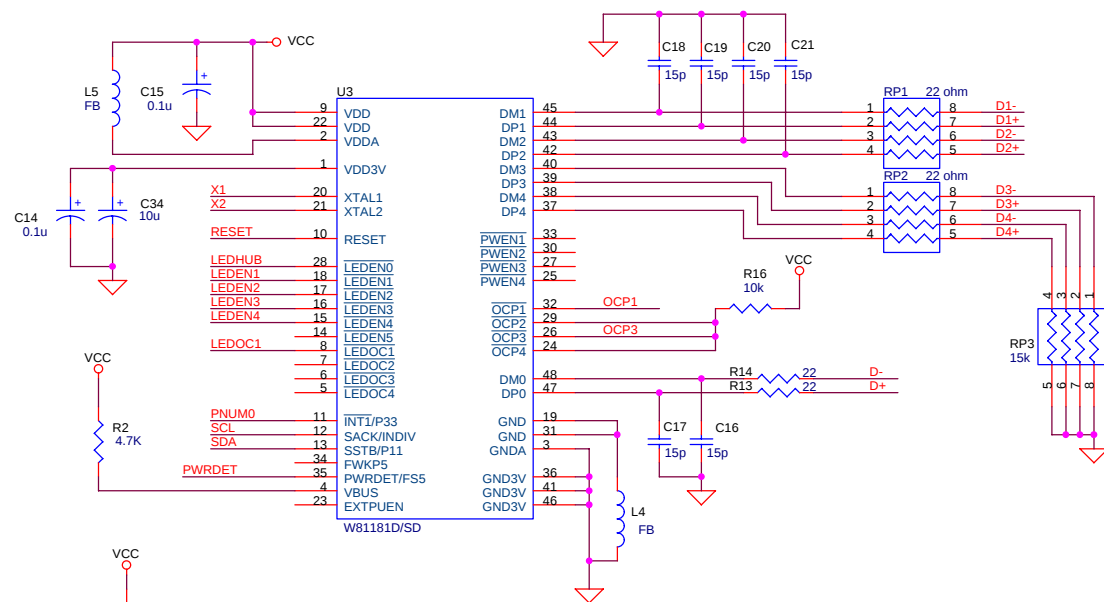




1. PIO0 - 2 used to selection which type of card reader work.			
2. tie to ground or Vcc via 100K resistor.			
3. tie the P41 (SMC_CD) to ground for on-board NAND gate flash.			
4. EEPROM used to store USB information.			
5. Area 1 + Area 2 : CF			
6. Area 1 + Area 3 + Area 4 : SD			
7. Area 1 + Area 4 + Area 5 : SMC			
8. Area 1 + Area 2 + Area 4 + Area 5 : SMC+CF			
9. Area 1 + Area 2 + Area 3 + Area 4 : CF + SD			
10. Area 1 + Area 3 + Area 4 : SMC + SD			
11. PIO3 used to select with or without EEPROM.			
12. If used EEPROM, PIO3 tie to VCC via 100K resistor.			
13. truth table for selection reader type:			
	PIO2	PIO1	PIO0
Reserved	0	0	0
SMC	0	0	1
CF	0	1	0
SD	0	1	1
SMC+CF	1	0	0
SMC+SD	1	0	1
CF+SD	1	1	0
SMC+CF+SD	1	1	1

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C173 2IN1 CARD READER		
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W81181D/SD USB HUB Reference Schematic (Use Poly_Switch)

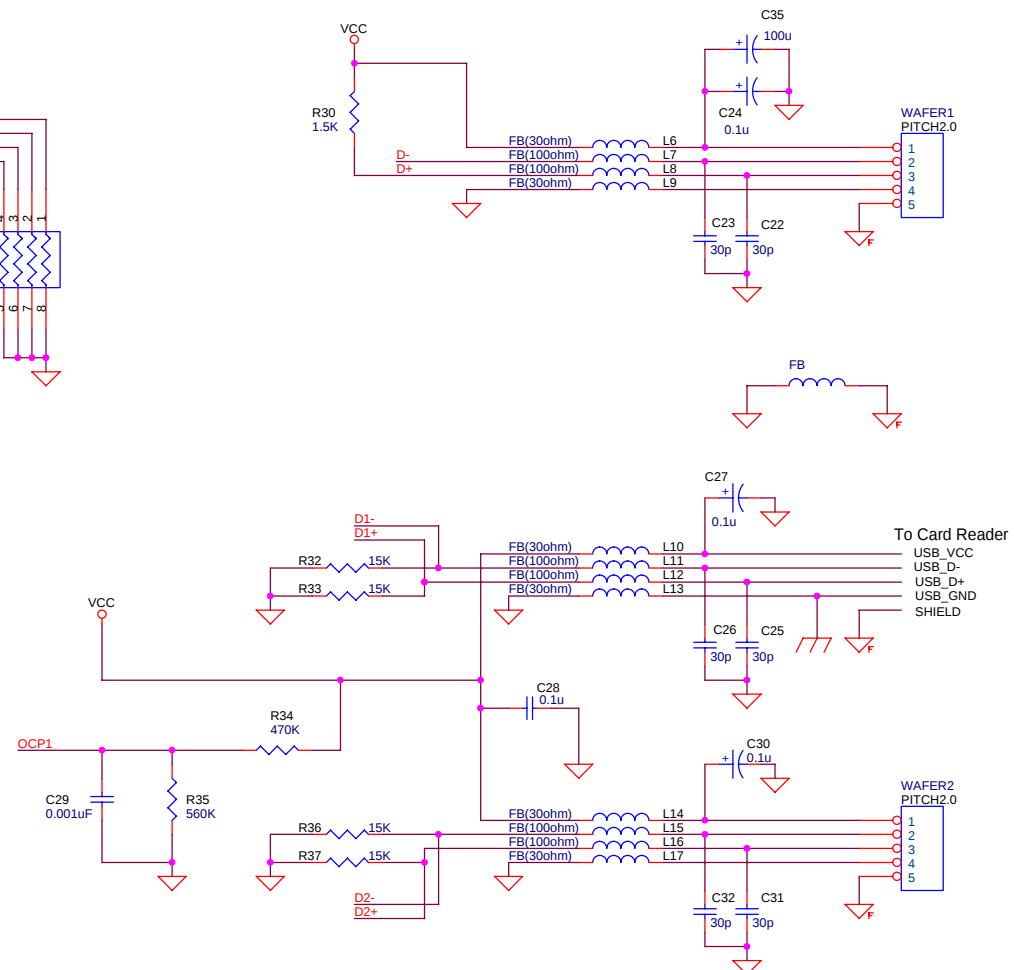


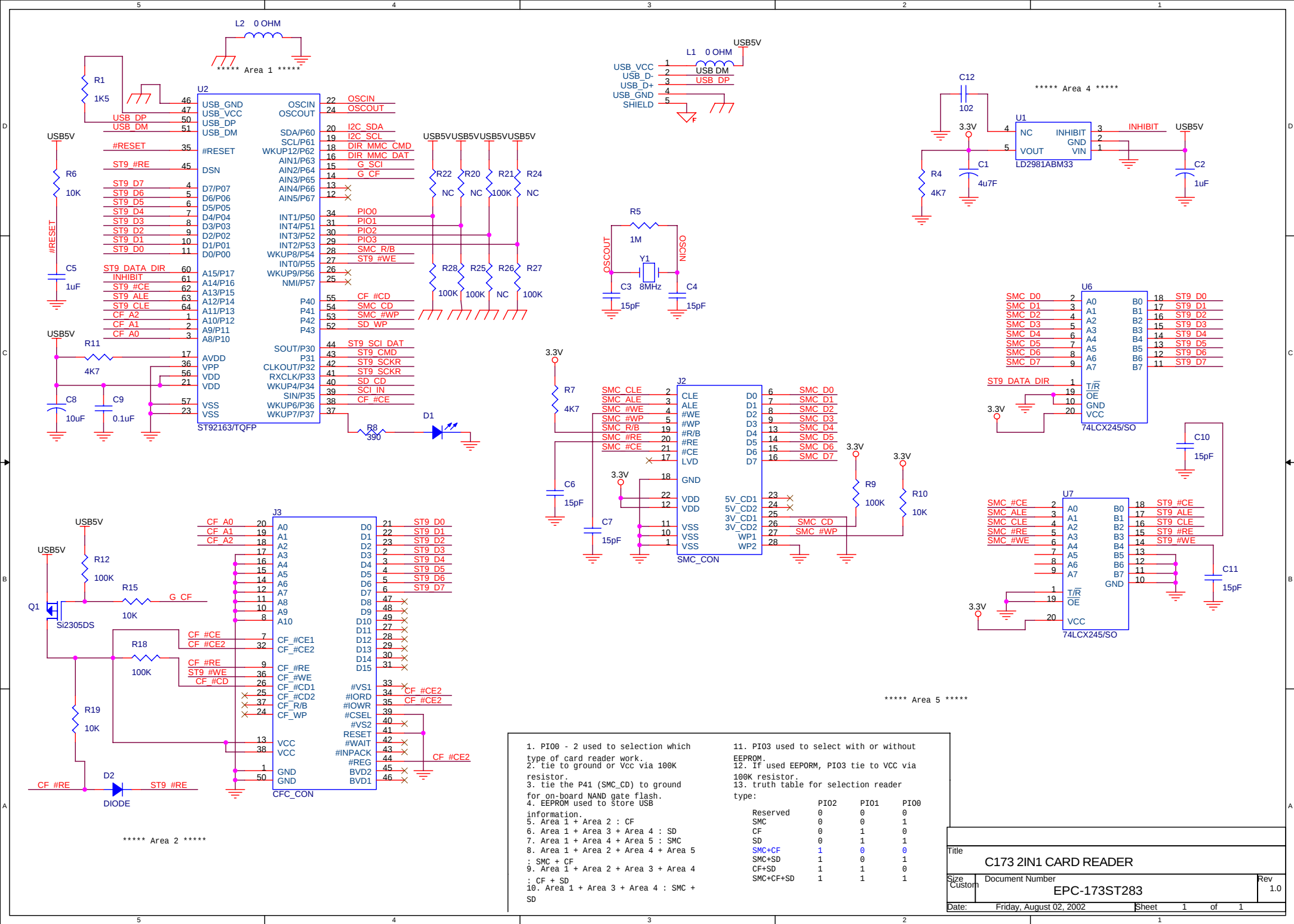
NOTES :

USE PORT	R17	R29
1 PORT	10K	10K
2 PORT	10K	X
3 PORT	X	10K
4 PORT	X	X

Check Point for Layout:

- a. All of down_stream ports need to be pull-down R(15K) to GND even the port is unused.
- b. Pin 1 VDD3V needs to be connected a C(10uF) to GND only. Do not use to drive any other circuit.
- c. Pin 4, 24, 26, 29, 32 & 35 need to add pull-up resistor to VCC when they are unused.
- d. All of LED pins can be floating if they are unused.





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SMC+SD	1	0	1
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SMC+CF+SD	1	1	1

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