Battery Operations

The right bay battery is the primary battery source and is identified as battery number 1. The left bay battery is the secondary battery source identified as battery number 2. Using two or more batteries will allow you to swap charged batteries into the module bays for extended portable operation.

Single battery operation

The ReVolution running on a single battery will deplete battery charge until an alarm level is met then trigger the action set for that alarm. Set the alarm level and action in Power Options in Windows Control Panel.

Stand-by battery operation

When running the ReVolution with two charged batteries, the secondary battery will be depleted first. The primary battery will power the ReVolution when the secondary battery is empty, keeping the ReVolution running as a stand-by battery. Replace the depleted secondary battery with a charged battery to resume powering the ReVolution on the secondary battery.

Alternate battery operation

Place a battery in either bay and run the ReVolution until a low battery message is displayed. Place a charged battery in the remaining open bay to continue operation and remove the discharged battery.

Touchscreen

Your ReVolution is designed with a resistive touch screen that acts as a mouse pointer. Use the provided stylus for best results.



User Settings

Multiple behavior settings are available for the touchscreen. Your preferences may differ from the default settings for interacting with screen objects. You can change these settings by:

- Running the touchscreen property sheet from the Start-Programs-UPDD-Settings menu,
- Clicking on "Pointer Device Settings" in the system tray, or
- Running "Pointer Devices" in Windows Control Panel. The following screen will appear.

🖪 Pointer Device Properties	×
Devices Hardware Settings Advanced Events General Windows Calibration Status About	
	— I I
The following devices are installed:	
# Device Segment Priority Controller	
01 Device 1 Whole Desktop Interlock eTurboTouch, T4/T6/Turbo Pen, USB (Pid	
	_
Add	
Modifu	7
	-
Bemove	
	-
	'
Calibrate Test OK Cancel Apply H	Help

- Navigate the Settings and Advanced tabs to adjust click responsiveness.
- Select the Events tab to program the mouse event that executes when touching the screen.
- The Windows tab provides double-click settings and a test area to try your new settings.
- Click the **Help** button if you need more information.

Driver Installation

The driver for your touchscreen is loaded at the factory when you order a Microsoft Windows operating system. If you need to reload the driver, use the ReVolution Utilities CD included with your unit.

Calibrate

The ReVolution touchscreen is calibrated at the factory. Run the calibration routine when an alignment problem exists between the mouse pointer and the stylus contact location on the screen. You can adjust the calibration of the touchscreen by running the program at **Start-Programs-UPDD-Calibrate**. Carefully touch the location of the markers with your stylus to recalibrate the touch screen.



After calibrating the ReVolution touchscreen you can test the alignment by pressing the **Test** button to view the tracking accuracy.

Touch Pad

The built-in touch pad is a PS/2-compatible pointing device that senses movement on its surface; the cursor responds as you move your finger on the surface of the touch pad. The central location on the palm rest provides optimal comfort and support.

Touch Pad Basics:

- 1. Move your finger across the touch pad to move the cursor.
- 2. Press the left and right buttons located on the bottom edge of the touch pad to select and execute. These two buttons are similar to the left and right buttons on a mouse. Tapping on the touch pad produces similar results.

Function	Left Button	Right Button	Тар
Execute	Click twice quickly		Tap twice (at same speed
			as double-clicking a mouse
			button)
Select	Click once		Tap once
Drag	Click and hold, then use finger to drag the cursor on the touch pad		Tap twice (at same speed as double-clicking a mouse button), then hold finger to the touch pad on the second tap and drag the cursor.
Access Content Menu		Click once	
Scroll			

Note: Keep your fingers clean and dry when using the touch pad. Also keep the touch pad dry and clean. The touch pad is sensitive to finger movements. Hence, the lighter the touch, the better the response. Tapping harder will not increase the touch pad's responsiveness.

Change the behavior of the touch pad by adjusting the Mouse Properties in the Microsoft Windows Control Panel, displayed on the following page. For additional touch pad control, load the touch pad driver found on the ReVolution Utilities CD included with your unit.

Special touch pad features allow greater control of touch pad usage. Familiarize yourself with these features on this screen.

Mouse Properties		
O Scrolling O Tap Zones O More Features O Button Actions Buttons Pointers Motion Hardware O Touch O Edge Motion		
Synaptics TouchPad Properties - Version 6.0.20 260ct01 Synaptics TouchPad V4.1 on PS/2 Port		
Edge Motion		
✓ When Dragging □ Always		
When Scrolling		
Edge <u>M</u> otion Speed (9)		
Slow Fast		
Defaults www.synaptics.com		
OK Cancel Apply Help		

USB Ports

Driver Installation

The ReVolution is designed with a USB 2.0 compliant controller that requires an operating system driver to exploit its capabilities. The driver is preinstalled at the factory and a backup of the driver is on the ReVolution Utilities CD delivered with your system.

Video

The ReVolution is designed with the Intel i830M grapics and memory ccontroler (GMCH). You can control the video system through the Microsoft Windows Control Panel. You can also gain quick access to video options by clicking the Intel Graphics Technology icon in the system tray or by right clicking on the desktop, as follows.

8 A.	l
	L
1 C	Ľ,

A COMPANY AND A	
Save As Scheme	
Graphics Options	Panel Fit 🔹 🕨
Display Modes 🔹 🕨	Hot Keys 🔹 🕨
Active Desktop	Tray Icon Graphics Properties
Arrange Icons	many a committee of
Line Up Icons	1220 B CONDERCE
Refresh	
Paste Paste Shortcut	
New 🕨	
Properties	-
	Revi
Rotate	
Rotate to 0	1
Rotate to 90	
Rotate to 180	
Rotate to 270	

The Graphics Controller property sheet through the Windows Control Panel allows you view and change device color depth, resolution, color correction, display schemes, and hot keys.

Intel(R) 82830M Graphics Controller Properties			? ×
Devices Color	Schemes Hot Keys Settings Colors Screen Area	Information	Y
	F	full Screen (No Border)	
		OK Cancel	Apply

Driver Installation

Browse the ReVolution Utilities CD to locate the video driver installation. Intel frequently updates the graphics drivers for i830M. Download this driver from <u>www.intel.com</u>.

Video Rotation

The ReVolution can change the orientation of the display image in four degrees of rotation. Rotating the screen can be useful when running the ReVolution in tablet mode, to suit your workspace. The normal orientation when in laptop mode is zero degrees. You may want to switch to 90 or 270 for a "Portarit" display or simply flip the screen by setting 180 rotation. There is three ways to change screen rotation. Use the system tray icon, right click on the desktop, or use the hot keys.

Hot Key Rotation:

To rotate your screen's image press <Control> + <Shift> and hit the <R> key. Rotate to 0 press <Control> + <Shift> and hit the <0> key. Rotate to 90 press <Control> + <Shift> and hit the <9> key. Rotate to 180 press <Control> + <Shift> and hit the <8> key. Rotate to 270 press <Control> + <Shift> and hit the <7> key.

Task Bar Rotation



Desktop Rotation:



Wireless - 802.11b (WLAN)



Do not operate the ReVolution in areas sensitive to radio interference, such as airplanes and hospitals.

There is no means to shut off the Bluetooth and WLAN radios installed in the system.

The integrated Wireless LAN (WLAN) device in the ReVolution is an 802.11b wireless network card that is attached to the computer via a mini-PCI slot.

The main characteristics include:

- Operating frequency in the 2.4 GHz band.
- Compliance with WECA Wireless Fidelity (Wi-Fi) testing standard and ability to communicate up the maximum transfer rate of 11 Mbps.
- Maximum range of about 105 Meters.

This device provides a plug-and-play seamless connectivity to all network resources, and Internet access at up to 11 Mbps. No cables are necessary to run, just an access point. It provides a high-speed connectivity at up to 11 Mbps over an extended operating range. It automatically falls back to 5.5, 2, and 1 Mbps. It is compliant with IEEE 802.11b standards which also assures compatibility with other 802.11b compliant devices and networks.

Frequency Range:	2.4 GHz to 2.4835 GHz
Typical outdoor operating range	30 M @ 11 Mbps
	50 M @ 5.5 Mbps
	100 M @ 2 Mbps
	105 M @ 1 Mbps
Modulation Technique:	DSSS(Direct Sequence Spread Spectrum) with BPSK
	(1Mbps), QPSK (2Mbps), and CKK(5.5 and 11 Mbps)
Channel Support	US/Canada: 11 (1 ~ 11)
	Major European Country: 13(1 ~ 13)
	France: 4(10~13)
	Japan: 14(1 ~ 13 or 14 th)

Driver

The software for the WLAN allows you to view and set the connection to other 802.11b devices. Run this utility by navigating to Start-Programs-IEEE 802.11b MiniPCI Utility in Windows 2000 or Windows 98. The following screen will appear. When running Windows XP go to Device Manager.

IEEE 802.11b WLAN Utility		_ I X
Link Information	Diagnostic Setting	About Site Survey
Access Point SSID	Access Point MAC 00:30:BD:62:43:C0	Rescan
Signal Strength 90 %		
Signal Quality		100 %

To secure transmissions with an 802.11b device that has WEP enabled, set a pass phrase or key on the Advanced Setting window.

IEEE 802.11b WLAN Utility	Advanced Setting
Link Information Diagnostic About LAN Status Setting Site Survey Computer Name PREINSTAL-MERCB Profiles SSID SSID Infrastructure	Transmission Rate Power Saving Mode Fully Auto Enable Disable WEP Required Mode Pass Phrase ASCII 64 bits Key1 Key2
Channel Range 1 ~ 11 6	C Key3 C Key4 Apply Cancel

Wireless - Bluetooth



Do not operate the ReVolution in areas sensitive to radio interference, such as airplanes and hospitals. There is no means to shut off the Bluetooth and WLAN radios installed in the system.

For PAN environments, Bluetooth provides freedom from wired connections. By using this radio-based link, computers, mobile phones, PDA and other portable handheld devices are able to transmit data to each other or connect to Internet without a single cable.

While the possibilities are nearly endless for the applications of the technology, some of the current capabilities include:

- Eliminating the need for wired connections between electronic products and accessories such as a keyboard, mouse, headsets, printers, other computers;
- Exchanging files, business cards, calendar appointments, etc. with groups of Bluetooth users;
- Transferring and synchronizing files between devices;
- Connecting to localized content services in public areas;
- Functioning as remote controls, keys, tickets and e-cash wallets.

The Bluetooth wireless technology and Wireless LAN (802.11b) are complementary technologies. The Bluetooth wireless technology is designed to replace cables between cell phones, laptops, and other computing and communication devices within a 10-meter range. Wireless LAN is wireless Ethernet; it provides an extension or replacement of wired networks for dozens of computing devices.

Issues do arise with the coexistence of both Bluetooth and Wireless LAN. Any time devices are operated in the same frequency band, there is the potential for interference which results in a lower bandwidth

This device has a maximum range of about 10 meters, depending on the environment. The maximum data transfer rate is 1.0Mbps. This is a Class 2 device with a typical Tx power of 0 dBm. The operating frequency range is 2.402 GHz to 2.480 GHz with a channel spacing of 1 MHz.

Technical Data

ReVolution

Main Specification

Processor	Intel Mobile Pentium® III-M 1.06 GHz or higher
Main Battery	Lithium Ion battery - 57 Watt, 3.5 hour life (approximate)
BIOS:	PhoenixBIOS 4.0, Release 6
RAM:	128 MB SDRAM standard
L2-Cache	512KB
Keyboard:	Full-size QWERTY keyboard with 84 keys (USA Standard)
Interfaces:	1x Power Connector with 10-28 VDC, 4.6 Amps 1x Serial Port (COM 1) 2x USB 2.0 Ports Docking connector 1x RJ11 56K V.90 Modem 1x RJ45 10/100Mbs Ethernet/LAN Audio phonejacks: 1x MIC-In 1x Line-In 1x Speaker-Out Integrated: 802.11b WLAN, Bluetooth PAN
Hard Disk Drives:	 40GB primary removable hard drive standard – upgradeable to 60GB 40GB secondary, optional removable hard drive – upgradeable to 60MB Call Technical Support or your Sales Representative for other available options.
PCMCIA:	Card BUS connector provides two open PC Card slots that accommodate two Type II, or one Type III PCMCIA device.
Graphics Controller:	Integrated Direct AGP Graphics Core frequency up to 166 MHz 350 MHz RAM DAC Video-RAM: 8 to 48 MB of Dynamic Video Memory (shared) Up to 32 MB with 128 MB RAM; 48 MB with 256 MB RAM
Pointing Device:	Touch Pad on the keyboard, Touchscreen on the display
Power specification	10-24 VDC
AC adapter:	Input: 90-264 VAC, 50-60 Hz; Output: 12V, 55W
RTC/CMOS Battery	Rechargeable Llon real-time clock/CMOS battery on main board. Not user-serviceable.

Mechanical Specification

Dimensions:	
Width	12.8" (326 mm)
Depth	10.25" (261 mm)
Height	2.125" (54 mm)
Weight:	8 lbs. with one battery module installed.
Chassis:	Rigid, lightweight magnesium design

Environmental Specifications

Operating temperature	–15 C to +50°C (5 F to 140 °F)
	NOTE: Excludes all CD-ROM, DVD and floppy devices. See device specifications later in this section.
Storage temperature	–20 C to +70 °C (–4 F to 158 °F)
	NOTE: Excludes all CD-ROM, DVD and floppy devices. See device specifications later in this section.
Operating humidity	10–88 % relative humidity, non-condensing
Storage humidity	5–95 % relative humidity, non condensing
Shock	Mil-Std 810F: Method: 516.5 Procedures: I = 40G IV = 26 drops @ 36" V = 75G NOTE: Excludes all CD-ROM, DVD and floppy
	devices. See device specifications later in this section.
Vibration	Mil-Std 810F: Method: 514.5 Procedure I Category 20 & 24
Blowing Rain	IP54/NEMA 3
Sand and Dust	NEMA 3
Altitude Operating	Up to 15,000 ft. (4,500 m)
Altitude Non-Operating	Up to 35,000 ft. (10,668 m)
EMI & Safety	FCC Part 15 Class B (ETSI 300 328/1997) CE Directive Class B (ETSI 301489-17/2002) UL, EN 60950

CE-Directives and Standards

CE –Directives		
Low Voltage directive (Electrical Safety)	EN 60950	
EMC Directive	EN 55022	

Electrical Safety	Standards	
U.S.A.	UL 60950/2002	
Canada	CSA 22.2 No. 60950-00	

EMC	Standards
U.S.A.	FCC Part 15.247/2002

CD-ROM Module

Interface	IDE
Temperature range	in operation: 5°C to +50°C in storage: -30°C to +65°C
Humidity	5% - 90% (relative, non-condensing)
Features	Read Data 24X CAV max. Audio Play 8X CAV max.

CD-RW Module

Interface	IDE
Temperature range	in operation: 5°C to +50°C in storage: -20°C to +60°C
Humidity	10% - 80% (relative, non-condensing)
Features	Write 8X max. Read 24X max.

DVD Module

Interface	IDE
Temperature range	in operation: 5°C to +50°C in storage: -30°C to +65°C
Humidity	5% - 90% (relative, non-condensing)
Features	DVD-ROM 24X CAV max. CD 8X CAV max.

DVD/CD-RW Module

Interface	IDE
Temperature range	in operation: 5°C to +50°C in storage: -20°C to +60°C
Humidity	10% - 80% (relative, non-condensing)
Features	Read: DVD-ROM 8X CAV max. CD-ROM 24X CAV max. Write: CD-R 8X CLV CD-RW 4X CLV High Speed CD-RW 8X CLV

Floppy Disk Drive Module

Interface	TTL
Temperature range	in operation: 5°C to +50°C in storage: -40°C to +60°C
Humidity	20% - 80% (relative, non-condensing)
FDD:	3.5" 1.44Mb Formatted

Lithium Ion Battery Pack

Interface	SMBUS VI.0	
Temperature range	Operating: 0° to +45°C charging -20° to +60°C discharging	
Service Life	300 Cycles typ.	
Typical Capacity	8000mAH@.2C	
Nominal Voltage	7.40V	
Remaining Capacity LEDs:		
Green (Three)	76-100% remaining	
Green (Two)	51-75% remaining	
Green (One)	26-50% remaining	
Red	0-25%	

Technical Appendices

The following tables show the connector pin-out assignments for the external connections of the ReVolution computer. Active low signals are indicated by a minus sign. Refer to the "ReVolution at a Glance" section for locations.

RS232 Serial Port (COM A)

Pin	Si	gnal name	9-pin SUB D-plug
1	DCD	(Data Carrier Detect)	
2	RXD	(Receive Data)	\bigcup
3	TXD	(Transmit Data)	
4	DTR	(Data Terminal Ready)	5 9
5	GND	(Signal Ground)	
6	DSR	(Data Set Ready)	1 ● ● 6
7	RTS	(Request to Send)	
8	CTS	(Clear to Send)	\bigcirc
9	RI	(Ring Indicator)	

Parallel Port (I/O Stick Option Only)



Analog Monitor (VGA-Output, I/O Stick Option Only)

Pin	Signal name	15-pin SUB D-socket
1	red	\bigcirc
2	green	
3	blue	
4, 5	4 is NC, 5 is GND	1-000-11
6–8	GND	
9	not connected	5-0-15
10–12	10-GND, 11-NC, 12-DDC Data	10
13	HSYNC	
14	VSYNC	
15	DDC Clock	

USB 2.0 Ports

Pin	Signal name	USB
1	+5 Volts	
2	P-	
3	P+	
4	GND	USB PIN 1 PIN 4

Power Supply Connector

Signal name	4-pin Female socket
	(Looking into socket on back interface panel))
Ground	
Ground	
DC Input(+10 to +28 Volts DC)	
DC Input(+10 to +28 Volts DC)	3 • • 2
	Signal name Ground Ground DC Input(+10 to +28 Volts DC) DC Input(+10 to +28 Volts DC)

PS/2 Keyboard/Mouse Connector (I/O Stick Option Only)

Pin	Name) Dir.	Description	
1	DATA	+	Mouse Data	6 5
2	DATA	+	Keyboard Data	4 (3 3
3	GND		Ground	
4	VCC	1	Power, +5 VDC	
5	CLK	1	Mouse Clock	_
6	CLK	+	Keyboard Clock	-

This illustrates the pin-out of the external PS/2 Keyboard/Mouse connector. This connection is "hot pluggable" and interchangeable between the external keyboard and the external mouse. Hot pluggable means you may connect either the external keyboard or external mouse while the computer power is on.

RJ-11 Modem Port

Pin	Name	
1	Not Connected	
2	TIP	
3	RING	
4	Not Connected	

RJ-45 LAN Port

Pin	Name	Description	RJ-45 LAN Port
1	TX+	Transmit Data+	18
2	TX-	Transmit Data-	
3	RX+	Receive Data+	
4	Not Connected	Pin 4 is shorted to Pin 5 then AC coupled to ground through a 75 Ohm resistor	
5	Not Connected	See Above	
6	RX-	Receive Data-	
7	Not Connected	Pin 7 is shorted to Pin 8 then AC coupled to ground through a 75 Ohm resistor	
8	Not Connected	See Above	

Line-In



MIC-In



Speakers



System Hardware Assignments

The ReVolution, like all computers based on standard IBM-compatible personal computer architecture, contains a set of user hardware- and software-configurable resources. The system uses some of these resources for various standard and optional features. The user can modify others.

This section describes the standard system resources, their use and assignment status.

The following table of Interrupt **ReQ**uests (IRQs) are assigned by the BIOS. Plug-and-play operating systems (Windows 98/2000/XP) may change the IRQ assignments.

IRQ Number	Use	Туре	Status
0	Timer	ISA	Permanent Assignment
1	Keyboard	ISA	Permanent Assignment
2	Programmable Interrupt Controller	ISA	Permanent Assignment
3	COM Port B (2)	ISA	BIOS enable/disable
4	COM Port A (1)	ISA	BIOS enable/disable
5		PNP/PCI	
6	Floppy Disk	ISA	Permanent Assignment, if installed
7	LPT1		BIOS Adjustable
8	Real Time Clock	ISA	Permanent Assignment
9	ACPI EC	ISA	PNP/PCI Assignment
10		PNP/PCI	
11		PNP/PCI	
12	Mouse	ISA	BIOS enable/disable
13	Math Coprocessor	ISA	Permanent Assignment
14	IDE Controller	ISA	PCI/PNP assignment
15			

Table of IRQ Assignment by BIOS

DMA Channel Table

DMA Channel	Use	Status
0	Unused	Available To User
1	Unused	Available To User
2	Floppy Disk	Permanent Assignment
3	ECP Parallel Port	BIOS adjustable
4	DMA Controller	Permanent Assignment
5	Unused	Available To User
6	Unused	Available To User
7	Unused	Available To User

Embedded Controller

The Embedded Controller (EC) in the ReVolution supervises its "power state" by monitoring incoming power levels and temperature, making decisions for safe operation. The EC also interacts with the system BIOS and informs the APM manager with power-related messages. When a fault condition occurs, the EC will change the power state and indicate the fault by flashing a series of codes on the power LED:

Embedded Controller LEDs

Symbol	Name	Purpose
1	NumLock	Indicates NumLock state. LED is on when NumLock is active.
	Caps Lock	Indicates capital letter state. LED is on when Caps Lock is active.
	Battery Indicator	Indicates battery status
\$	Power	Indicates AC power attached
\bigcirc	Hard Disk Drive Activity	Indicates when hard drive is accessed.
	LAN Activity	Indicates embedded LAN activity.
() ()	Wireless Activity	Future Use
\ge	Mail	Future Use

Refer to the "ReVolution at a Glance" section for LED locations.

You must clear the active fault before the EC will continue operation. The EC is continuously on when power is applied to the ReVolution. It is field-upgradeable.

The EC will perform a power-down override, which forces the ReVolution to turn off, when a user presses the **power on/off button** continually for 4 seconds.

Note: The Embedded Controller is active whenever there is a DC power source (external or battery) present, even with ReVolution power off. If the ReVolution is stored with one battery installed, the EC will discharge a fully charged battery in approximately 27 days. Kontron recommends that the ReVolution be stored for extended periods with no battery installed.

Power and Battery Indicators

Power State	Power LED	Battery Indicator LED
OFF	OFF	OFF
On, Charging	ON	Slow Blink
Low Battery	OFF	Fast Blink
Running on battery	OFF	ON
Running on AC not charging	ON	OFF
Battery Malfunction	Fast Blink	Fast Blink
Sleep mode battery	Slow Blink	OFF
Sleep mode AC	OFF	Slow Blink

Slow blink is defined as - 0.5 Hz, 50% duty cycle, i.e. ON for one second, OFF for one second Fast blink is defined as - 1 Hz, 50 % duty cycle, i.e. On for 500 ms, OFF for 500 ms

Phoenix BIOS Setup

Use the Phoenix BIOS Setup program for:

- Setting system time and date.
- Installing new drives for hard disks and floppy disks.
- Enhancing system performance by controlling advanced features such as shadow memory and cache memory.

To start the Phoenix BIOS Setup utility:

- 1. Turn on or reboot your system.
- 2. Press the **ESC** key when ReVolution splash screen appears.
- 3. PhoenixBIOS displays this message: Press <F2> to enter SETUP
- 4. Pressing <F2> displays the Main Menu.



Incorrect settings can cause your system to malfunction.

Navigating the Setup Menus

The Menu Bar at the top of the window lists these selections:

Main	Use this menu for basic system configuration.
Advanced	Use this menu to set the Advanced Features available on your system's chipset.
Security	Use this menu to set User and Supervisor Passwords and the Backup and Virus-Check reminders.
Power	Use this menu to configure Power-Management features.
Exit	Exits the current menu.

Use the left/right $\leftarrow \rightarrow$ arrow keys to make a selection.

Legend Bar

Use the keys listed in the legend bar on the bottom of the screen to make your selections or exit the current menu. The chart on the following page describes the legend keys and their alternates:

Key Function	
<f1> or <alt-h></alt-h></f1>	General Help window (See below).
<esc></esc>	Exit this menu.
↔ Left or right arrow keys	Select a different menu.
↓ Up or down arrow keys	Move cursor up and down.
<tab> or <shift-tab></shift-tab></tab>	Cycle cursor up and down.
<home> or <end></end></home>	Move cursor to top or bottom of window.
<pgup> or <pgdn></pgdn></pgup>	Move cursor to next or previous page.
<f5> or <-></f5>	Select the Previous Value for the field.
<f6> or <+> or <space></space></f6>	Select the Next Value for the field.
<f9></f9>	Load the Default Configuration values for this menu.
<f10></f10>	Load the Previous Configuration values for this menu.
<enter></enter>	Execute Command or Select P Submenu.
<alt-r></alt-r>	Refresh screen.

To select an item:

- Use the arrow keys to move the cursor to the field you want.
- Use the plus-and-minus value keys to select a value for that field. The Save Values commands in the Exit Menu save the values currently displayed in all the menus.

To display a sub menu:

- Use the arrow keys to move the cursor to the sub menu you want.
- Press **<Enter>**. A pointer (_) marks all sub menus.

Field Help Window

The **Help** window on the right side of each menu displays the help text for the currently selected field. It updates as you move the cursor to each field.

General Help Window

Advanced Hard Disk Features

If Advanced Hard Disk Features are installed, select one of the Master or Slave sub-menus on the Main Menu.

Use the legend keys listed on the bottom to make your selections and exit to the Main Menu.

Use the chart on the following page to configure the hard disk drive with Advanced Hard Disk Features:

Advanced Hard Disk Features

Feature	Options	Description
Туре	None	None = Autotyping is not able to
	User	supply the drive type, or end user has
	Auto (Default)	selected None, disabling any drive
	IDE Removable	that may be installed.
	CD-ROM	User = You supply the hard-disk
	ATAPI Removable	drive information in the following
		fields.
		Auto = Autotyping, the drive itself
		supplies the information.
		IDE Removable = Removable disk
		drive
		CD-ROM = CD-ROM drive.
		AIAPI Removable = Removable
Calindana	1 40 (5.52)	disk drive.
Useda	1 to 16	Number of cylinders.
Fleads Sectors	1 10 10	Number of read/write heads.
Multi Sector Transforg	Disabled	Any selection exact Dischlad
Wulti-Sector Hansiers	Standard	determines the number of sectors
	2 sectors	transferred per block Standard is 1
	4 sectors	sector per block
	8 sectors	sector per block.
	16 sectors	
LBA Mode Control	Enabled	Enabling LBA causes Logical Block
	Disabled	Addressing to be used in place of
		Cylinders, Heads, & Sectors.
		-
32-Bit I/O	Enabled	This setting enables or disables 32-bit
	Disabled (Default)	IDE data transfers.
	~	
Transfer Mode	Standard	Selects the method for transferring
	Fast PIO 1	the data between the hard disk and
	Fast PIO 2	system memory. The Setup menu
	Fast PIO 3 Foot DIO 4	the drive and platform
	OP	the drive and platform.
	Standard	
	Fast DMA A	
	Fast DMA B	
	Fast DMA F	
Ultra DMA Mode	Disabled	Selects the Ultra DMA mode used for
	Mode 0	moving data to/from the drive.
	Mode 1	Autotype the drive to select the
	Mode 2	optimum transfer mode.
	Mode 3	
	Mode 4	
	Mode 5	

Memory Cache

Enabling **cache** saves time for the CPU by holding data most recently accessed in regular memory (dynamic RAM or DRAM) in a special storage area of static RAM (SRAM), which is faster. Before accessing regular memory, the CPU first accesses the cache. If it does not find the data it is looking for there, it accesses regular memory.

Selecting **Memory Cache** from the Main Menu displays a menu like the one shown here. The actual features displayed depend on your system's hardware.

Feature	Options	Description
Memory Cache	Enabled (Default)	Sets the state of the memory cache.
-	Disabled	
Cache System BIOS area	Uncached	Controls caching of BIOS system.
	Write Protect (Default)	
Cache Video BIOS area	Uncached	Controls caching of video BIOS area.
	Write Protect (Default)	
Cache Base 0-512K:	Uncached	Controls caching of 512k base
	Write Through	memory
	Write Protect	
	Write Back (Default)	
Cache Base 512k-640k:	Uncached	Controls caching of 512k – 640k base
	Write Through	memory
	Write Protect	
	Write Back (Default)	
Cache Extended Memory Area:	Uncached	Controls caching of system memory
	Write Through	above one megabyte
	Write Protect	
	Write Back (Default)	
Cache segments, e.g., E800-EFFF	Enabled	Controls caching of individual
	Disabled (Default)	segments of memory usually reserved
	Write Through	for shadowing system or option
	Write Protect	ROMs
	Write Back	

WARNING: Incorrect settings can cause your system to malfunction.

Boot Features Menu

Select **Boot** from the menu bar on the Main Menu.

Use the legend keys to make your selections and exit to the Main Menu.

Use the following chart to select your boot options.

Feature	Options	Description
Boot-time Diagnostic Screen:	Enabled	Display the diagnostic screen during
	Disabled (Default)	boot.
Quickboot Mode:	Enabled (Default) Disabled	Allows the system to skip certain tests while booting. This will decrease the time to boot the system.

The BIOS attempts to load the operating system from the disk drives in the sequence selected here. The topmost item is the first boot device that BIOS will attempt to boot an operating system. If the device is not bootable the BIOS will move to the next device until a bootable device is found. If no devices are found to be bootable then the BIOS will post an error message.

Advanced Menu

Select **Advanced** from the menu bar on the Main Menu.

Use the legend keys to make your selections and exit to the Main Menu.

Use the following chart to configure the keyboard features:

Feature	Options	Description
Installed O/S	Other	Select the operating system installed
	Win95	on your system that you will use most
	Win98 (Default)	often.
	WinME	NOTE: An incorrect setting can
	Win2000	cause some operating systems to
		display unexpected behavior.
Reset Configuration Data:	No (Default)	Select "Yes" if you want to clear the
	Yes	Extended System Configuration Data
		(ESCD) area.
Large Disk Access Mode:	Other	UNIX, Novell Netware, or other
	DOS (Default)	operating systems, select 'Other'. If
		you are installing new software and
		the drive fails, change this selection
		and try again. Different operating
		systems require different
		representations of drive geometries.
Local Bus IDE adapter:	Disabled (Default)	Enable the integrated local bus IDE
	Primary (Default)	adapter
OEM Platform Advanced Menu		The items in this menu will allow the
		user to:
		1) Test Mobile features of the
		Almador-m Chipset
		2) Alter the Reference board
		environment.
Advanced Chinset Control		
I/O Davias Configuration		
Variation		
	Enabled (Default)	Enchle anne est fan Leasers Heimenel
Legacy USB Support	Disabled	Serial Bus

OEM Platform Advanced Memory Menu

Feature	Options	Description
Platform Power Management Sub-		These items will control the
Menu		various CPU and Chipset Power
		Management Features of this
		platform
ACPI Table/Features Control Sub		These items will control:
Menu		1) Which ACPI Tables will be
		include in the RSDT Entry
		Table Field.
		2) The values stored in specific
		ACPI Table Fields.
		3) The Enabling of Specific
		ACPI Features.
Integrated Devise Control Sub-		These items determine whether
Menu		the integrated PCI Devices will
		be enabled in PCI Config. Space
ATA 66/ATA 100 Support	Enabled	This item allows IDE drives to be
	Disabled	set above ATA 33 if the drive
		supports that speed.

Integrated Device Control Sub-Menu		
Feature	Options	Description
USB – Device 29	Disabled	Enable or Disable all ICH3 USB
	Enabled (Default)	1.1 Devices by setting item to the
		desired value.
USB – Device 29, Function 1	Disabled	Enable or Disable all ICH3 USB
	Enabled (Default)	1.1 Devices by setting item to the
		desired value.
USB – Device 29, Function 2	Disabled	Enable or Disable all ICH3 USB
	Enabled (Default)	1.1 Devices by setting item to the
		desired value.
AC97 – Device 31, Function 5	Disabled	Enable or Disable the AC97 Audio
	Enabled (Default)	Device if present. This Setup Item
		will have no effect if an AC97
		Audio MDC is not present.

Integrated Device Control Sub-Menu

Advanced Chipset Control Menu

Feature	Options	Description
IGD Boot Type	VBIOS Default (Default)	Select the Video Display that the
	CRT	Internal Graphics Device will
	LCD	make active during the POST:
	CRT LCD	1) VBIOS Default
	_	2) CRT
		3) LCD
		4) CRT LCD
		,
		Selecting "VBIOS Default" will
		allow the VBIOS to choose the
		Video Display to enable.
IGD – LCD Panel Type	800x600 LVDS	Select the LCD Panel used by the
	1024x768 LVDS (Default)	Internal Graphics Device by
		selecting the appropriate setup
		item. The first item is Panel 1, the
		last item is Panel 16. Some
		Panels are not numbered due to
		size constraints.
		NOTE: SVGA screen requires
		change to 800x600 LVDS.
Default Primary Video	AGP (Default)	Select PCI to use a PCI video
-	PCI	card for the boot display device.
		Select AGP to use an AGP video
		card for the boot display device.
Graphics Aperture	32MB	Select the size of the Graphics
1 1	64MB (Default)	Aperture for the AGP video
	128MB	device.
	256MB	
Enable Memory Gap	Disable (Default)	Free RAM Address space for use
	Extended	with an option card starting at
		15MB.

I/O Device Configuration Menu

The CPU communicates with external devices such as printers through devices called **Input/Output (I/O) ports** such as serial and parallel ports. These I/O devices require the use of system resources such as I/O addresses and interrupt lines. If these devices are Plug and Play, either the BIOS can allocate the devices during POST, or the operating system can do it.

If the I/O devices are not Plug and Play, they may require manually setting them in Setup. On some systems, the **chipset** manages the communication devices. Other systems have, instead, a separate **I/O chip** on the motherboard for configuring and managing these devices.

Many systems allow you to control the configuration settings for the I/O ports.

Select **I/O Device Configuration** on the Advanced Menu to display this menu and specify how you want to configure these I/O Devices:

Use the legend keys to make your selections and exit to the Main Menu.

Use the following chart to configure the Input/Output settings:

Feature	Options	Description
Serial port A: Serial port B:	Disabled Enabled (Default) Auto OS Controlled	Disabled turns off the port. Enabled requires you to enter the base Input/Output address and the Interrupt number on the next line. Auto makes the BIOS configure the port automatically during POST. OS Controlled lets the PnP Operating System (such as Windows 95) configure the port after POST.
Parallel Port:	Disabled Enabled (Default) Auto OS Controlled	Disabled turns off the port. Enabled requires you to enter the base Input/Output address and the Interrupt number below. Auto makes the BIOS auto configure the port during POST. OS Controlled lets the PnP Operating System (such as Windows 95) configure the port after POST.
Mode	Output only Bi-directional ECP (Default) EPP & ECP	Output only is standard one-way protocol for a parallel device. Bi-directional uses two-way protocol of an Extended Capabilities Port (ECP).
Floppy Disk Controller	Disabled Enabled Auto (Default)	Enables the on-board legacy diskette controller. Disabled turns off all legacy diskette drives. Auto select per BIOS or OS

Use this menu to specify how the I/O (Input and Output) ports are configured:

- Manually by you.
- Automatically by the BIOS during POST
- Automatically by a PnP Operating System such as Windows 95 after the Operating System boots.

Warning: If you choose the same I/O address or Interrupt for more than one port, the menu displays an asterisk (*) at the conflicting settings. It also displays this message at the bottom of the menu:

* Indicates a DMA, Interrupt, I/O, or memory resource conflict with another device. Resolve the conflict by selecting another settings for the devices.

Keyboard Features

Select Keyboard from the menu bar on the Main Menu.

Use the legend keys to make your selections and exit to the Main Menu.

Use the following chart to configure the keyboard features:

Feature	Options	Description
Numlock	Auto	On or Off turns NumLock on or off
	On	at bootup. Auto turns NumLock on if
	Off (Default)	it finds a numeric key pad.
Key Click	Enabled	Enables key click.
	Disabled (Default)	
Keyboard auto-repeat rate	2/sec	Sets the number of times per second
	6/sec	to repeat a keystroke when you hold
	10/sec	the key down.
	13.3/sec	
	21.8/sec	
	26.7/sec	
	30/sec (Default)	
Keyboard auto-lag delay	¹ / ₄ sec	Sets the delay time after the key is
	$\frac{1}{2}$ sec (Default)	held down before it begins to repeat
	³ / ₄ sec	the keystroke.
	1 sec	

Security Menu

Select **Security** from the menu bar on the Main Menu.

Use the legend keys to make your selections and exit to the Main Menu.

Enabling "Supervisor Password" requires a password for entering Setup. The passwords are not case sensitive.

Pressing **<Enter>** at either Set Supervisor Password or Set User Password displays a dialog box like this:

Set Password	
Enter password: []
Confirm password: []
Enter: Accept	

Type the password and press **<Enter>**. Repeat.

Note: In some systems, the User and Supervisor passwords are related; you cannot have a User password without first creating a Supervisor password. In other systems, you can create and use them independently.

Use the following chart to configure the system-security and anti-virus options.

Feature	Options	Description
Set Supervisor Password	Up to seven alphanumeric	Pressing <enter> displays dialog box for entering</enter>
	characters	the supervisor password. In related systems, this
		password gives full access to Setup menus.
Set User Password	Up to seven alphanumeric	Pressing <enter> displays the dialog box for entering</enter>
	characters	the user password. In related systems, this password
		gives restricted access to SETUP menus.
Password on Boot	Enabled	Enabled requires a password on boot. Requires prior
	Disabled	setting of the Supervisor password. If supervisor
		password is set and this option disabled, BIOS
		assumes user is booting.
Diskette Access	Enabled	Enabled requires a password to boot from or access
	Disabled	the floppy disk.

Boot Menu

Select **Boot** from the menu bar on the Main Menu.

Use this menu to arrange to specify the priority of the devices from which the BIOS will attempt to boot the Operating System. The BIOS will attempt first to boot from the CD-ROM drive (the only Removable Device listed). Failing that, it will attempt to boot from the Primary Master hard disk, and so on down the list.

Removable Devices, **Hard Drive**, and **Network Boot** are the generic types of devices on your system from which you can boot an operating system. You may have more than one device of each type. If so, the generic type is marked with a plus or minus sign. Use the **<Enter>** key to expand or collapse the devices marked with <+> or <->. Press **<Ctrl+Enter>** to expand all such devices.

Note: Floppy drives are not managed on this menu as part of Removable Devices. To change a device's priority on the list, first select it with the up-or-down arrows, and move it up or down using the <+> and <-> keys. Pressing <n> moves a device between the Removable Devices and Hard Drive. Pressing <Shift+1> enables or disables a device.

Feature	Options	Description
Removable Devices	Legacy Floppy Drives	Keys used to view or configure
		devices
Hard Drive	Toshiba MK6412MAT-(PM)	
	Bootable Add - Cards	
CD-ROM Drive		

Exit Menu

Select Exit from the menu bar on the Main Menu.

The following sections describe each of the options on this menu. Note that **<Esc>** does not exit this menu. You must select one of the items from the menu or menu bar to exit.

Exit Saving Values

After making your selections on the Setup menus, always select either "Exit Saving Value" or "Save Changes." Both procedures store the selections displayed in the menus in **CMOS** (short for "battery-backed CMOS RAM") a special section of memory that stays on after you turn your system off. The next time you boot your computer, the BIOS configures your system according to the Setup selections stored in CMOS.

After you save your selections, the program displays this message: Values have been saved to CMOS! Press <space> to continue

If you attempt to exit without saving, the program asks if you want to save before exiting.

During bootup, *Phoenix*BIOS attempts to load the values saved in CMOS. If those values cause the system boot to fail, reboot and press **<F2>** to enter Setup. In Setup, you can get the Default Values (as described below) or try to change the selections that caused the boot to fail.

Exit Discarding Changes

Use this option to exit Setup without storing in CMOS any new selections you may have made. The selections previously in effect remain in effect.

Load Setup Defaults

To display the default values for all the Setup menus, select "Load Setup Defaults" from the Main Menu. The program displays this message:

ROM Default values have been loaded! Press <space> to continue

If, during bootup, the BIOS program detects a problem in the integrity of values stored in CMOS, it displays these messages:

System CMOS checksum bad - run SETUP Press <F1> to resume, <F2> to Setup

The CMOS values have been corrupted or modified incorrectly, perhaps by an application program that changes data stored in CMOS. Press **<F1>** to resume the boot or **<F2>** to run Setup with the ROM default values already loaded into the menus. You can make other changes before saving the values to CMOS.

Discard Changes

If, during a Setup Session, you change your mind about changes you have made and have not yet saved the values to CMOS, you can restore the values you previously saved to CMOS. Selecting "Discard Changes" on the Exit menu updates all the selections and displays this message:

CMOS values have been loaded! Press <space> to continue

Save Changes

Selecting "Save Changes" saves all the selections without exiting Setup. You can return to the other menus if you want to review and change your selections.

BIOS Messages

The following is a list of the messages that the BIOS can display. Most of them occur during POST. Some of them display information about a hardware device, e.g., the amount of memory installed. Others may indicate a problem with a device, such as the way it has been configured.

Following the list are explanations of the messages and remedies for reported problems. *If your system displays one of the messages marked below with an asterisk (*), write down the message and contact Kontron Technical Support.

If your system fails after you make changes in the Setup menus, reset the computer, enter Setup and install Setup defaults or correct the error.

0200 Failure Fixed Disk

Fixed disk is not working or not configured properly. Check to see if fixed disk is attached properly. Run Setup. Find out if the fixed-disk type is correctly identified.

0210 Stuck key Stuck key on keyboard.

0211 Keyboard error

Keyboard not working.

*0212 Keyboard Controller Failed

Keyboard controller failed test. May require replacing keyboard controller.

0213 Keyboard locked - Unlock key switch

Unlock the system to proceed.

0220 Monitor type does not match CMOS - Run SETUP Monitor type not correctly identified in Setup

*0230 Shadow Ram Failed at offset: nnnn

Shadow RAM failed at offset nnnn of the 64k block at which the error was detected.

*0231 System RAM Failed at offset: nnnn

System RAM failed at offset nnnn of in the 64k block at which the error was detected.

***0232 Extended RAM Failed at offset:** nnnn Extended memory not working or not configured properly at offset nnnn.

0250 System battery is dead - Replace and run SETUP

The CMOS clock battery indicator shows the battery is dead. Replace the battery and run Setup to reconfigure the system. **Note:** ReVolution's CMOS battery is rechargeable and should never need replacement.

0251 System CMOS checksum bad - Default configuration used

System CMOS has been corrupted or modified incorrectly, perhaps by an application program that changes data stored in CMOS. The BIOS installed Default Setup Values. If you do not want these values, enter Setup and enter your own values. If the error persists, check the system battery or contact KMC.

*0260 System timer error

The timer test failed. Requires repair of system board.

*0270 Real time clock error

Real-Time Clock fails BIOS hardware test. May require board repair.

0271 Check date and time settings

BIOS found date or time out of range and reset the Real-Time Clock. May require setting legal date (1991-2099).

0280 Previous boot incomplete - Default configuration used

Previous POST did not complete successfully. POST loads default values and offers to run Setup. If the failure was caused by incorrect values and they are not corrected, the next boot will likely fail. On systems with control of **wait states**, improper Setup settings can also terminate POST and cause this error on the next boot. Run Setup and verify that the waitstate configuration is correct. This error is cleared the next time the system is booted.

0281 Memory Size found by POST differed from CMOS

Memory size found by POST differed from CMOS.

02B0 Diskette drive A error

02B1 Diskette drive B error

Drive A: or B: is present but fails the BIOS POST diskette tests. Check to see that the drive is defined with the proper diskette type in Setup and that the diskette drive is attached correctly.

02B2 Incorrect Drive A type - run SETUP Type of floppy drive A: not correctly identified in Setup.

02B3 Incorrect Drive B type - run SETUP Type of floppy drive B: not correctly identified in Setup.

02D0 System cache error - Cache disabled

RAM cache failed and BIOS disabled the cache. On older boards, check the cache jumpers. You may have to replace the cache. See your dealer. A disabled cache slows system performance considerably.

02F0: CPU ID:

CPU socket number for Multi-Processor error.

***02F4: EISA CMOS not writeable** ServerBIOS2 test error: Cannot write to EISA CMOS.

*02F5: DMA Test Failed

ServerBIOS2 test error: Cannot write to extended DMA (Direct Memory Access) registers.

*02F6: Software NMI Failed

ServerBIOS2 test error: Cannot generate software NMI (Non-Maskable Interrupt).

*02F7: Fail-Safe Timer NMI Failed

ServerBIOS2 test error: Fail-Safe Timer takes too long.

device Address Conflict

Address conflict for specified device.

Allocation Error for: device

Run ISA or EISA Configuration Utility to resolve resource conflict for the specified device.

CD ROM Drive CD ROM Drive identified.

Entering SETUP Starting Setup program

*Failing Bits: nnnn

The hex number **nnnn** is a map of the bits at the RAM address which failed the memory test. Each 1 (one) in the map indicates a failed bit. See errors 230, 231, or 232 above for offset address of the failure in System, Extended, or Shadow memory.

Fixed Disk n

Fixed disk n (0-3) identified.

Invalid System Configuration Data Problem with NVRAM (CMOS) data.

I/O device IRQ conflict

I/O device IRQ conflict error.

PS/2 Mouse Boot Summary Screen:

PS/2 Mouse installed.

nnnn kB Extended RAM Passed

Where **nnnn** is the amount of RAM in kilobytes successfully tested.

nnnn Cache SRAM Passed

Where nnnn is the amount of system cache in kilobytes successfully tested.

nnnn kB Shadow RAM Passed

Where **nnnn** is the amount of shadow RAM in kilobytes successfully tested.

nnnn kB System RAM Passed

Where nnnn is the amount of system RAM in kilobytes successfully tested.

One or more I2O Block Storage Devices were excluded from the Setup Boot Menu There was not enough room in the IPL table to display all installed I₂O block-storage devices.

Operating system not found

Operating system cannot be located on either drive A: or drive C:. Enter Setup and see if fixed disk and drive A: are properly identified.

*Parity Check 1 nnnn

Parity error found in the system bus. BIOS attempts to locate the address and display it on the screen. If it cannot locate the address, it displays ????. Parity is a method for checking errors in binary data. A parity error indicates that some data has been corrupted.

*Parity Check 2 nnnn

Parity error found in the I/O bus. BIOS attempts to locate the address and display it on the screen. If it cannot locate the address, it displays ????.

Press <F1> to resume, <F2> to Setup, <F3> for previous

Displayed after any recoverable error message. Press $\langle F1 \rangle$ to start the boot process or $\langle F2 \rangle$ to enter Setup and change the settings. Press $\langle F3 \rangle$ to display the previous screen (usually an initialization error of an **Option ROM**, i.e., an add-on card). Write down and follow the information shown on the screen.

Press <F2> to enter Setup

Optional message displayed during POST. Can be turned off in Setup.

PS/2 Mouse:

PS/2 mouse identified.

Run the I2O Configuration Utility

One or more unclaimed block storage devices have the Configuration Request bit set in the LCT. Run an I2O Configuration Utility (e.g. the SAC utility).

System BIOS shadowed

System BIOS copied to shadow RAM.

UMB upper limit segment address: nnnn

Displays the address *nnnn* of the upper limit of **Upper Memory Blocks**, indicating released segments of the BIOS which can be reclaimed by a virtual memory manager.

Video BIOS shadowed

Video BIOS successfully copied to shadow RAM.

Test Points and Beep Codes

At the beginning of each POST routine, the BIOS outputs the test point error code to I/O address 80h. Use this code during trouble shooting to establish at what point the system failed and what routine was being performed. The following is a list of the checkpoint codes written at the start of each test and the beep codes issued for terminal errors. Unless otherwise noted, these codes are valid for PhoenixBIOS 4.0 Release 6.x.

02h Verify Real Mode 03h Disble Non-Maskable Interrupt (NMI) 04h Get CPU type 06h Initialize system hardware 07h Disable shadow and execute code from the ROM. 08h Initialize chipset with initial POST values 09h Set IN POST flag 0Ah Initialize chipset with initial POST values 0Eh Initialize chipset to initial POST values 0Eh Initialize to O component 0Fh Initialize to O component 10h Initialize to O component 17h Load alternate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize keyboard controller 16h 1-2-2-3 17h Initialize to Component 17h Initialize to Component 17h Initialize to Comp	Code	Beeps	Description
03h Disable Non-Maskable Interrupt (NMI) 04h Get CPU type 06h Initialize system hardware 07h Disable shadow and execute code from the ROM. 08h Initialize chypes with Initial POST values 09h Set IN POST flag 0Ah Initialize CPU registers 0Bh Enable CPU cache 0Ch Initialize CPU registers 0Bh Enable CPU cache 0Ch Initialize Power Management 10h Load alternate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize POWer Maasgement 14h Load alternate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize Ache bofroe memory Auto size 14h Initialize Ache bofroe memory Auto size 18h 8237 DMA controller 1Ah 8237 DMA controller initialization 1Ch Rest Programmable Interrupt Controller 2bh 1-3-1-3 Test M24 Ryboad Controller 2bh Set ES segment	02h		Verify Real Mode
04h Get CPU type 06h Initialize system hardware 07h Disable shadow and execute code from the ROM. 08h Initialize chipset with initial POST values 09h Set LN POST Tag 0Ah Initialize chipset with initial POST values 0Bh Enable CPU cache 0Ch Initialize code to initial POST values 0Fh Initialize to component 0Fh Initialize to component 0Fh Initialize Power Management 11h Load alternate registers with initial POST values 12h Restore CPU concil word during warm boot 13h Initialize POE Bas Mastering devices 14h Initialize ache before memory Auto size 18h Restore CPU conchecksum 17h Initialize tache before memory Auto size 18h Rest Programmable Interrupt Controller 16h 1-2-2-3 17h Est DRAM refresh 21h 1-3-1-3 17s test DRAM refresh 22h 1-3-1-3 17s test SF42 Keyboard Controller 24h <td>03h</td> <td></td> <td>Disable Non-Maskable Interrupt (NMI)</td>	03h		Disable Non-Maskable Interrupt (NMI)
06h Initialize system hardware 07h Disable shadow and execute code from the ROM. 08h Initialize chipset with initial POST values 09h Set IN POST flag 08h Initialize CPU cache 0Ch Initialize aches to initial POST values 0Eh Initialize CD component 0Fh Initialize To component 0Fh Initialize Power Management 11h Load alternate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize POVE Two Markage devices 14h Initialize Reyboard controller 16h 1-2-2-3 17h Initialize ache before menory Auto size 18h 8254 timer initialization 17h Initialize ache before menory Auto size 18h 8254 timer initialization 17h Rest Programmable Interrupt Controller 20h 1-3-1-1 Test 8742 Keyboard Controller 21h 1-3-1-3 Test 8742 Keyboard Controller 22h 1-3-1-4 Test 8742 Keyboard Controller	04h		Get CPU type
107h Disable shadow and execute code from the ROM. 08h Initialize chipset with initial POST values 09h Set IN POST flag 0Ah Initialize CPU registers 0Bh Enable CPU cache 0Ch Initialize I/O component 0Fh Initialize I/O component 0Fh Initialize I/O component 0Fh Initialize Power Management 11h Load allernate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize PO E Bas Mastering devices 14h Initialize cache before memory Auto size 18h Restore CPU control word during warm boot 17h Initialize cache before memory Auto size 18h 8237 DMA controller 17h Initialize cache before memory Auto size 18h 8237 DMA controller initialization 1Ch Reset Programmable Interrupt Controller 20h 1-3-1-1 Test BRAM refresh 21h Set ES segment register to 4 GB 28h Auto size DRAM 20h Initialize Pose	06h		Initialize system hardware
Initialize chipset with initial POST values 09h Set IN POST flag 0Ah Initialize CPU registers 0Bh Enable CPU cache 0Ch Initialize caches to initial POST values 0Eh Initialize the local bus IDE 10h Initialize the local bus IDE 10h Initialize Power Management 11h Load alternate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize Reyboard controller 16h 1-2-2-3 BIOS ROM checksum 17h Initialize caches to before memory Auto size 18h 8254 timer initialization 1Ch Reset Programmable Interrupt Controller 18h 8237 DMA controller initialization 1Ch Reset Programmable Interrupt Controller 20h 1-3-1-1 Test ST42 Keyboard Controller 21h 1-3-1-3 Test Stagment register to 4 GB 28h Auto size DRAM 21h 1-3-4-3 RAM failure on address line xxx* 21h 1-3-4-1 RAM failure on address line xxx* <td>07h</td> <td></td> <td>Disable shadow and execute code from the ROM.</td>	07h		Disable shadow and execute code from the ROM.
OPh Set IN POST flag 0Ah Initialize CPU registers 0Bh Enable CPU cache 0Ch Initialize i/O component 0Fh Initialize i/O component 0Fh Initialize i/O component 0Fh Initialize i/O component 0Fh Initialize i/O component 11h Load alternate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize CI Bus Mastering devices 14h Initialize acahe before memory Auto size 18h 8254 timer initialization 1Ah Rest DRS ROM checksum 17h Initialize acahe before memory Auto size 18h 8254 timer initialization 1Ah 8237 DMA controller initialization 1Ah 8237 DMA controller 2bh 1-3-1-1 Test BRAM refresh 22h 1-3-1-3 Test RPA Keyboard Controller 24h Set ES segment register to 4 GB 28h Auto size DRAM 29h Initialize POST Memory Manager <td>08h</td> <td></td> <td>Initialize chipset with initial POST values</td>	08h		Initialize chipset with initial POST values
OAh Initialize CPU registers OBh Enable CPU cache OCh Initialize caches to initial POST values OEh Initialize caches to initial POST values OFh Initialize caches to initial POST values OFh Initialize the local bus IDE I0h Initialize Power Management 11h Load alternate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize keyboard controller 14h Initialize cache before memory Auto size 18h 8254 timer initialization 17h Initialize cache before memory Auto size 18h 8254 timer initialization 1Ah 8237 DMA controller initialization 1Ah 8237 DMA controller register box 4 GB 2bh 1-3-1-1 Test 8742 keyboard Controller 2bh Set ES segment register to 4 GB 2bh Auto size DRAM 2bh Clear 512 kB base RAM 2bh Clear 512 kB base RAM 2bh Enable cache before system BIOS shadow 3ch Test CPU bu	09h		Set IN POST flag
OBh Enable CPU cache OCh Initialize caches to initial POST values OEh Initialize to component OFh Initialize to component OFh Initialize to local bus IDE I0h Initialize to component OFh Initialize to component I1h Load alternate registers with initial POST values I2h Restore CPU control word during warm boot I3h Initialize PCI Bus Mastering devices I4h Initialize cache before memory Auto size I8h 8254 timer initialization IAh S237 DMA controller initialization IAh 8237 DMA controller registers values Oth Reset Programmable Interrupt Controller Oth Set ES segment register to 4 GB 28h Auto size DRAM 29h Initialize POST Memory Manager 2Ah Clear 512 Rb Base RAM 2Ch 1-3-4-3 RAM failure on address line xxx** 21h Enable cache before system BIOS shadow 32h Test CPU bus-clock frequency 33h Initialize P	0Ah		Initialize CPU registers
OCh Initialize caches to initial POST values 0Eh Initialize I/O component 0Fh Initialize to local bus IDE 10h Initialize to local bus IDE 11h Load alternate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize to control word during warm boot 13h Initialize keyboard controller 16h 1-2-2-3 17h Initialize cache before memory Auto size 18h 8254 timer initialization 17h Initialize cache before memory Auto size 18h 8254 timer initialization 17h Rest Programmable Interrupt Controller 20h 1-3-1-1 Test B742 Keyboard Controller 21h Set ES segment register to 4 GB 24h 22h 1-3-1-3 Test 8742 Keyboard Controller 24h Set ES segment register to 4 GB 28h 28h Auto size DRAM 29h 27h Initialize POST Memory Manager 2Ah 26h 1-3-4-1 RAM failure on address line xxxx* 28h	0Bh		Enable CPU cache
OEh Initialize I/O component OFh Initialize the local bus IDE 10h Initialize voer Management 11h Load alternate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize PCU Bus Mastering devices 14h Initialize keyboard controller 16h 1-2-2-3 17h Initialize controller 16h 1-2-2-3 17h Initialize controller 17h Initialize controller 17h Initialize Controller 18h 8254 timer initialization 17ch Reset Programmable Interrupt Controller 20h 1-3-1-1 16h Reset Programmable Interrupt Controller 24h Set ES segment register to 4 GB 28h Auto size DRAM 29h Initialize POST Memory Manager 2Ah Clear 512 LBb ase RAM 20ch 1-3-4-1 RAM failure on adates line xxxx* 21h Test CPU bus-clock frequency 33h Initialize Phoenix Dispa	0Ch		Initialize caches to initial POST values
OFh Initialize the local bus IDE 10h Initialize Power Management 11h Load alternate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize PCI Bus Mastering devices 14h Initialize keyboard controller 16h 1-2-2-3 BIOS ROM checksum 17h Initialize acche before memory Auto size 18h 8254 timer initialization 1Ah 8237 DMA controller initialization 1Ch Reset Programmable Interrupt Controller 20h 1-3-1-1 Test DRAM refresh 21h -3-1-3 Test SP42 Keyboard Controller 24h Set ES segment register to 4 GB Auto size DRAM 29h Initialize POST Memory Manager 2Ah 21h 1-3-4-1 RAM failure on address line xxxt* ^a 21h 1-3-4-3 RAM failure on address line xxxt* ^a 21h 1-3-4-4 RAM failure on address line xxxt* ^a 21h 1-3-4-3 RAM failure on address line xxxt* ^a 21h 1-3-4-4 RAM failure on address line xxx	0Eh		Initialize I/O component
10h Initialize Power Management 11h Load alternate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize PCI Bus Mastering devices 14h Initialize PCI Bus Mastering devices 14h Initialize cache before memory Auto size 18h 8254 timer initialization 17h Initialize cache before memory Auto size 18h 8254 timer initialization 17h Reset Porgrammable Interrupt Controller 20h 1-3-1-1 Test DRAM refresh 21h 1-3-1-3 Test 8742 Keyboard Controller 24h Set ES segment register to 4 GB 28h Auto size DRAM 29h Initialize POST Memory Manager 2Ah Clear 512 kB base RAM 2Ch 1-3-4-3 RAM failure on address line xxx* 2Eh 1-3-4-3 RAM failure on address line xxx* 2Fh Enable cache before system BIOS shadow 32h Initialize Phoenix Dispatch Manager 33h Initialize Phoenix Dispatch Manager 34h Auto size ca	0Fh		Initialize the local bus IDE
11h Load alternate registers with initial POST values 12h Restore CPU control word during warm boot 13h Initialize PCI Bus Mastering devices 14h Initialize PCI Bus Mastering devices 14h Initialize PCI Bus Mastering devices 16h 1-2-2-3 BIOS ROM checksum 17h Initialize cache before memory Auto size 18h 8254 timer initialization 1Ah 8237 DMA controller initialization 1Ch Reset Programmable Interrupt Controller 20h 1-3-1-3 Test R742 Keyboard Controller 21h 1-3-1-3 Test 8742 Keyboard Controller 22h 1-3-1-3 Test 8742 Keyboard Controller 24h Set ES segment register to 4 GB 28h Auto size DRAM 29h Initialize POST Memory Manager 2Ah Clear 512 kB base RAM 21h Easle cache before system BIOS shadow 32h Test CPU bus-clock frequency 33h Initialize Phoenix Dispatch Manager 36h Warm start shut down 38h Shadow system BIOS ROM	10h		Initialize Power Management
12h Restore CPU control word during warm boot 13h Initialize PCI Bus Mastering devices 14h Initialize keyboard controller 16h 1-2-2-3 17h Initialize cache before memory Auto size 18h 8254 timer initialization 17h Initialize cache before memory Auto size 18h 8254 timer initialization 1Ch Reset Programmable Interrupt Controller 20h 1-3-1-1 Test DRAM refresh 21h Set ES segment register to 4 GB 28h Auto size DRAM 29h Initialize POST Memory Manager 2Ah Clear 512 kB base RAM 20ch 1-3-4-1 RAM failure on address line xxxx* 21h 1-3-4-3 RAM failure on data bits xxx* of why be of memory bus 21rh Enable cache before system BIOS shadow 32h Test CPU bus-clock frequency 33h Initialize Phoenix Dispatch Manager 34h Auto size cache 37h Advanced configuration of chipset registers 37h Load alternate registers with CMOS values 37h In	11h		Load alternate registers with initial POST values
13h Initialize PCI Bus Mastering devices 14h Initialize keyboard controller 16h 1-2-2-3 17h Initialize cache before memory Auto size 18h 8254 timer initialization 1Ah 8237 DMA controller initialization 1Ch Reset Programmable Interrupt Controller 20h 1-3-1-1 Test BRAM refresh 21h 1-3-1-3 Test 8742 Keyboard Controller 24h Set ES segment register to 4 GB 28h Auto size DRAM 29h Initialize POST Memory Manager 2Ah Clear 512 kB base RAM 2Ch 1-3-4-1 RAM failure on address line xxx** 2Eh 1-3-4-3 RAM failure on address line xxx** 2Eh 1-3-4-3 RAM failure on data bits xxx** of low byte of memory bus 2Fh Enable cache before system BIOS shadow 32h Initialize Phoenix Dispatch Manager 33h Initialize Phoenix Dispatch Manager 34h Auto size cache 3Ch Advanced configuration of chipset registers 3Bh Inititalize transmory for RomPilot	12h		Restore CPU control word during warm boot
14h Initialize keyboard controller 16h 1-2-2-3 BIOS ROM checksum 17h Initialize cache before memory Auto size 18h 8254 timer initialization 1Ah 8237 DMA controller initialization 1Ah 8237 DMA controller initialization 1Ch Reset Programmable Interrupt Controller 20h 1-3-1-1 Test RAM refresh 21h 1e-31-3 Test 8742 Keyboard Controller 24h Set ES segment register to 4 GB 28h Auto size DRAM 29h Initialize POST Memory Manager 2Ah Clear 512 kB base RAM 2Ch 1-3-4-1 RAM failure on address line xxx** 2Eh 1-3-4-3 RAM failure on data bits xxx** of low byte of memory bus 2Fh Enable cache before system BIOS shadow 32h Test CPU bus-clock frequency 33h Initialize Poenix Dispatch Manager 36h Warm start shut down 38h Shadow system BIOS ROM 3Ah Auto size cache 3Ch Advanced configuration of chipset registers 3Dh Load alternate registers with CMOS values <td>13h</td> <td></td> <td>Initialize PCI Bus Mastering devices</td>	13h		Initialize PCI Bus Mastering devices
16h 1-2-2-3 BIOS ROM checksum 17h Initialize cache before memory Auto size 18h 8254 timer initialization 1Ah 8237 DMA controller initialization 1Ch Reset Programmable Interrupt Controller 20h 1-3-1-1 Test DRAM refresh 22h 1-3-1-3 Test 8742 Keyboard Controller 24h Set ES segment register to 4 GB 28h Auto size DRAM 29h Initialize POST Memory Manager 2Ah Clear 512 kB base RAM 2Ch 1-3-4-3 RAM failure on address line xxxt* 2Eh 1-3-4-3 RAM failure on date bits xxxt* of low byte of memory bus 2Fh Enable cache before system BIOS shadow 32h Test CPU bus-clock frequency 33h Initialize Phoenix Dispatch Manager 36h Warm start shut down 38h Shadow system BIOS ROM 3Ah Auto size cache 3Ch Advanced configuration of chipset registers 3Dh Load alternate registers with CMOS values 41h Initialize interrupt vectors 45h <td< td=""><td>14h</td><td></td><td>Initialize keyboard controller</td></td<>	14h		Initialize keyboard controller
17hInitialize cache before memory Auto size18h8254 timer initialization1Ah8237 DMA controller initialization1ChReset Programmable Interrupt Controller20h1-3-1-122h1-3-1-3Test 8742 Keyboard Controller24hSet ES segment register to 4 GB28hAuto size DRAM29hInitialize POST Memory Manager2AhClear 512 kB base RAM2Ch1-3-4-1RAM failure on address line xxxx*2Eh1-3-4-3RAM failure on data bits xxxx* of low byte of memory bus2FhEnable cache before system BIOS shadow32hTest CPU bus-clock frequency33hInitialize Phoenix Dispatch Manager36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3ChLoad alternate registers with CMOS values41hInitialize interrupt vectors45hPOST device initialization46h2-1-2-3Check ROM copyright notice47hInitialize 120 support48hCheck video configuration against CMOS49hInitialize all video adapters in system48hCheck video configuration against CMOS48hQuietBoot start (optional)48hCheck video configuration against CMOS48hCheck video configuration against CMOS48hCheck video configuration against CMOS49hInitialize PI bus and devices41hInitialize PI bus and devices<	16h	1-2-2-3	BIOS ROM checksum
18h 8254 timer initialization 1Ah 8237 DMA controller initialization 1Ch Reset Programmable Interrupt Controller 20h 1-3-1-1 Test DRAM refresh 22h 1-3-1-3 Test 8742 Keyboard Controller 24h Set ES segment register to 4 GB 28h Auto size DRAM 29h Initialize POST Memory Manager 2Ah Clear 512 kB base RAM 2Ch 1-3-4-1 RAM failure on address line xxxx* 2Eh 1-3-4-3 RAM failure on by system BIOS shadow 32h Test CPU bus-clock frequency 33h Initialize Phoenix Dispatch Manager 36h Warm start shut down 38h Shadow system BIOS ROM 3Ah Auto size cache 3Ch Advanced configuration of chipset registers 3Dh Load alternate registers with CMOS values 41h	17h		Initialize cache before memory Auto size
1Ah8237 DMA controller initialization1ChReset Programmable Interrupt Controller20h1-3-1-1Test R42 Keyboard Controller24hSet ES segment register to 4 GB28hAuto size DRAM29hInitialize POST Memory Manager2AhClear 512 kB base RAM2Ch1-3-4-3RAM failure on address line xxx*2Eh1-3-4-3RAM failure on address line xxx*2Eh1-3-4-3RAM failure on success2BhEnable cache before system BIOS shadow32hTest CPU bus-clock frequency33hInitialize Phoenix Dispatch Manager36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize interrupt vectors45hPOST device initialization46h2-1-2-3Check ROM copyright notice47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize all video adapters in system48hQuietBoot start (optional)4AhInitialize BOS ROM48hCheck video configuration against CMOS49hInitialize BOS ROM48hCheck video configuration against CMOS49hInitialize BOS ROM44hInitialize BOS ROM45hDisplay BIOS copyright notice47hInitialize MultiBoot<	18h		8254 timer initialization
1Ch Reset Programmable Interrupt Controller 20h 1-3-1-1 Test DRAM refresh 22h 1-3-1-3 Test 8742 Keyboard Controller 24h Set ES segment register to 4 GB 28h Auto size DRAM 29h Initialize POST Memory Manager 2Ah Clear 512 kB base RAM 2Ch 1-3-4-1 RAM failure on datess line xxxx* 2Eh 1-3-4-3 RAM failure on data bits xxxx* of low byte of memory bus 2Fh Enable cache before system BIOS shadow 32h Test CPU bus-clock frequency 33h Initialize Phoenix Dispatch Manager 36h Warm start shut down 38h Shadow system BIOS ROM 3Ah Auto size cache 3Ch Advanced configuration of chipset registers 3Dh Load alternate registers with CMOS values 41h Initialize interrupt vectors 45h POST device initialization 46h 2-1-2-3 Check ROM copyright notice 47h Initialize PCI bus and devices 4Ah Initialize PCI bus and devices 4Ah Initialize RU ousodapters in system	1Ah		8237 DMA controller initialization
20h 1-3-1-1 Test DRAM refresh 22h 1-3-1-3 Test 8742 Keyboard Controller 24h Set ES segment register to 4 GB 28h Auto size DRAM 29h Initialize POST Memory Manager 2Ah Clear 512 kB base RAM 20ch 1-3-4-1 RAM failure on address line xxxx* 2Eh 1-3-4-3 RAM failure on data bis xxxx* of low byte of memory bus 2Fh Enable cache before system BIOS shadow 32h Test CPU bus-clock frequency 33h Initialize Phoenix Dispatch Manager 36h Warm start shut down 38h Shadow system BIOS ROM 3Ah Auto size cache 3Ch Advanced configuration of chipset registers 3Dh Load alternate registers with CMOS values 41h Initialize extended memory for RomPilot 42h Initialize interrupt vectors 45h POST device initialization 46h 2-1-2-3 Check ROM copyright notice 47h Initialize I20 support 48h Check video configuration against CMOS 49h Initialize all video adapters in system </td <td>1Ch</td> <td></td> <td>Reset Programmable Interrupt Controller</td>	1Ch		Reset Programmable Interrupt Controller
22h1-3-1-3Test 8742 Keyboard Controller24hSet ES segment register to 4 GB28hAuto size DRAM29hInitialize POST Memory Manager2AhClear 512 kB base RAM2Ch1-3-4-1RAM failure on address line xxxr*2Eh1-3-4-3RAM failure on data bits xxxr* of low byte of memory bus2FhEnable cache before system BIOS shadow32hTest CPU bus-clock frequency33hInitialize Phoenix Dispatch Manager36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3DhLoad alternate registers with CMOS values31hInitialize extended memory for RomPilot41hInitialize interrupt vectors45hPOST device initialization46h2-1-2-3Check video configuration against CMOS49hInitialize PCI bus and devices4AhInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4FhInitialize all video adapters in system	20h	1-3-1-1	Test DRAM refresh
24hSet ES segment register to 4 GB28hAuto size DRAM29hInitialize POST Memory Manager2AhClear 512 kB base RAM2Ch1-3-4-1RAM failure on address line xxx*2Eh1-3-4-3RAM failure on data bits xxx* of low byte of memory bus2FhEnable cache before system BIOS shadow32hTest CPU bus-clock frequency33hInitialize Phoenix Dispatch Manager36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize interrupt vectors45hPOST device initialization46h2-1-2-347hInitialize 120 support48hCheck ROM copyright notice47hInitialize alt video configuration against CMOS49hInitialize alt video adapters in system48hQuietBoot start (optional)42hInitialize PCI bus and devices44hInitialize MultiBoot	22h	1-3-1-3	Test 8742 Keyboard Controller
28hAuto size DRAM29hInitialize POST Memory Manager2AhClear 512 kB base RAM2Ch1-3-4-1RAM failure on address line xxxx*2Eh1-3-4-3RAM failure on data bits xxxx* of low byte of memory bus2FhEnable cache before system BIOS shadow32hTest CPU bus-clock frequency33hInitialize Phoenix Dispatch Manager36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize extended memory for RomPilot42hInitialize interrupt vectors45hPOST device initialization46h2-1-2-347hInitialize I20 support48hCheck ROM copright notice47hInitialize PCI bus and devices4AhInitialize all video adapters in system4BhQuietBoot start (optional)4EhDisplay BIOS Copyright notice4FhInitialize MultiBoot	24h		Set ES segment register to 4 GB
29hInitialize POST Memory Manager2AhClear 512 kB base RAM2Ch1-3-4-1RAM failure on address line xxx*2Eh1-3-4-3RAM failure on data bits xxxx* of low byte of memory bus2FhEnable cache before system BIOS shadow32hTest CPU bus-clock frequency33hInitialize Phoenix Dispatch Manager36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize interrupt vectors45hPOST device initialization46h2-1-2-347hInitialize 120 support48hCheck video configuration against CMOS49hInitialize all video adapters in system48hQuietBoot start (optional)42hInitialize ID support48hQuietBoot start (optional)44hInitialize All video adapters in system45hPOST device offiguration against CMOS49hInitialize All video adapters in system48hQuietBoot start (optional)42hShadow video BIOS ROM44FhInitialize MultiBoot	28h		Auto size DRAM
2AhClear 512 kB base RAM2Ch1-3-4-1RAM failure on address line xxxx*2Eh1-3-4-3RAM failure on data bits xxxx* of low byte of memory bus2FhEnable cache before system BIOS shadow32hTest CPU bus-clock frequency33hInitialize Phoenix Dispatch Manager36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize interrupt vectors45hPOST device initialization46h2-1-2-3Check ROM copyright notice47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize alt video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4FhInitialize all video adapters in system	29h		Initialize POST Memory Manager
2Ch1-3-4-1RAM failure on address line xxxx*2Eh1-3-4-3RAM failure on data bits xxxx* of low byte of memory bus2FhEnable cache before system BIOS shadow32hTest CPU bus-clock frequency33hInitialize Phoenix Dispatch Manager36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize interrupt vectors45hPOST device initialization46h2-1-2-3Check ROM copyright notice47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize att (optional)4ChShadow video BIOS ROM	2Ah		Clear 512 kB base RAM
2Eh1-3-4-3RAM failure on data bits xxx* of low byte of memory bus2FhEnable cache before system BIOS shadow32hTest CPU bus-clock frequency33hInitialize Phoenix Dispatch Manager36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize interrupt vectors45hPOST device initialization46h2-1-2-3Check ROM copyright notice47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4EhDisplay BIOS copyright notice	2Ch	1-3-4-1	RAM failure on address line xxxx*
2FhEnable cache before system BIOS shadow32hTest CPU bus-clock frequency33hInitialize Phoenix Dispatch Manager36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize extended memory for RomPilot42hInitialize interrupt vectors45hPOST device initialization46h2-1-2-3Check ROM copyright notice47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4EhDisplay BIOS copyright notice	2Eh	1-3-4-3	RAM failure on data bits xxxx* of low byte of memory bus
32hTest CPU bus-clock frequency33hInitialize Phoenix Dispatch Manager36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize extended memory for RomPilot42hInitialize interrupt vectors45hPOST device initialization46h2-1-2-3Check ROM copyright notice47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4EhDisplay BIOS copyright notice	2Fh		Enable cache before system BIOS shadow
33hInitialize Phoenix Dispatch Manager36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize extended memory for RomPilot42hInitialize interrupt vectors45hPOST device initialization46h2-1-2-347hInitialize I20 support48hCheck video configuration against CMOS49hInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4FhInitialize MultiBoot	32h		Test CPU bus-clock frequency
36hWarm start shut down38hShadow system BIOS ROM3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize extended memory for RomPilot42hInitialize interrupt vectors45hPOST device initialization46h2-1-2-347hInitialize 120 support48hCheck ROM copyright notice49hInitialize PCI bus and devices4AhInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4EhDisplay BIOS copyright notice	33h		Initialize Phoenix Dispatch Manager
38hShadow system BIOS ROM3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize extended memory for RomPilot42hInitialize interrupt vectors45hPOST device initialization46h2-1-2-3Check ROM copyright notice47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4EhDisplay BIOS copyright notice4FhInitialize MultiBoot	36h		Warm start shut down
3AhAuto size cache3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize extended memory for RomPilot42hInitialize interrupt vectors45hPOST device initialization46h2-1-2-3Check ROM copyright notice47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4FhInitialize MultiBoot	38h		Shadow system BIOS ROM
3ChAdvanced configuration of chipset registers3DhLoad alternate registers with CMOS values41hInitialize extended memory for RomPilot42hInitialize interrupt vectors45hPOST device initialization46h2-1-2-347hInitialize I20 support48hCheck video configuration against CMOS49hInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4EhDisplay BIOS copyright notice	3Ah		Auto size cache
3DhLoad alternate registers with CMOS values41hInitialize extended memory for RomPilot42hInitialize interrupt vectors45hPOST device initialization46h2-1-2-347hInitialize I20 support48hCheck ROM copyright notice49hInitialize PCI bus and devices4AhInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4FhInitialize MultiBoot	3Ch		Advanced configuration of chipset registers
41hInitialize extended memory for RomPilot42hInitialize interrupt vectors45hPOST device initialization46h2-1-2-3Check ROM copyright notice47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize PCI bus and devices4AhInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4EhDisplay BIOS copyright notice4FhInitialize MultiBoot	3Dh		Load alternate registers with CMOS values
42hInitialize interrupt vectors45hPOST device initialization46h2-1-2-3Check ROM copyright notice47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize PCI bus and devices4AhInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4EhDisplay BIOS copyright notice4FhInitialize MultiBoot	41h		Initialize extended memory for RomPilot
45hPOST device initialization46h2-1-2-3Check ROM copyright notice47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize PCI bus and devices4AhInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4EhDisplay BIOS copyright notice4FhInitialize MultiBoot	42h		Initialize interrupt vectors
46h2-1-2-3Check ROM copyright notice47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize PCI bus and devices4AhInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4EhDisplay BIOS copyright notice4FhInitialize MultiBoot	45h		POST device initialization
47hInitialize I20 support48hCheck video configuration against CMOS49hInitialize PCI bus and devices4AhInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4EhDisplay BIOS copyright notice4FhInitialize MultiBoot	46h	2-1-2-3	Check ROM copyright notice
48hCheck video configuration against CMOS49hInitialize PCI bus and devices4AhInitialize all video adapters in system4BhQuietBoot start (optional)4ChShadow video BIOS ROM4EhDisplay BIOS copyright notice4FhInitialize MultiBoot	47h		Initialize I20 support
49h Initialize PCI bus and devices 4Ah Initialize all video adapters in system 4Bh QuietBoot start (optional) 4Ch Shadow video BIOS ROM 4Eh Display BIOS copyright notice 4Fh Initialize MultiBoot	48h		Check video configuration against CMOS
4Ah Initialize all video adapters in system 4Bh QuietBoot start (optional) 4Ch Shadow video BIOS ROM 4Eh Display BIOS copyright notice 4Fh Initialize MultiBoot	49h		Initialize PCI bus and devices
4Bh QuietBoot start (optional) 4Ch Shadow video BIOS ROM 4Eh Display BIOS copyright notice 4Fh Initialize MultiBoot	4Ah		Initialize all video adapters in system
4Ch Shadow video BIOS ROM 4Eh Display BIOS copyright notice 4Fh Initialize MultiBoot	4Bh		QuietBoot start (optional)
4Eh Display BIOS copyright notice 4Fh Initialize MultiBoot	4Ch		Shadow video BIOS ROM
4Fh Initialize MultiBoot	4Eh		Display BIOS copyright notice
	4Fh		Initialize MultiBoot

50h		Display CPU type and speed
51h		Initialize EISA board
52h		Test keyboard
54h		Set key click if enabled
55h		Enable USB devices
58h	2-2-3-1	Test for unexpected interrupts
59h	2251	Initialize POST display service
5Ah		Display prompt "Press F2 to enter SETUP"
5Bh		Disable CPU cache
5Ch		Test RAM between 512 and 640 kB
60h		Test extended memory
62h		Test extended memory address lines
64h		Jump to UserPatch1
66h		Configure advanced cache registers
67h		Initialize Multi Processor A PIC
68h		Enable external and CPU caches
60h		Satur System Management Mode (SMM) area
64h		Display external L2 cooke size
6Dh		L and system defaults (antional)
0DII 6Ch		Load custom defautis (optional)
OCN		Display shadow-area message
6En		Display possible high address for UMB recovery
70h		Display error messages
/2h		Check for configuration errors
76h		Check for keyboard errors
7Ch		Set up hardware interrupt vectors
7Dh		Initialize Intelligent System Monitoring
7Eh		Initialize coprocessor if present
80h		Disable onboard Super I/O ports and IRQs
81h		Late POST device initialization
82h		Detect and install external RS232 ports
83h		Configure non-MCD IDE controllers
84h		Detect and install external parallel ports
85h		Initialize PC-compatible PnP ISA devices
86h		Re-initialize onboard I/O ports.
87h		Configure Motherboard Configurable Devices (optional)
88h		Initialize BIOS Data Area
89h		Enable Non-Maskable Interrupts (NMIs)
8Ah		Initialize Extended BIOS Data Area
8Bh		Test and initialize PS/2 mouse
8Ch		Initialize floppy controller
8Fh		Determine number of ATA drives (optional)
90h		Initialize hard-disk controllers
91h		Initialize local-bus hard-disk controllers
92h		Jump to UserPatch2
93h		Build MPTABLE for multi-processor boards
95h		Install CD ROM for boot
96h		Clear huge ES segment register
97h		Fix up Multi Processor table
98h	1-2	Search for option ROMs. One long, two short beeps on checksum failure
99h		Check for SMART Drive (optional)
9Ah		Shadow option ROMs
9Ch		Set up Power Management
9Dh		Initialize security engine (optional)
9Eh		Enable hardware interrupts
9Fh		Determine number of ATA and SCSI drives
A0h		Set time of day
A2h		Check key lock
A4h		Initialize typematic rate

4.01	1	
Aðn		Erase F2 prompt
AAh		Scan for F2 key stroke
ACh		Enter SETUP
AEh		Clear Boot flag
B0h		Check for errors
B1h		Inform RomPilot about the end of POST.
B2h		POST done - prepare to boot operating system
B4h	1	One short been before boot
B 5h	1	Terminate OujetBoot (ontional)
B6h		Check password (optional)
B0h B7h		Initialize A CDI BIOS
D/II D0h		Dremore De et
D911		
BAn		
BBh		Initialize PnP Option ROMs
BCh		Clear parity checkers
BDh		Display MultiBoot menu
BEh		Clear screen (optional)
BFh		Check virus and backup reminders
C0h		Try to boot with INT 19
C1h		Initialize POST Error Manager (PEM)
C2h		Initialize error logging
C3h		Initialize error display function
C4h		Initialize system error handler
C5h		PnPnd dual (MOS (ontional)
Cóh		Initializa noto deal: (optional)
COll		Initialize note dock (optional)
C/h		
C8h		Force check (optional)
C9h		Extended checksum (optional)
CAh		Redirect Int 15h to enable remote keyboard
CBh		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and
CBh		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk
CBh CCh		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video
CBh CCh CDh		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA
CBh CCh CDh CEh		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message
CBh CCh CDh CEh D2h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt
CBh CCh CDh CEh D2h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM
CBh CCh CDh CEh D2h E0h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset
CBh CCh CDh CEh D2h E0h E1h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge
CBh CCh CDh CEh D2h E0h E1h E2h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the CPU
CBh CCh CDh CEh D2h E0h E1h E2h E3h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the CPU Initialize system timer
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize system timer Initialize system I/O
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the cPU Initialize system timer Initialize system I/O Check force recovery boot
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the CPU Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the CPU Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the CPU Initialize system timer Initialize system timer Initialize system I/O Checks force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the chipset Initialize the CPU Initialize system timer Initialize system timer Initialize system I/O Checks force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize Multi Processor
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAL		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the chipset Initialize the CPU Initialize system timer Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize Multi Processor Initialize Multi Processor
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAh		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the CPU Initialize system timer Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize OEM special code
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAh EBh		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the bridge Initialize system timer Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize OEM special code Initialize PIC and DMA
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAh EBh ECh		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the bridge Initialize system timer Initialize system timer Initialize system timer Initialize System I/O Check force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize OEM special code Initialize PIC and DMA Initialize Memory type
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAh EBh ECh EDh		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize system timer Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize OEM special code Initialize II PIC and DMA Initialize Memory type Initialize Memory size
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAh EBh ECh EBh ECh EEh		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the bridge Initialize the CPU Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize OEM special code Initialize Memory type Initialize Memory type Initialize Memory size Shadow Boot Block
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAh EBh ECh EBh ECh EFh		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the chipset Initialize the bridge Initialize the CPU Initialize system timer Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize PIC and DMA Initialize Memory type Initialize Memory type Initialize Memory test
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAh EBh ECh EFh F0h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the bridge Initialize system timer Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize OEM special code Initialize Memory type Initialize Memory type Initialize Memory type Initialize Memory type Initialize Interrupt vectors
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAh EBh ECh EFh F0h F1h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the bridge Initialize system timer Initialize system timer Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize OEM special code Initialize PIC and DMA Initialize Memory type Initialize Memory size Shadow Boot Block System memory test Initialize Interrupt vectors Initialize Run Time Clock
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAh EBh ECh EFh F0h F1h F2h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the bridge Initialize system timer Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize OEM special code Initialize Multi Processor Initialize Memory type Initialize Memory size Shadow Boot Block System memory test Initialize interrupt vectors Initialize Run Time Clock Initialize video
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAh EBh ECh EFh F0h F1h F2h F3h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the chipset Initialize the CPU Initialize system timer Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize OEM special code Initialize Multi Processor Initialize Memory type Initialize Memory type Initialize Memory type Initialize Memory test Initialize Interrupt vectors Initialize Run Time Clock Initialize video Initialize Video
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAh EBh ECh EFh F0h F1h F2h F3h F4h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the bridge Initialize system timer Initialize system I/O Check force recovery boot Checksum BIOS ROM Go to BIOS Set Huge Segment Initialize PIC and DMA Initialize Memory type Initialize Memory type Initialize Memory type Initialize Memory test Initialize interrupt vectors Initialize Run Time Clock Initialize Video Initialize System Management Manager Outnut one been
CBh CCh CDh CEh D2h E0h E1h E2h E3h E4h E5h E6h E7h E8h E9h EAh EBh ECh EFh F0h F1h F2h F3h F4h F5h		Redirect Int 13h to Memory Technologies Devices such as ROM, RAM, PCMCIA, and serial disk Redirect Int 10h to enable remote serial video Re-map I/O and memory for PCMCIA Initialize digitizer and display message Unknown interrupt The following are for boot block in Flash ROM Initialize the chipset Initialize the bridge Initialize the bridge Initialize system timer Initialize system timer Initialize system timer Initialize MIOS Set Huge Segment Initialize Multi Processor Initialize Memory size Shadow Boot Block System memory test Initialize interrupt vectors Initialize Run Time Clock Initialize System Management Manager Output one beep

F6h	Boot to Mini DOS
F7h	Boot to Full DOS

Customer Service

This section provides contact information should you need technical support for your system, or need to return merchandise.

Technical Support

If you should encounter difficulties with your application or with this product, or need guidance on setting up your system, we are ready to assist you. Please contact our Technical Support department at the following locations:

USA:

Technical Support hours are: 7:00AM to 6:00PM – Monday – Friday TEL: (888) 343-5396 (Toll free in US and Canada) (952) 974-7200 FAX: (952) 949-2791 E-mail: support@kontronmobile.com

Europe, Middle East, Africa:

 TEL:
 (+49) 8165-77 112

 FAX:
 (+49) 8165-77 110

 E-mail:
 techsup@kontron.com

Kontron Asia (except China):

TEL: 011-886-2-2910-3532 FAX: 011-886-2-2910-3482

Sales Contact:

E-mail: sales@kontron-asia.com

Technical Support Contact:

E-mail: support@kontron-asia.com

Kontron China:

 TEL:
 +86 21 5426 1660

 FAX:
 +86 21 5426 1650

 E-mail:
 FAE@kontron.com.cn

Technical Support Contact:

E-mail: FAE@kontron.com.cn

When you call, make sure to have the following information on hand:

- unit part number (P/No #),
- serial number (S/No #) of the defective unit (found on the back of the unit).

Then, explain the nature of your problem to the service technician.

If you have any questions about Kontron Mobile Computing, or our products and services, you may reach us at the aforementioned telephone numbers, by e-mail, or by writing to:

Kontron Mobile Computing Inc. 7631 Anagram Drive Eden Prairie, MN 55344 USA

Returning Defective Merchandise

Before returning any merchandise, please follow these instructions:

1. In the USA / North America, contact:

KMC Technical Support Technical Support hours are: 7:00AM to 6:00PM – Monday – Friday TEL: (888) 343-5396 (Toll free in US and Canada) (952) 974-7200 FAX: (952) 949-2791 E-mail: support@kontronmobile.com

In Europe:

Contact our Service Department and request an RMA # (Return Material Authorization) by: Fax: (+49) 8165-77 331 E-mail: <u>service@kontron.com</u>

In **Asia**:

Contact your sales representative and request an RMA # (Return Material Authorization) by: FAX: 011-886-2-2910-3482 E-mail: sales@kontron-asia.com

In China:

Contact your sales representative and request an RMA # (Return Material Authorization) by: FAX: +86 21 5426 1650 E-mail: FAE@kontron.com.cn

- 2. Make sure that you receive a RMA # from Kontron-Service before returning any merchandise. Clearly write or mark this number on the outside of the package you are returning.
- Include the name and telephone number of a person whom we can contact for further explanations if necessary when returning goods. Where applicable, always include all duty papers and invoice(s) associated with the item(s) in question.
- 4. Ensure that the unit is packed in its original box, if available, or packed to avoid shipping damage.
- 5. Include a copy of the RMA form and problem description.

©2002 Kontron Mobile Computing Inc.

Kontron Mobile Computing 7631 Anagram Drive Eden Prairie, MN 55344-7310 USA