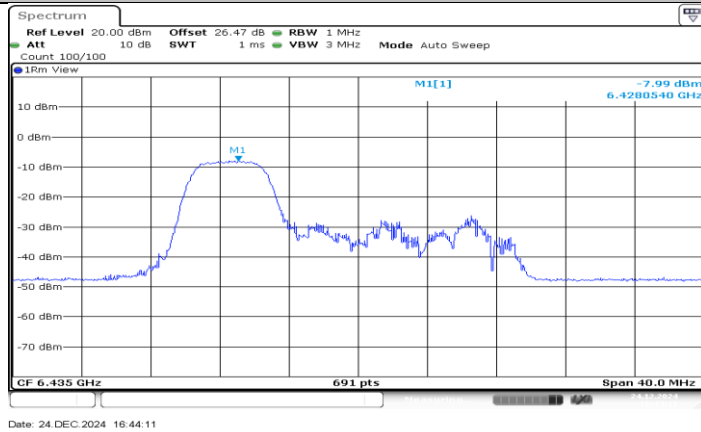
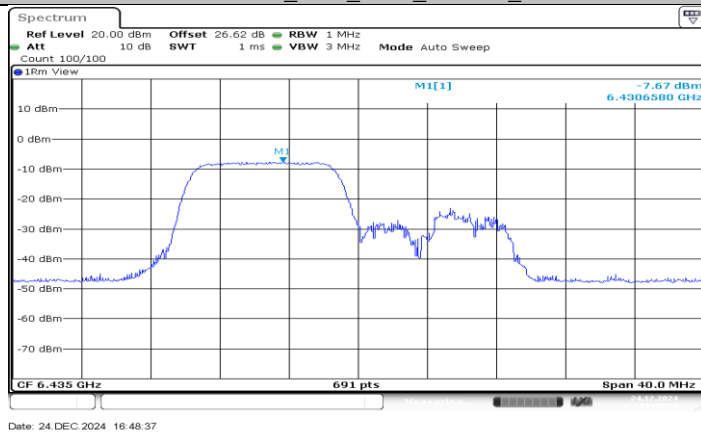


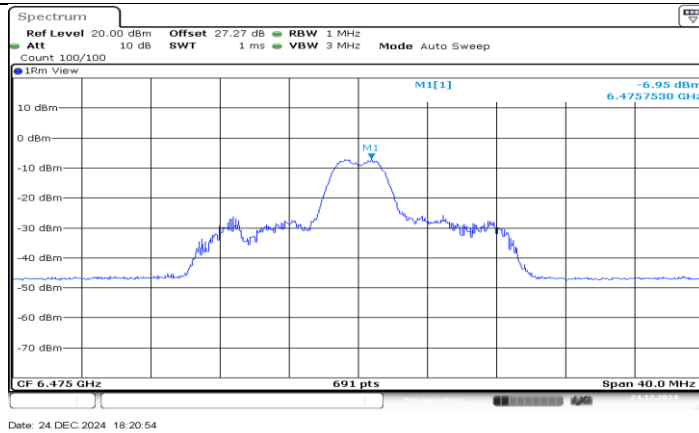
11AX20MIMO_Ant1_6435_26Tone_RU0



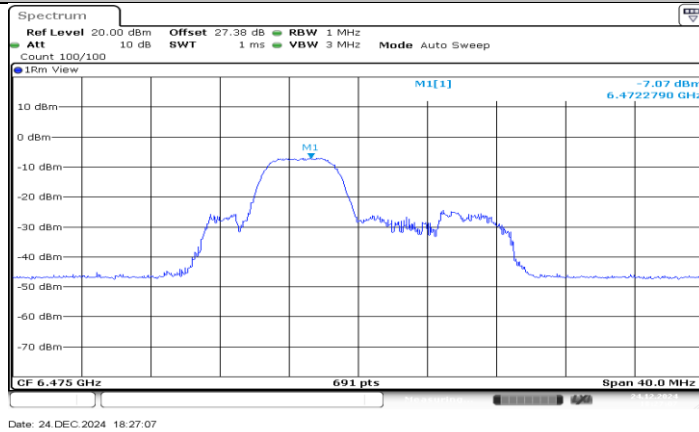
11AX20MIMO_Ant1_6435_52Tone_RU37



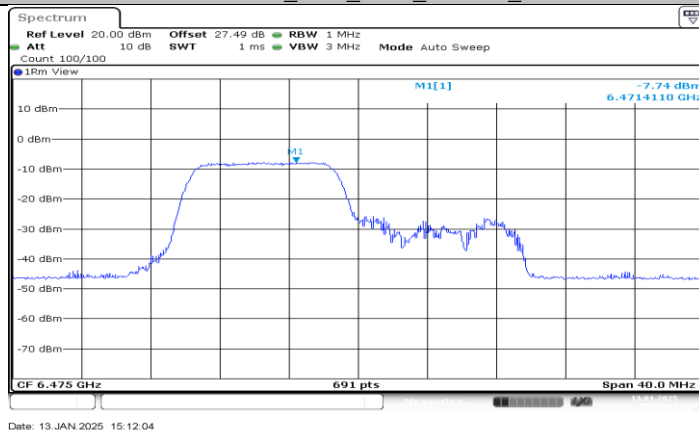
11AX20MIMO_Ant1_6435_106Tone_RU53



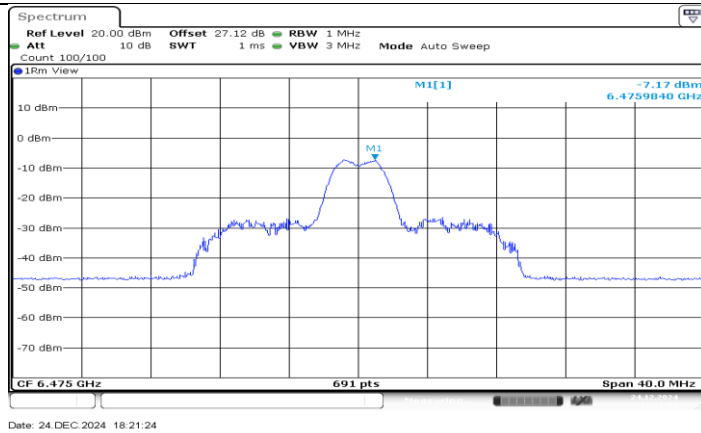
11AX20MIMO_Ant0_6475_26Tone_RU4



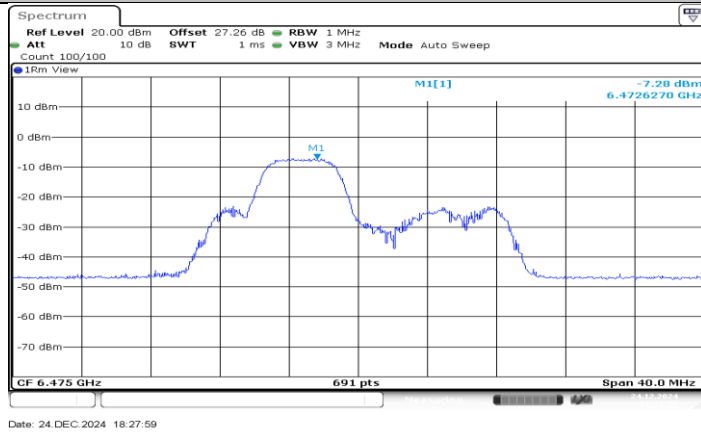
11AX20MIMO_Ant0_6475_52Tone_RU38



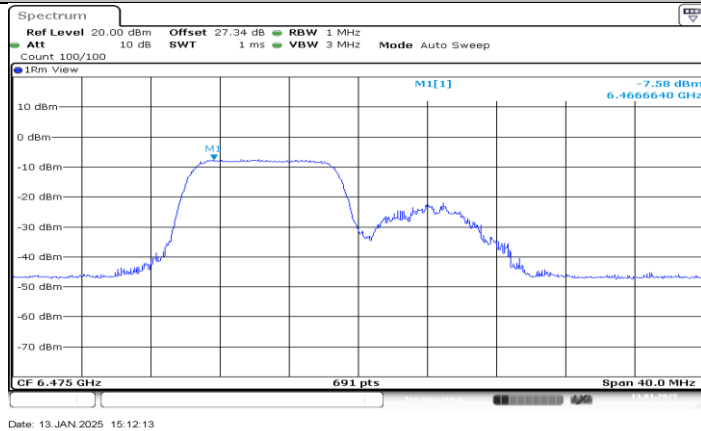
11AX20MIMO_Ant0_6475_106Tone_RU53



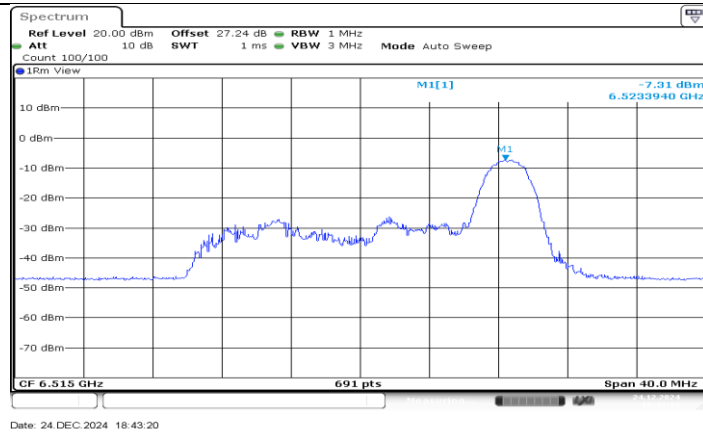
11AX20MIMO_Ant1_6475_26Tone_RU4



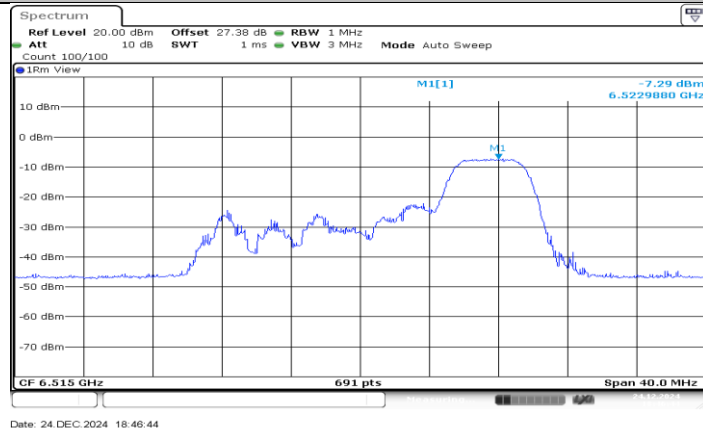
11AX20MIMO_Ant1_6475_52Tone_RU38



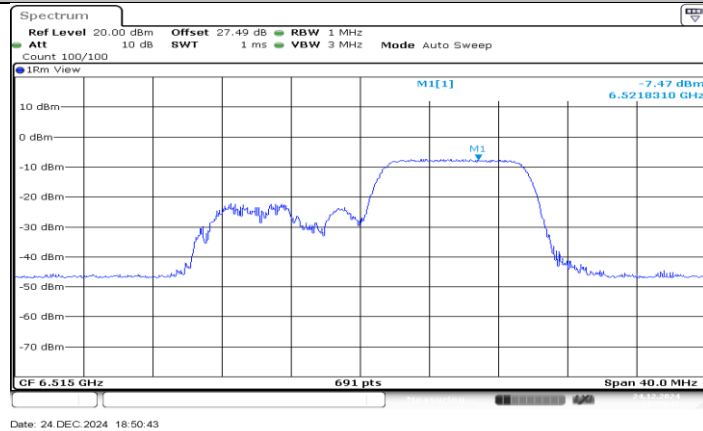
11AX20MIMO_Ant1_6475_106Tone_RU53



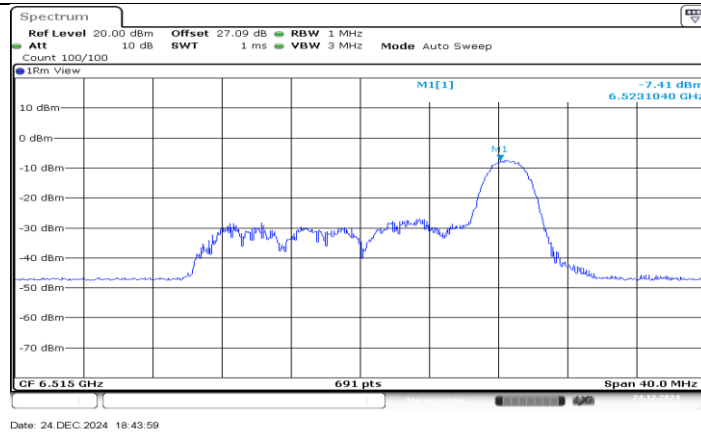
11AX20MIMO_Ant0_6515_26Tone_RU8



11AX20MIMO_Ant0_6515_52Tone_RU40



11AX20MIMO_Ant0_6515_106Tone_RU54



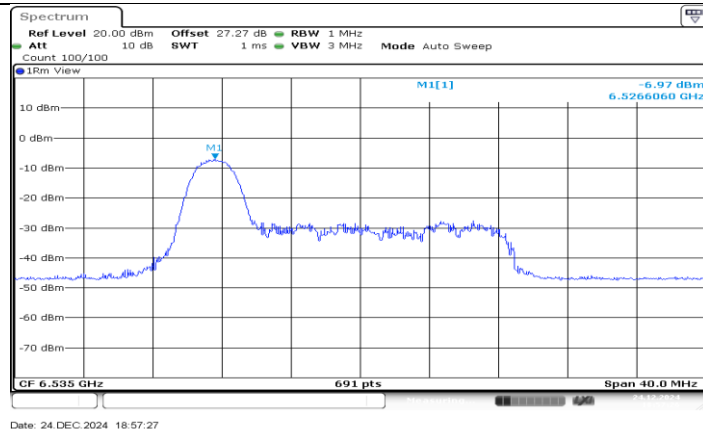
11AX20MIMO_Ant1_6515_26Tone_RU8



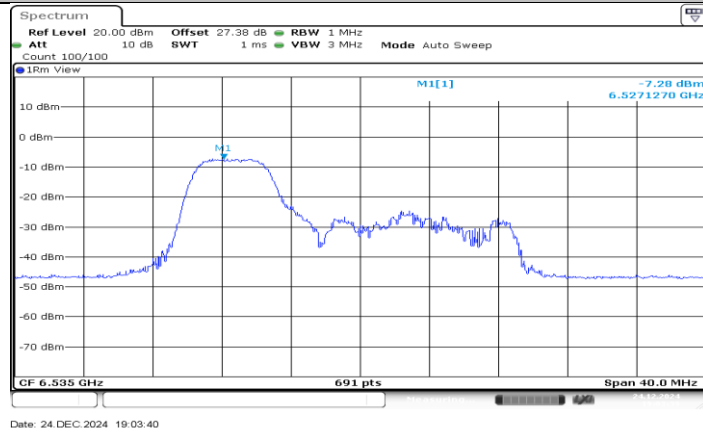
11AX20MIMO_Ant1_6515_52Tone_RU40



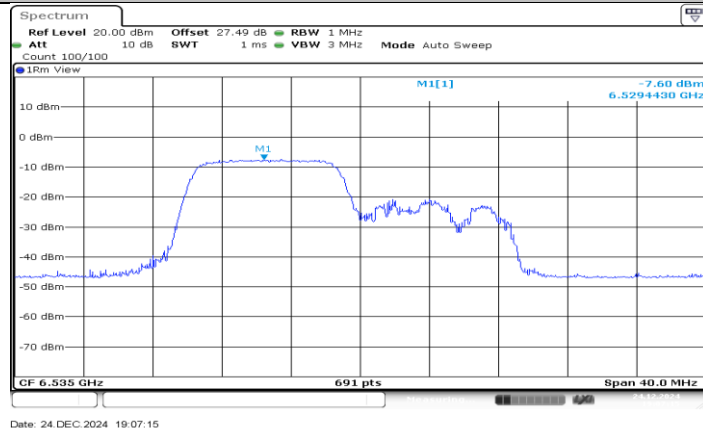
11AX20MIMO_Ant1_6515_106Tone_RU54



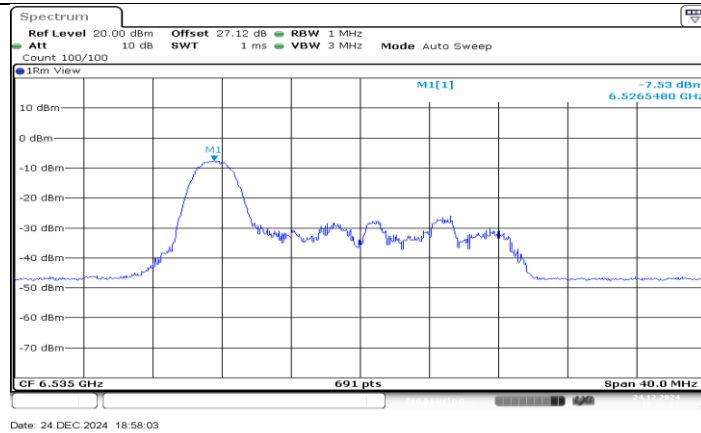
11AX20MIMO_Ant0_6535_26Tone_RU0



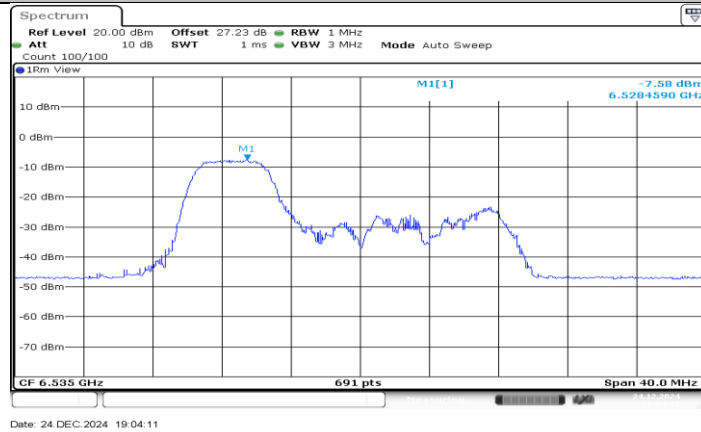
11AX20MIMO_Ant0_6535_52Tone_RU37



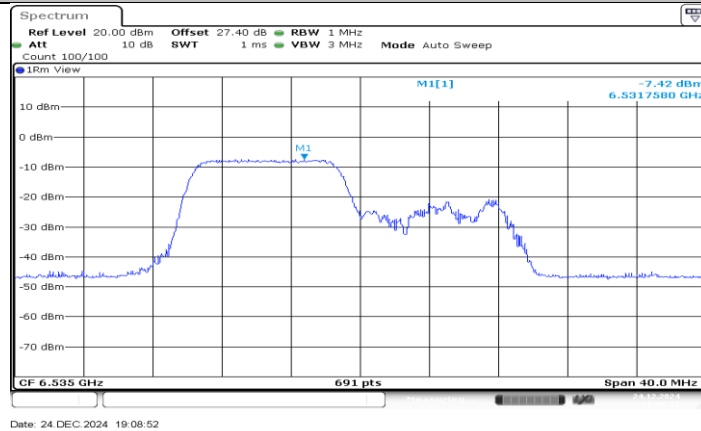
11AX20MIMO_Ant0_6535_106Tone_RU53



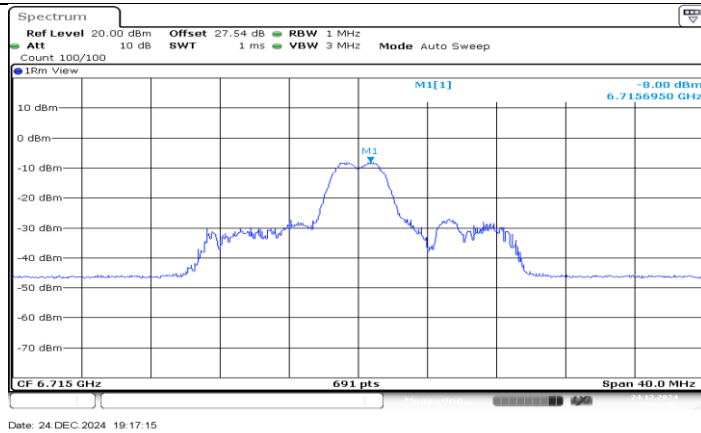
11AX20MIMO_Ant1_6535_26Tone_RU0



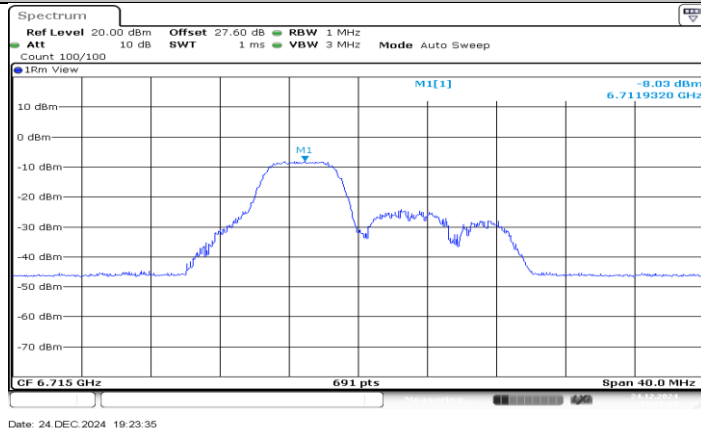
11AX20MIMO_Ant1_6535_52Tone_RU37



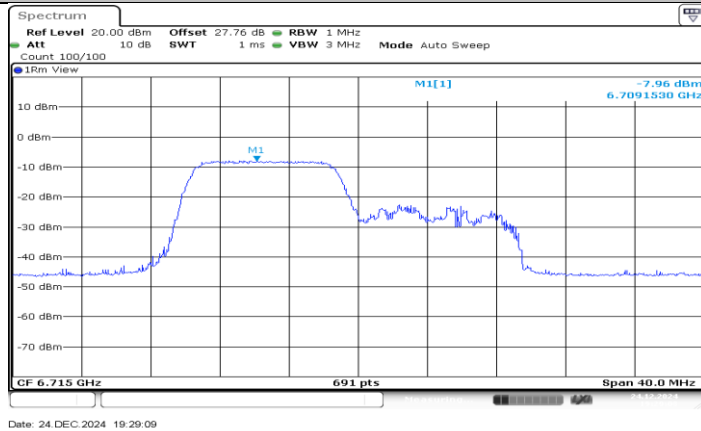
11AX20MIMO_Ant1_6535_106Tone_RU53



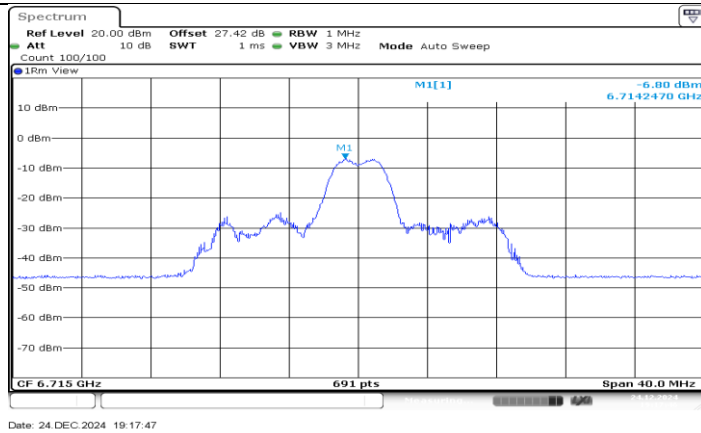
11AX20MIMO_Ant0_6715_26Tone_RU4



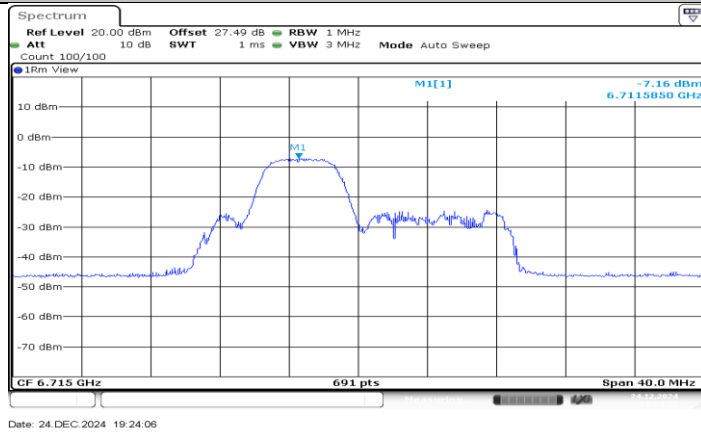
11AX20MIMO_Ant0_6715_52Tone_RU38



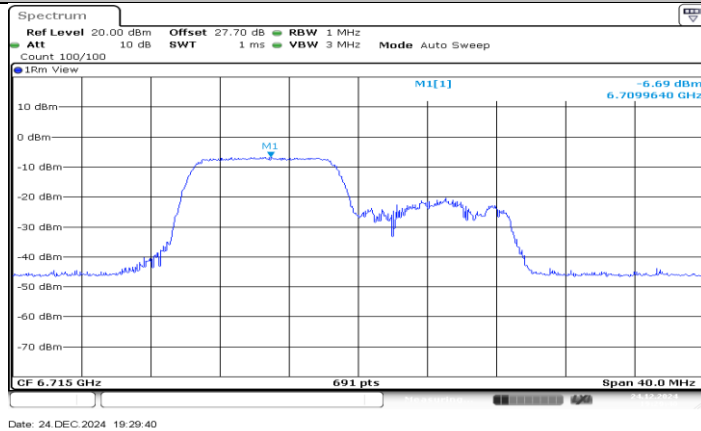
11AX20MIMO_Ant0_6715_106Tone_RU53



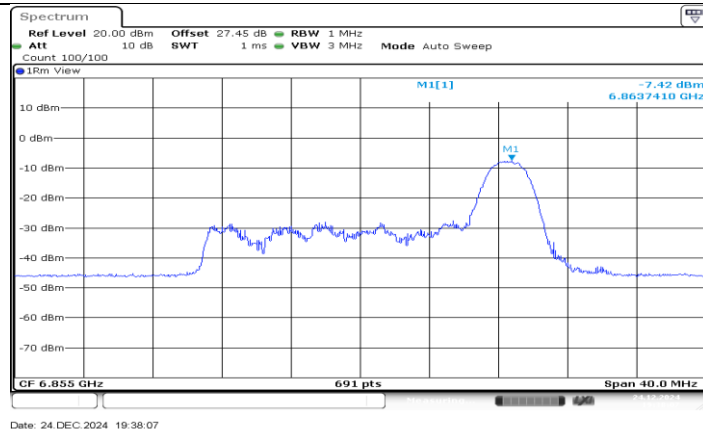
11AX20MIMO_Ant1_6715_26Tone_RU4



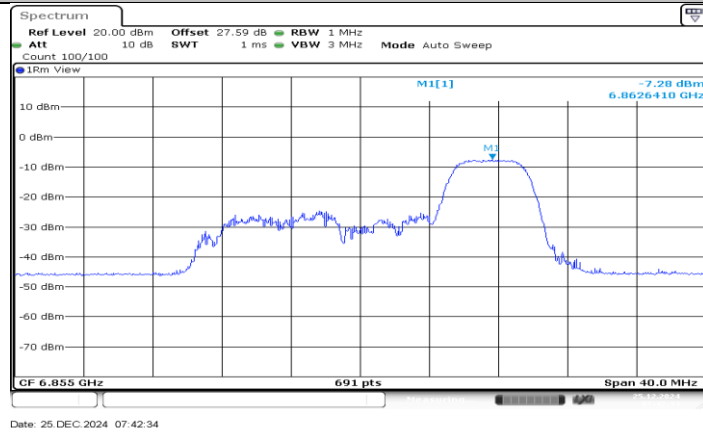
11AX20MIMO_Ant1_6715_52Tone_RU38



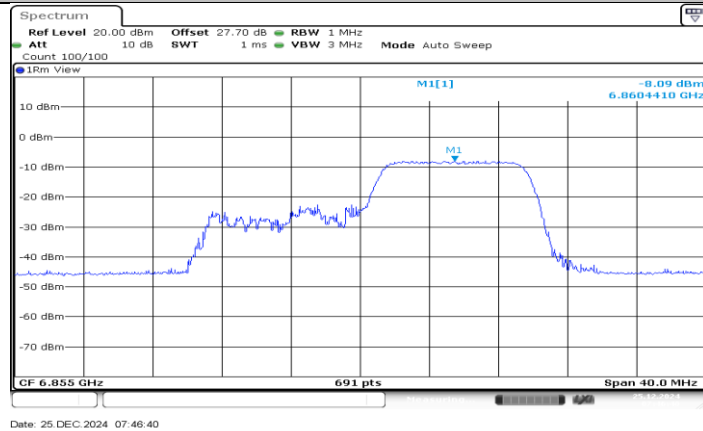
11AX20MIMO_Ant1_6715_106Tone_RU53



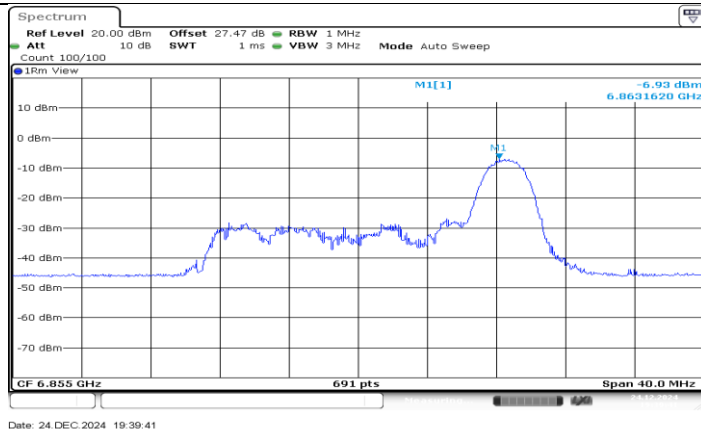
11AX20MIMO_Ant0_6855_26Tone_RU8



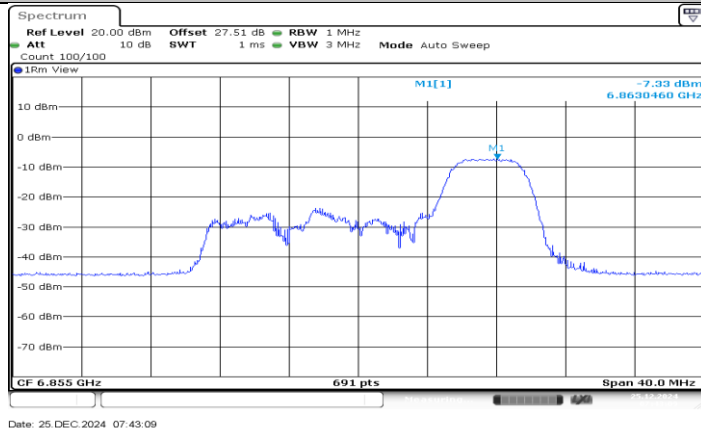
11AX20MIMO_Ant0_6855_52Tone_RU40



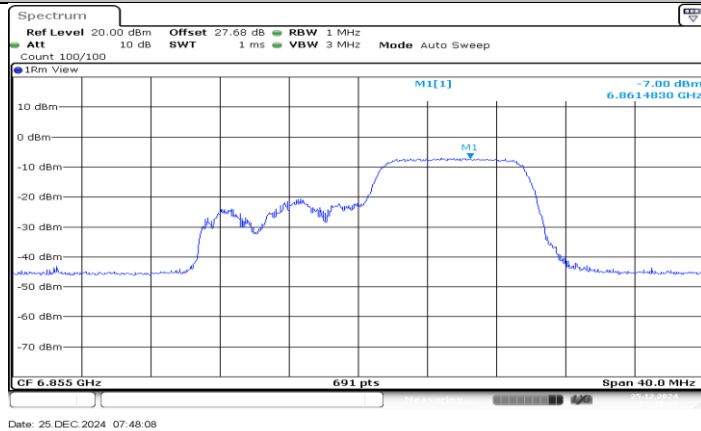
11AX20MIMO_Ant0_6855_106Tone_RU54



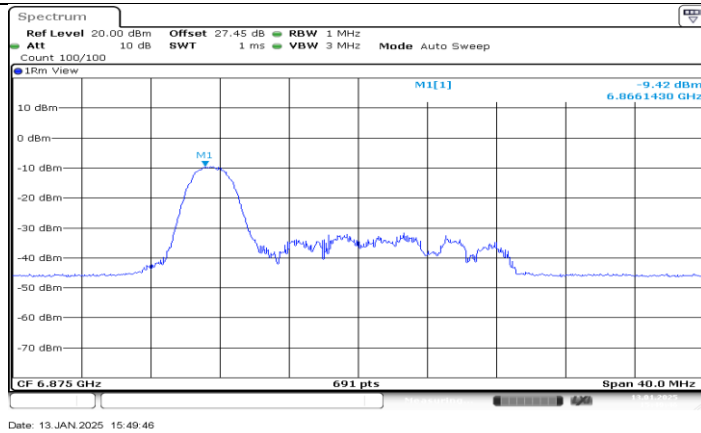
11AX20MIMO_Ant1_6855_26Tone_RU8



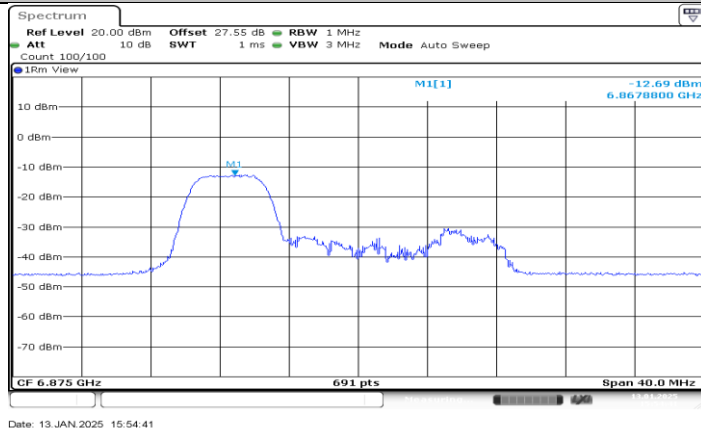
11AX20MIMO_Ant1_6855_52Tone_RU40



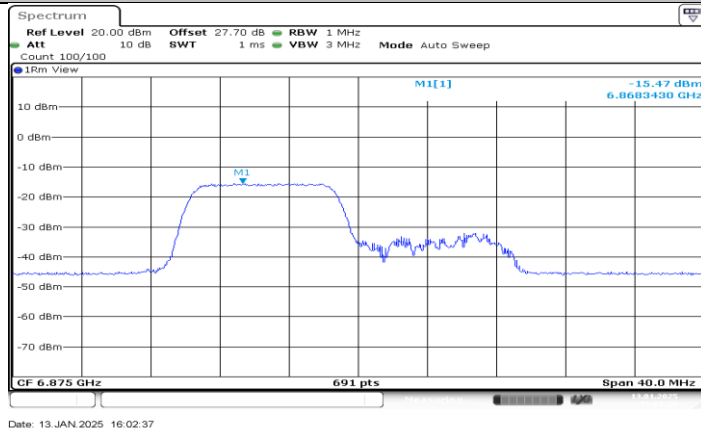
11AX20MIMO_Ant1_6855_106Tone_RU54



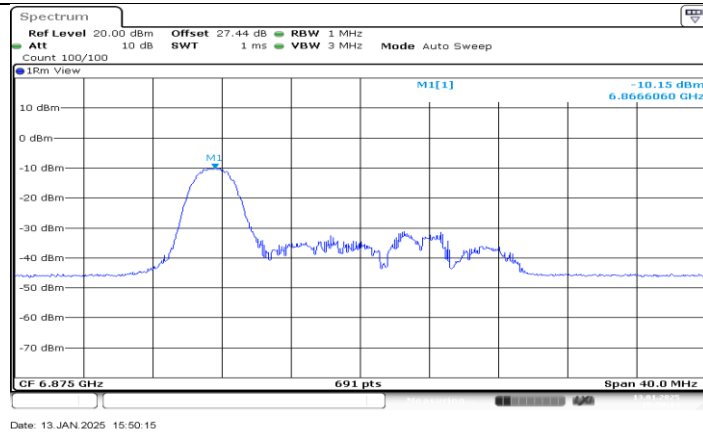
11AX20MIMO_Ant0_6875_26Tone_RU0



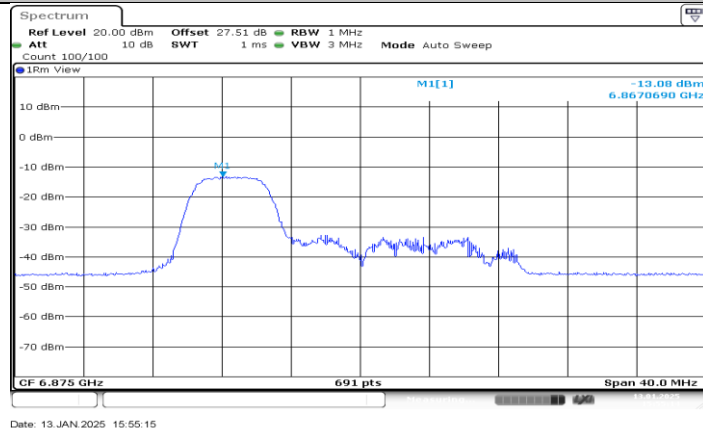
11AX20MIMO_Ant0_6875_52Tone_RU37



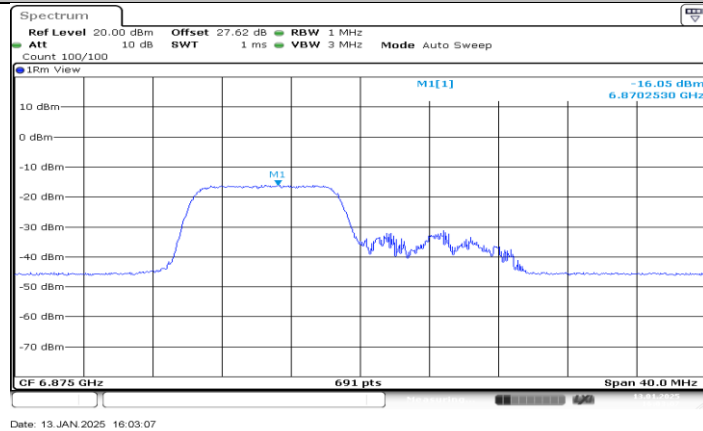
11AX20MIMO_Ant0_6875_106Tone_RU53



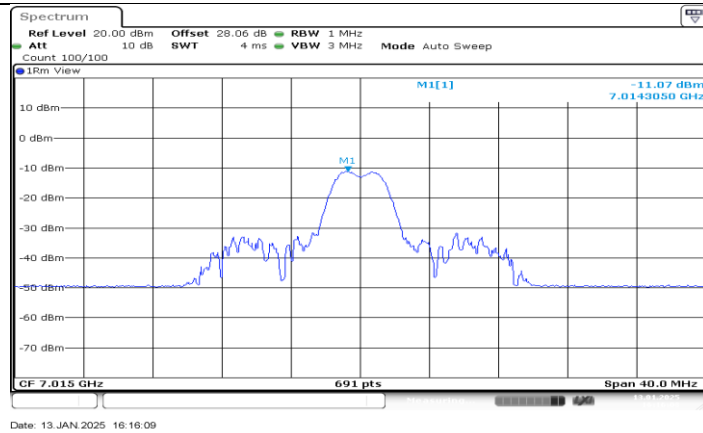
11AX20MIMO_Ant1_6875_26Tone_RU0



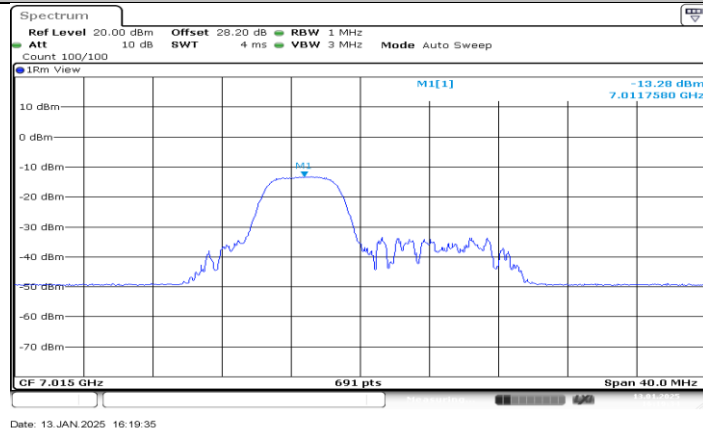
11AX20MIMO_Ant1_6875_52Tone_RU37



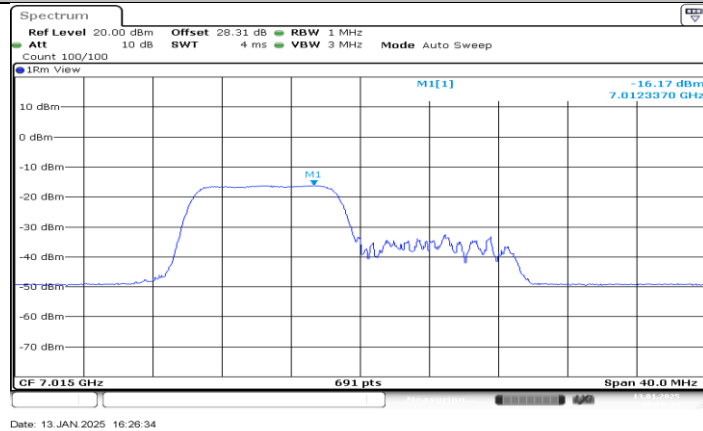
11AX20MIMO_Ant1_6875_106Tone_RU53



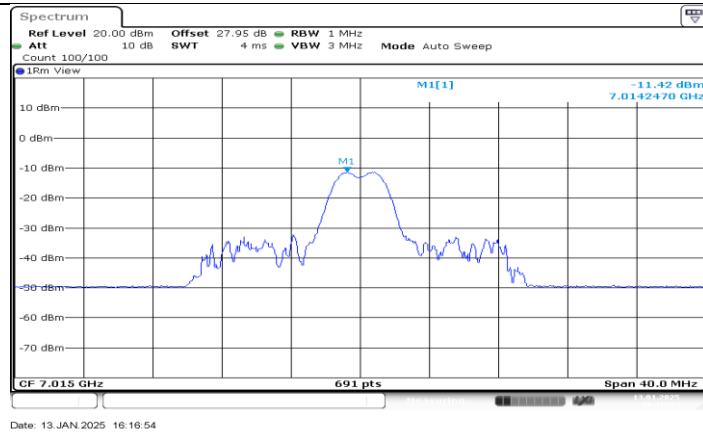
11AX20MIMO_Ant0_7015_26Tone_RU4



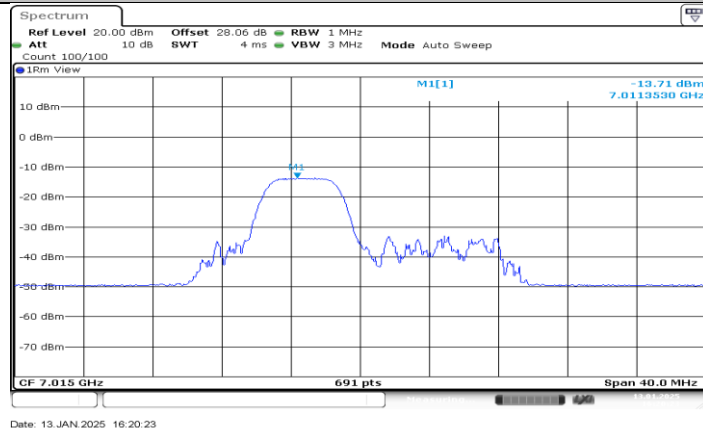
11AX20MIMO_Ant0_7015_52Tone_RU38



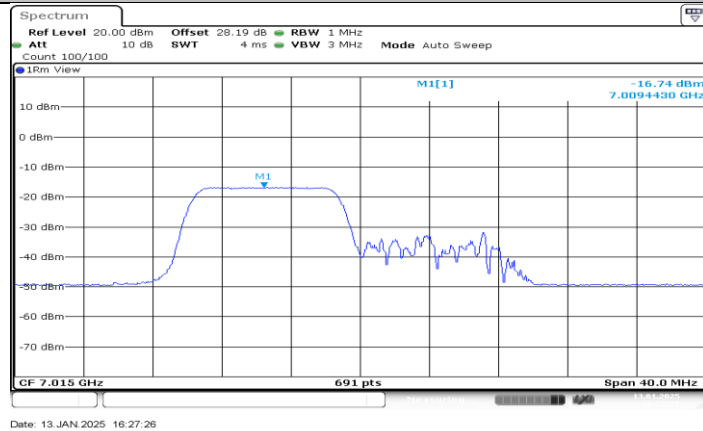
11AX20MIMO_Ant0_7015_106Tone_RU53



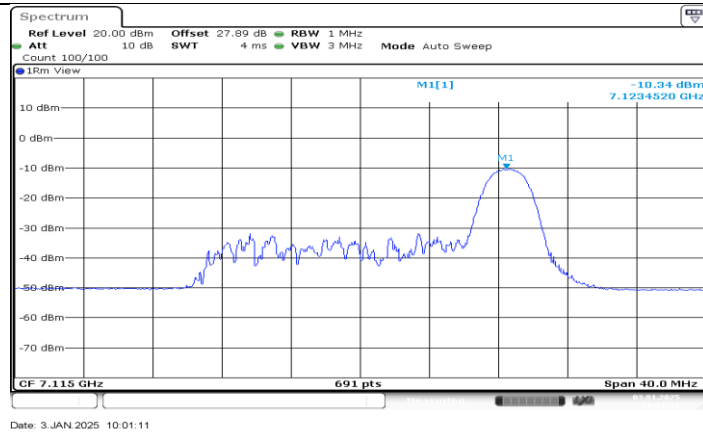
11AX20MIMO_Ant1_7015_26Tone_RU4



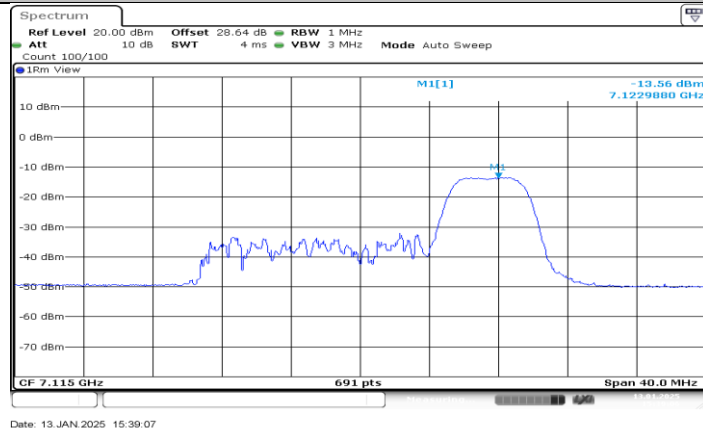
11AX20MIMO_Ant1_7015_52Tone_RU38



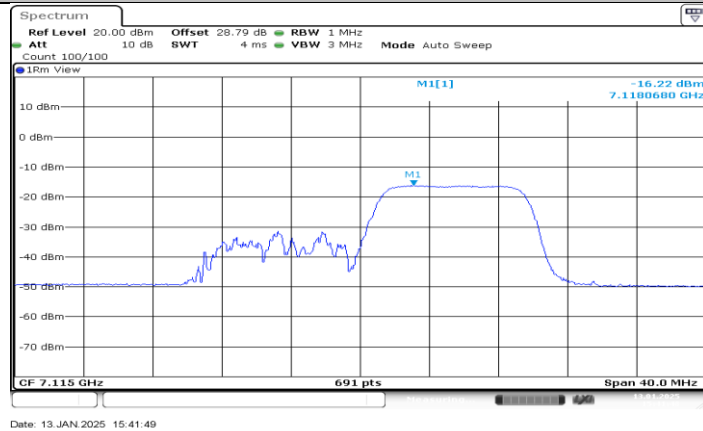
11AX20MIMO_Ant1_7015_106Tone_RU53



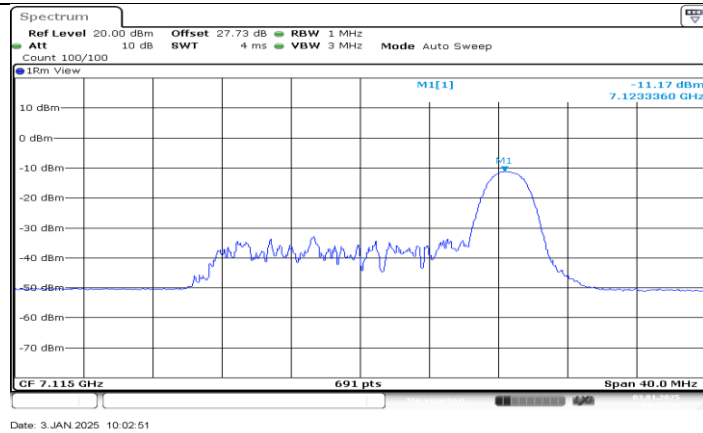
11AX20MIMO_Ant0_7115_26Tone_RU8



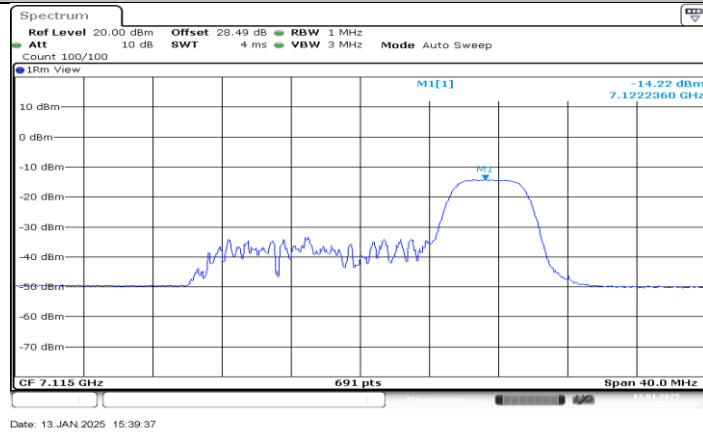
11AX20MIMO_Ant0_7115_52Tone_RU40



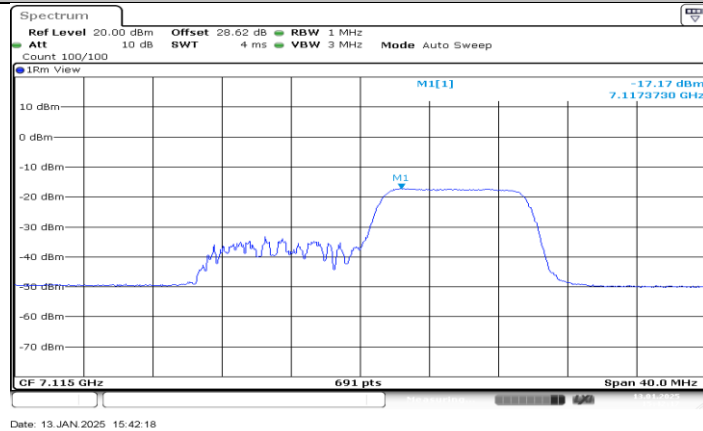
11AX20MIMO_Ant0_7115_106Tone_RU54



11AX20MIMO_Ant1_7115_26Tone_RU8



11AX20MIMO_Ant1_7115_52Tone_RU40



11AX20MIMO_Ant1_7115_106Tone_RU54

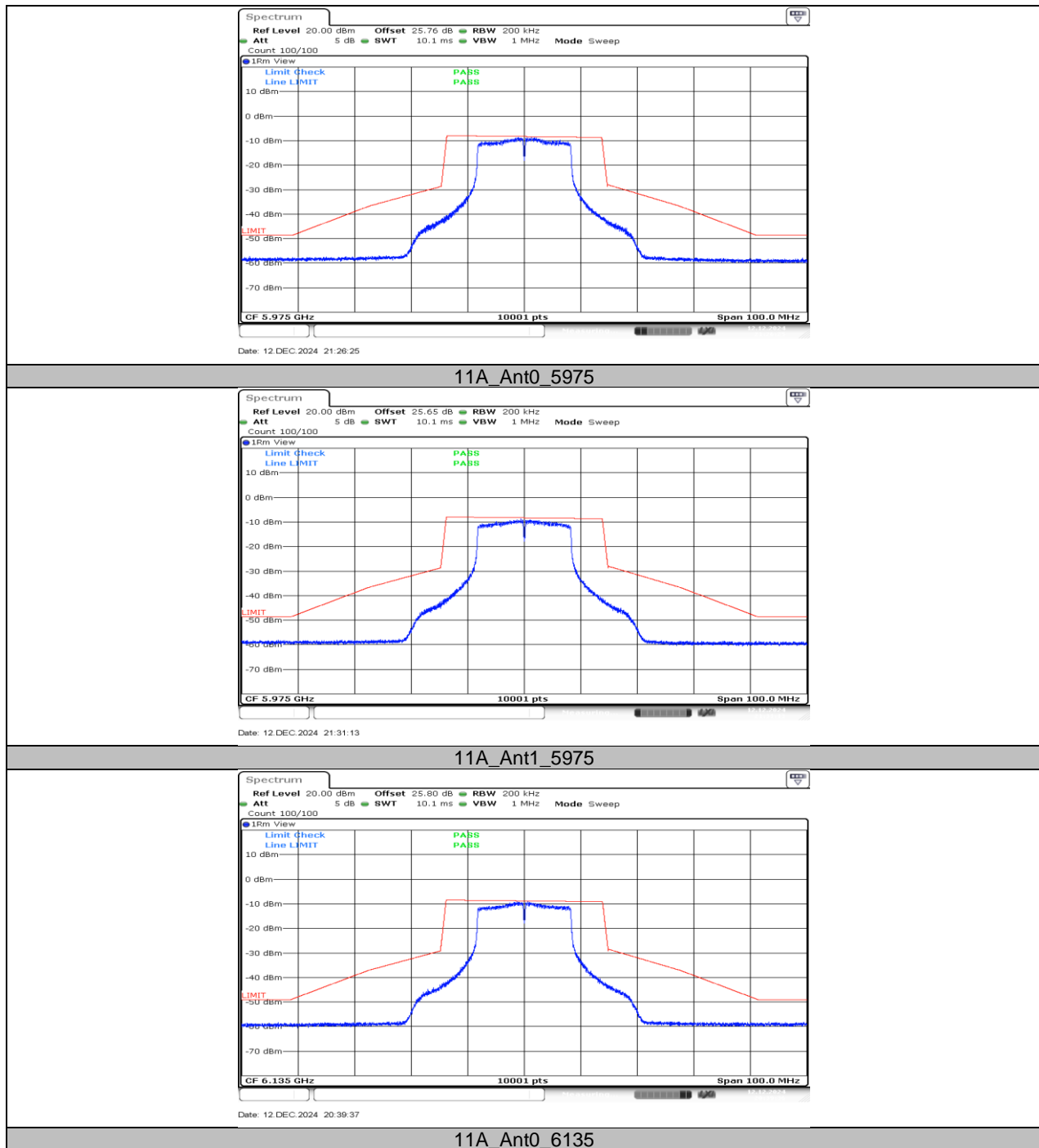
11.6. APPENDIX F: INBAND EMISSIONS

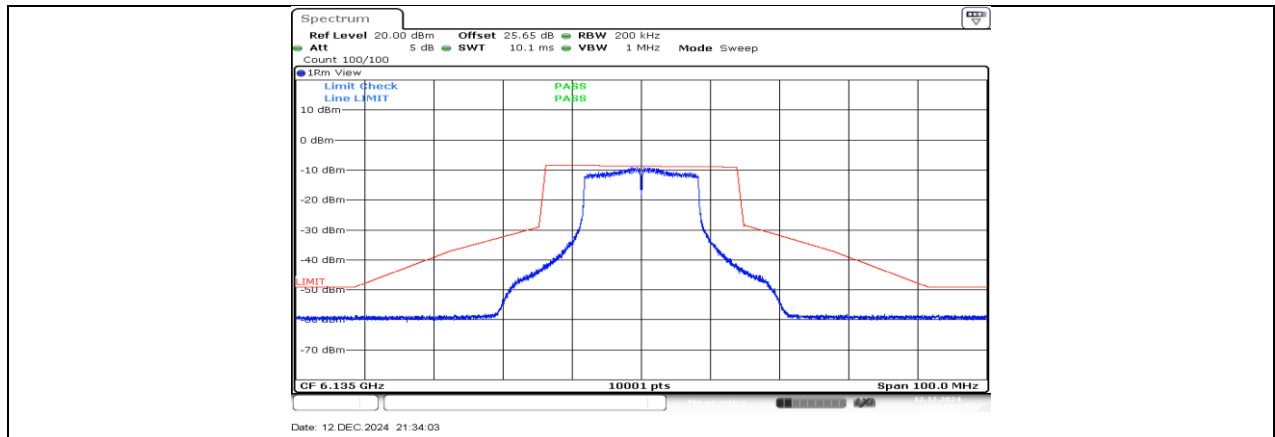
11.6.1. Test Result-SU Mode Worst Case

Test Mode	Antenna	Frequency[MHz]	Result	Limit	Verdict
11A	Ant0	5975	See test graph	See test graph	PASS
	Ant1	5975	See test graph	See test graph	PASS
	Ant0	6135	See test graph	See test graph	PASS
	Ant1	6135	See test graph	See test graph	PASS
	Ant0	6375	See test graph	See test graph	PASS
	Ant1	6375	See test graph	See test graph	PASS
	Ant0	6455	See test graph	See test graph	PASS
	Ant1	6455	See test graph	See test graph	PASS
	Ant0	6535	See test graph	See test graph	PASS
	Ant1	6535	See test graph	See test graph	PASS
	Ant0	6695	See test graph	See test graph	PASS
	Ant1	6695	See test graph	See test graph	PASS
	Ant0	6855	See test graph	See test graph	PASS
	Ant1	6855	See test graph	See test graph	PASS
	Ant0	6935	See test graph	See test graph	PASS
	Ant1	6935	See test graph	See test graph	PASS
	Ant0	7015	See test graph	See test graph	PASS
	Ant1	7015	See test graph	See test graph	PASS
	Ant0	7095	See test graph	See test graph	PASS
	Ant1	7095	See test graph	See test graph	PASS
11AX20MIMO	Ant0	5955	See test graph	See test graph	PASS
	Ant1	5955	See test graph	See test graph	PASS
	Ant0	6175	See test graph	See test graph	PASS
	Ant1	6175	See test graph	See test graph	PASS
	Ant0	6415	See test graph	See test graph	PASS
	Ant1	6415	See test graph	See test graph	PASS
	Ant0	6435	See test graph	See test graph	PASS
	Ant1	6435	See test graph	See test graph	PASS
	Ant0	6475	See test graph	See test graph	PASS
	Ant1	6475	See test graph	See test graph	PASS
	Ant0	6515	See test graph	See test graph	PASS
	Ant1	6515	See test graph	See test graph	PASS
	Ant0	6535	See test graph	See test graph	PASS
	Ant1	6535	See test graph	See test graph	PASS
	Ant0	6715	See test graph	See test graph	PASS
	Ant1	6715	See test graph	See test graph	PASS
	Ant0	6855	See test graph	See test graph	PASS
	Ant1	6855	See test graph	See test graph	PASS
	Ant0	6875	See test graph	See test graph	PASS
	Ant1	6875	See test graph	See test graph	PASS
11AX40MIMO	Ant0	7015	See test graph	See test graph	PASS
	Ant1	7015	See test graph	See test graph	PASS
	Ant0	7115	See test graph	See test graph	PASS
	Ant1	7115	See test graph	See test graph	PASS
	Ant0	5965	See test graph	See test graph	PASS
	Ant1	5965	See test graph	See test graph	PASS
	Ant0	6165	See test graph	See test graph	PASS
	Ant1	6165	See test graph	See test graph	PASS
	Ant0	6405	See test graph	See test graph	PASS
	Ant1	6405	See test graph	See test graph	PASS
	Ant0	6445	See test graph	See test graph	PASS
	Ant1	6445	See test graph	See test graph	PASS
	Ant0	6485	See test graph	See test graph	PASS
	Ant1	6485	See test graph	See test graph	PASS
	Ant0	6525	See test graph	See test graph	PASS

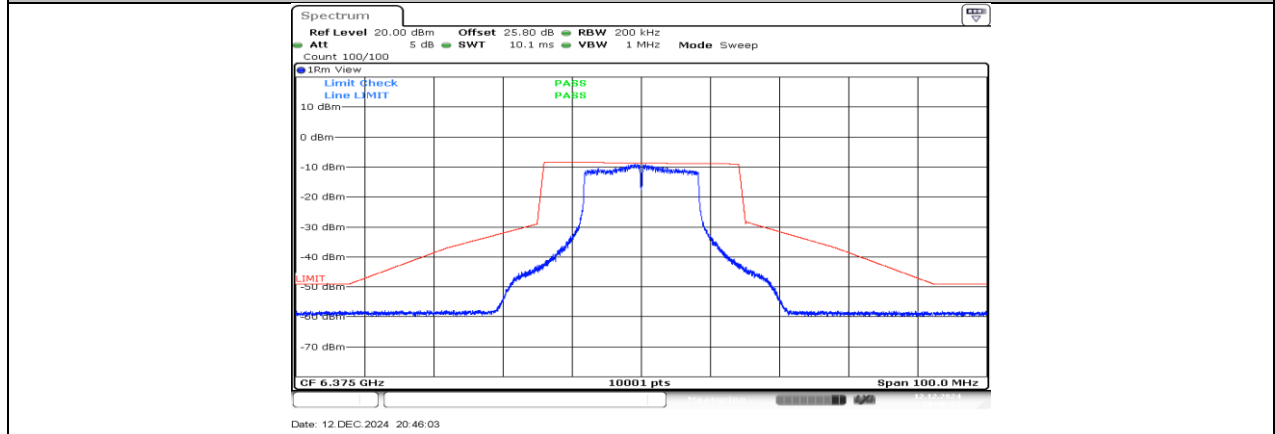
	Ant1	6525	See test graph	See test graph	PASS
	Ant0	6565	See test graph	See test graph	PASS
	Ant1	6565	See test graph	See test graph	PASS
	Ant0	6725	See test graph	See test graph	PASS
	Ant1	6725	See test graph	See test graph	PASS
	Ant0	6845	See test graph	See test graph	PASS
	Ant1	6845	See test graph	See test graph	PASS
	Ant0	6885	See test graph	See test graph	PASS
	Ant1	6885	See test graph	See test graph	PASS
	Ant0	7005	See test graph	See test graph	PASS
	Ant1	7005	See test graph	See test graph	PASS
	Ant0	7085	See test graph	See test graph	PASS
11AX80MIMO	Ant1	7085	See test graph	See test graph	PASS
	Ant0	5985	See test graph	See test graph	PASS
	Ant1	5985	See test graph	See test graph	PASS
	Ant0	6145	See test graph	See test graph	PASS
	Ant1	6145	See test graph	See test graph	PASS
	Ant0	6385	See test graph	See test graph	PASS
	Ant1	6385	See test graph	See test graph	PASS
	Ant0	6465	See test graph	See test graph	PASS
	Ant1	6465	See test graph	See test graph	PASS
	Ant0	6545	See test graph	See test graph	PASS
	Ant1	6545	See test graph	See test graph	PASS
	Ant0	6705	See test graph	See test graph	PASS
	Ant1	6705	See test graph	See test graph	PASS
	Ant0	6785	See test graph	See test graph	PASS
	Ant1	6785	See test graph	See test graph	PASS
	Ant0	6865	See test graph	See test graph	PASS
	Ant1	6865	See test graph	See test graph	PASS
	Ant0	6945	See test graph	See test graph	PASS
	Ant1	6945	See test graph	See test graph	PASS
	Ant0	7025	See test graph	See test graph	PASS
	Ant1	7025	See test graph	See test graph	PASS

11.6.2. Test Graphs-SU Mode Worst Case

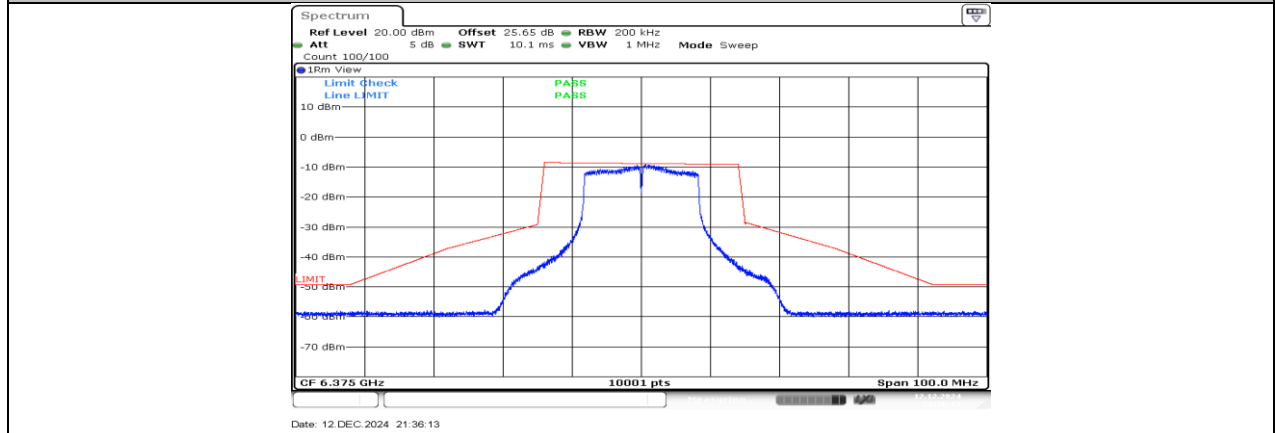




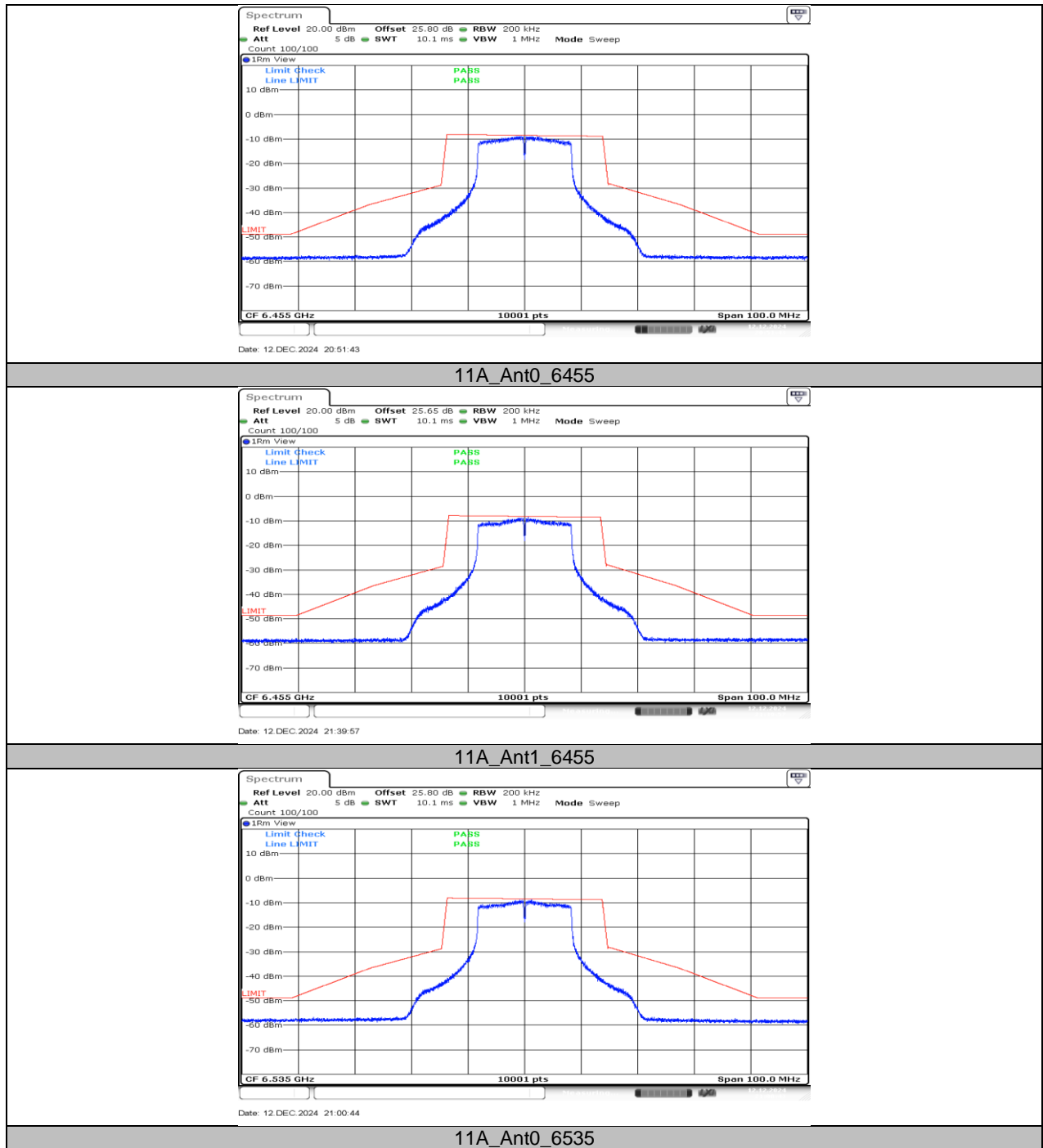
11A_Ant1_6135

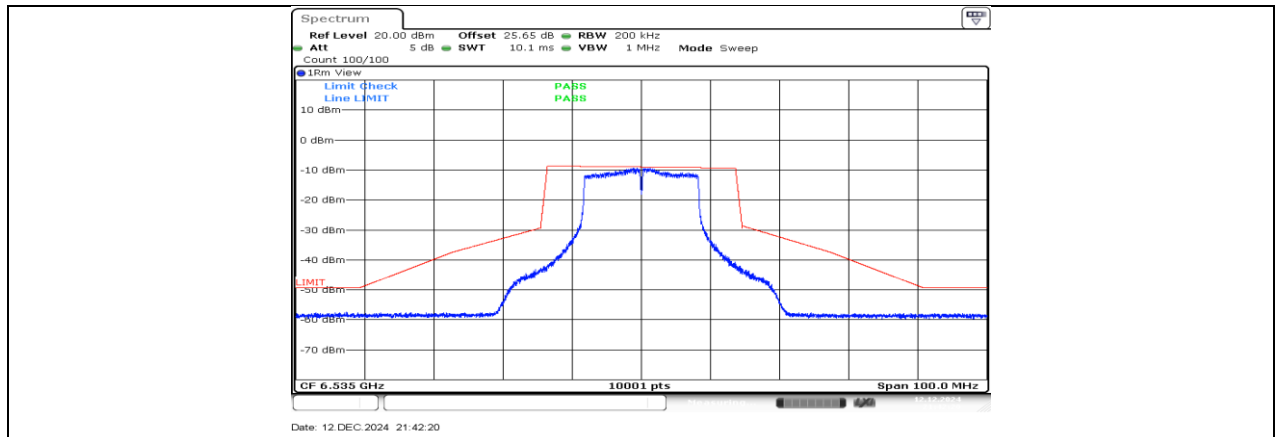


11A_Ant0_6375

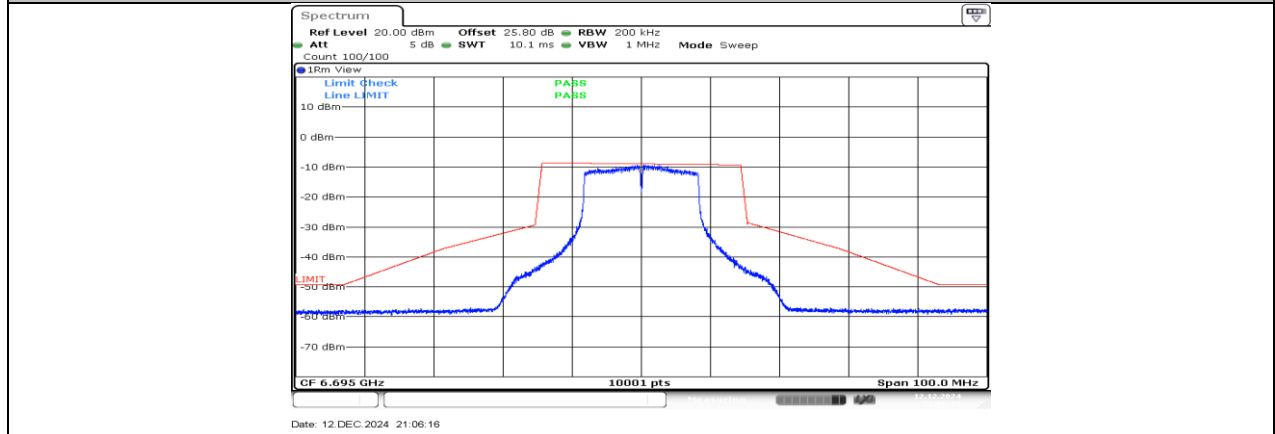


11A_Ant1_6375

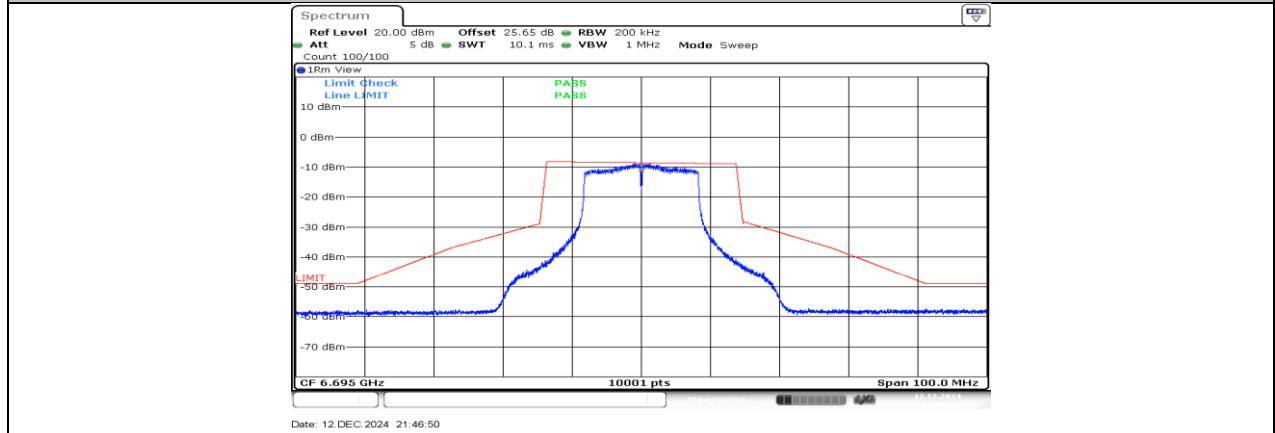




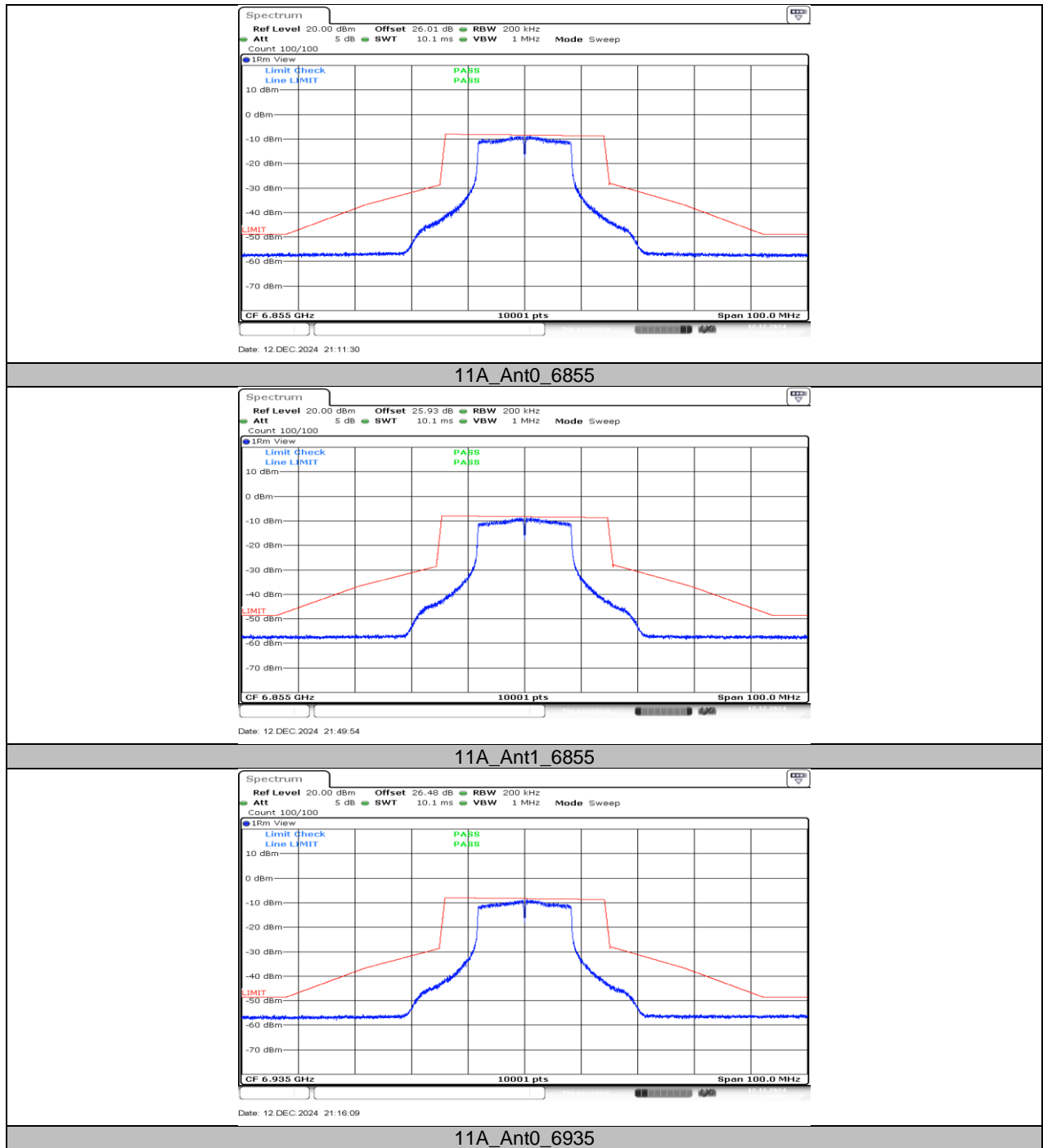
11A_Ant1_6535

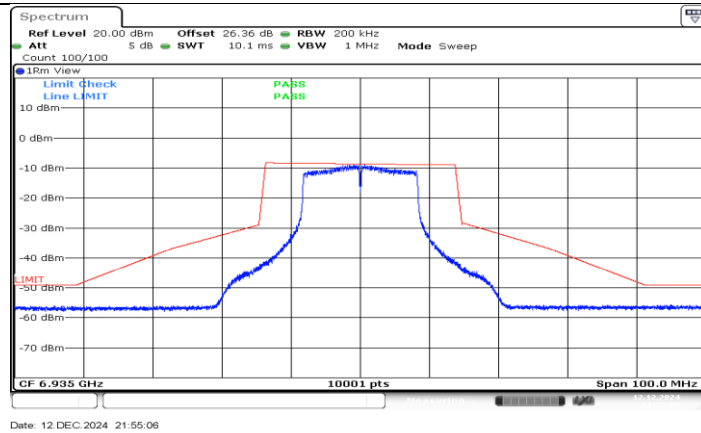


11A_Ant0_6695

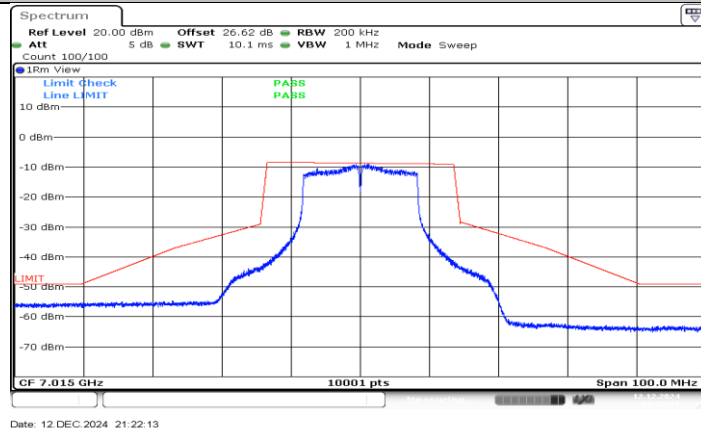


11A_Ant1_6695

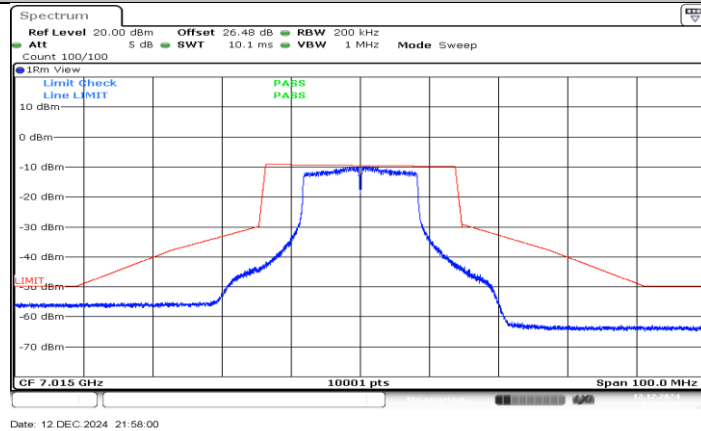




11A_Ant1_6935



11A_Ant0_7015



11A_Ant1_7015

