

## Antenna Information

The following figures depict the RFID antenna's physical layout (see Fig. 1) and the additional RFID circuitry it is connected to (see Fig. 2). The yellow artwork depicted in Fig. 1 indicates the RFID antenna layout printed on the PCB's bottom layer. While, the green artwork depicts a separate capacitive wakeup circuit printed onto the PCB's top layer.

This antenna contains two equal interwoven 0.508mm wide copper trace antenna loops. Together these loops sum to a total of length of 0.62m. The antenna's inductance measures an average of  $1.21\mu\text{H}$  via spectrum analyzer. Its etched outer dimensions, as measured from the copper traces found near the edges of its PCB, span 35.2mm high by 51.5mm wide.

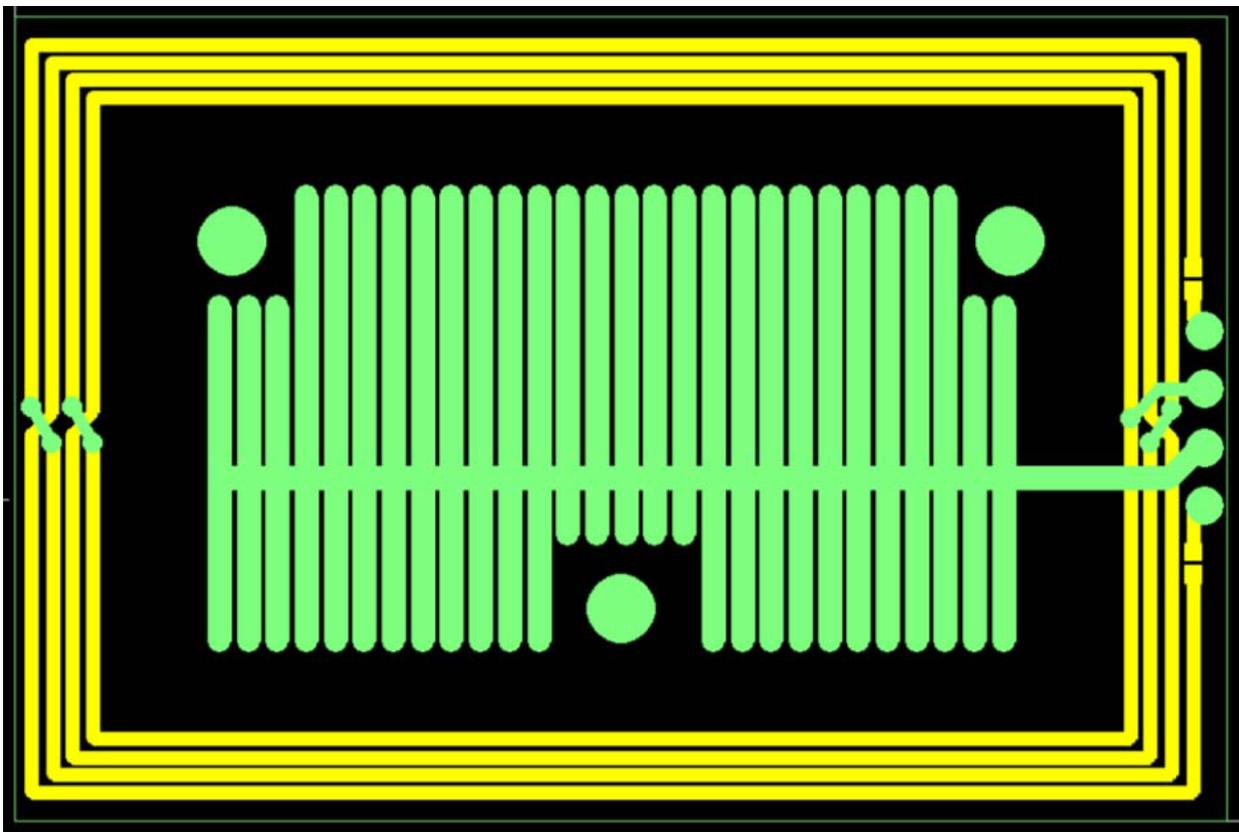


Figure 1. The PCB layout of the RFID antenna.

In Fig. 2, the antenna is soldered to its two other RFID PCB layers.



Figure 2. The RFID antenna mounted to the top of its other RFID PCBs.