

Appendix E.4: Maximum conducted output power

Test Result Channel Power

Test Mode	Antenna	Freq(MHz)	Channel Power [dBm]	Duty Cycle [%]	DC Factor [dBm]	Result [dBm]	Limit [dBm]	Verdict
11A	Ant0	5260	9.92	43.75	3.59	13.51	≤23.76	PASS
		5280	9.71	43.75	3.59	13.30	≤23.78	PASS
		5320	9.10	40.00	3.98	13.08	≤23.78	PASS
11N20SISO	Ant0	5260	10.33	47.06	3.27	13.60	≤23.80	PASS
		5280	10.25	47.06	3.27	13.52	≤23.86	PASS
		5320	9.61	44.44	3.52	13.13	≤23.83	PASS
11N40SISO	Ant0	5270	9.34	40.00	3.98	13.32	≤23.98	PASS
		5310	8.25	40.00	3.98	12.23	≤23.98	PASS
11AC20SISO	Ant0	5260	9.48	50.00	3.01	12.49	≤23.91	PASS
		5280	9.22	44.44	3.52	12.74	≤23.86	PASS
		5320	8.50	47.37	3.24	11.74	≤23.88	PASS
11AC40SISO	Ant0	5270	8.61	43.75	3.59	12.20	≤23.98	PASS
		5310	7.49	40.00	3.98	11.47	≤23.98	PASS
11AC80SISO	Ant0	5290	7.73	35.71	4.47	12.20	≤23.98	PASS
11AX20SISO	Ant0	5260	10.15	50.00	3.01	13.16	≤23.98	PASS
		5280	9.36	50.00	3.01	12.37	≤23.98	PASS
		5320	8.63	50.00	3.01	11.64	≤23.98	PASS
11AX40SISO	Ant0	5270	8.94	50.00	3.01	11.95	≤23.98	PASS
		5310	7.75	50.00	3.01	10.76	≤23.98	PASS
11AX80SISO	Ant0	5290	10.24	47.06	3.27	13.51	≤23.98	PASS

Appendix E.5: Maximum power spectral density

Test Result

TestMode	Antenna	Freq(MHz)	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant0	5260	2.85	≤11.00	PASS
		5280	2.68	≤11.00	PASS
		5320	2.38	≤11.00	PASS
11N20SISO	Ant0	5260	2.65	≤11.00	PASS
		5280	2.44	≤11.00	PASS
		5320	-1.54	≤11.00	PASS
11N40SISO	Ant0	5270	-4.08	≤11.00	PASS
		5310	-1.30	≤11.00	PASS
11AC20SISO	Ant0	5260	1.74	≤11.00	PASS
		5280	-1.43	≤11.00	PASS
		5320	-2.24	≤11.00	PASS
11AC40SISO	Ant0	5270	-0.87	≤11.00	PASS
		5310	-1.75	≤11.00	PASS
11AC80SISO	Ant0	5290	-4.85	≤11.00	PASS
11AX20SISO	Ant0	5260	2.41	≤11.00	PASS
		5280	1.60	≤11.00	PASS
		5320	0.79	≤11.00	PASS
11AX40SISO	Ant0	5270	-1.67	≤11.00	PASS
		5310	-2.69	≤11.00	PASS
11AX80SISO	Ant0	5290	-3.92	≤11.00	PASS

Note: 1.The Result and Limit Unit is dBm/500 kHz in the band 5.725–5.85 GHz.

2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs

11A_Ant0_5260



11A_Ant0_5280



11A_Ant0_5320



11N20SISO_Ant0_5260



11N20SISO_Ant0_5280



11N20SISO_Ant0_5320



11N40SISO_Ant0_5270



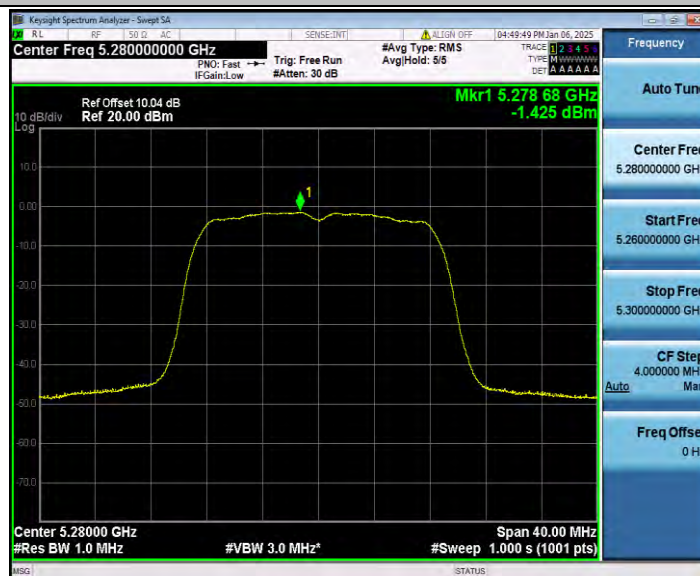
11N40SISO_Ant0_5310



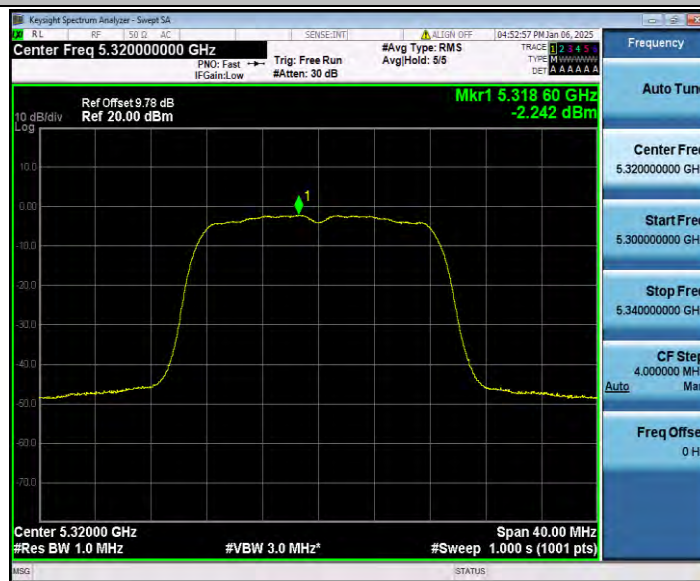
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11AC20SISO_Ant0_5280



11AC20SISO_Ant0_5320



11AC40SISO_Ant0_5270



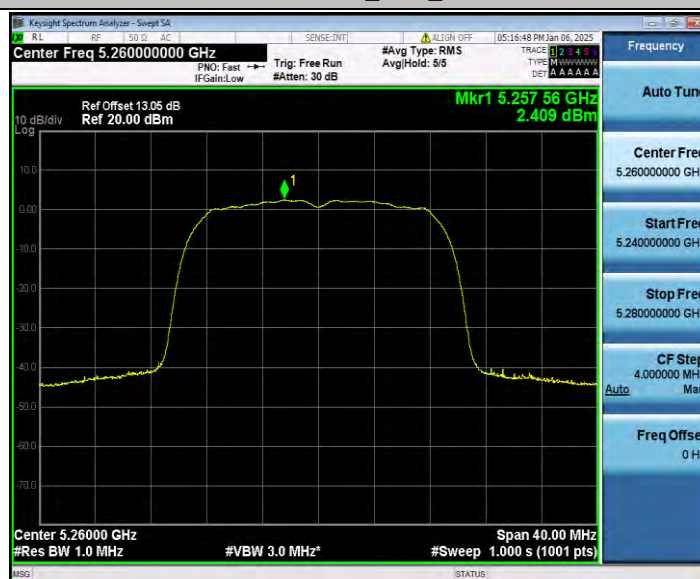
11AC40SISO_Ant0_5310



11AC80SISO_Ant0_5290



11AX20SISO_Ant0_5260



11AX20SISO_Ant0_5280



11AX20SISO_Ant0_5320



11AX40SISO_Ant0_5270



11AX40SISO_Ant0_5310



11AX80SISO_Ant0_5290

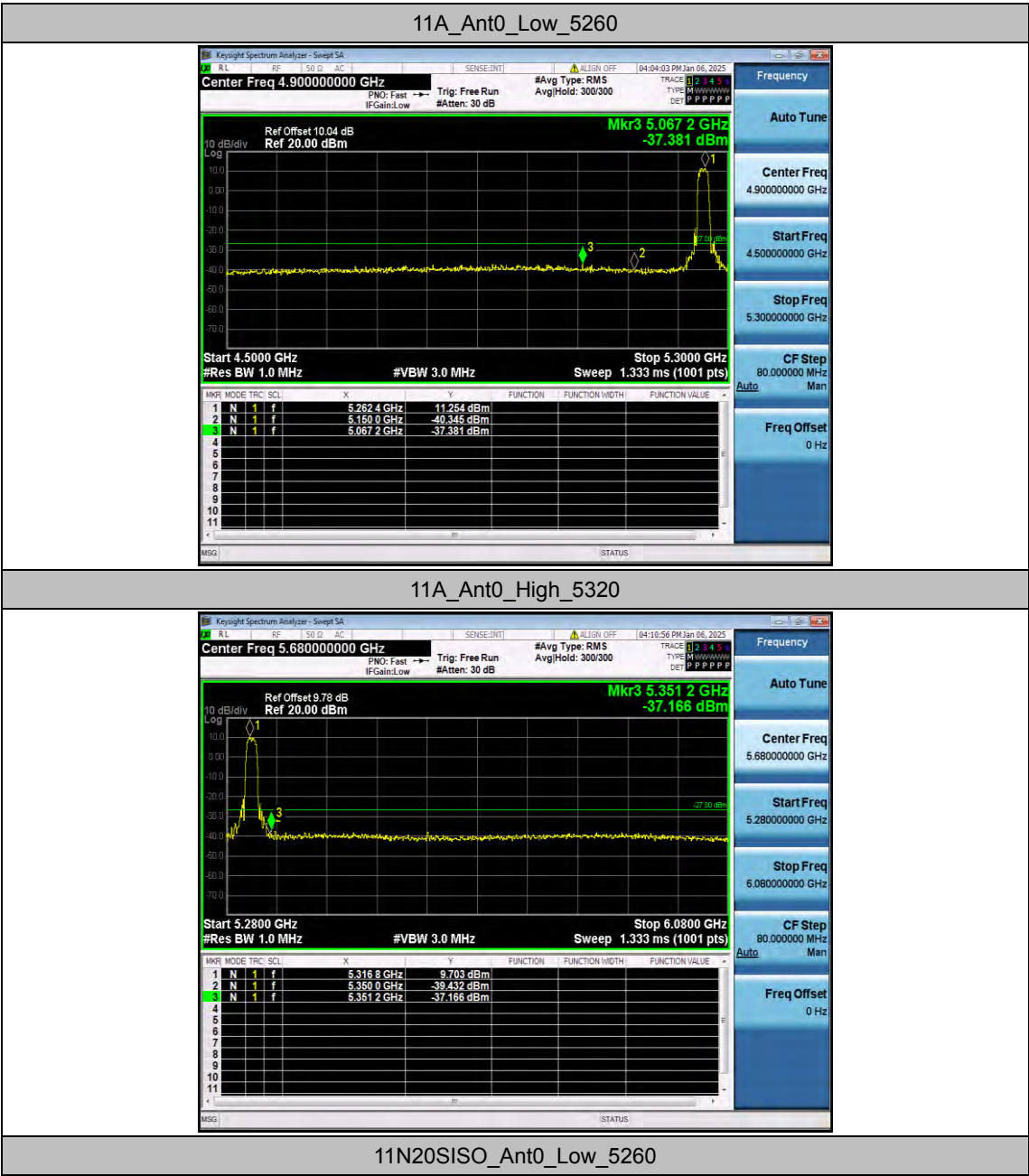


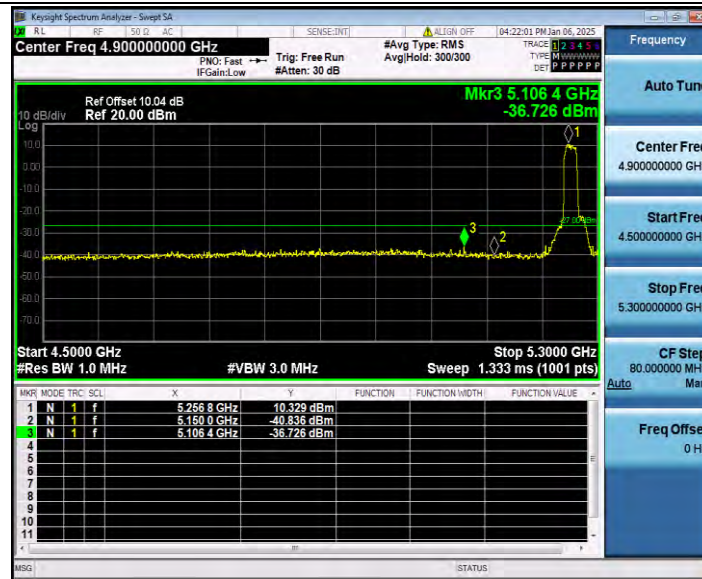
Appendix E.6: Band edge measurements

Test Result B2

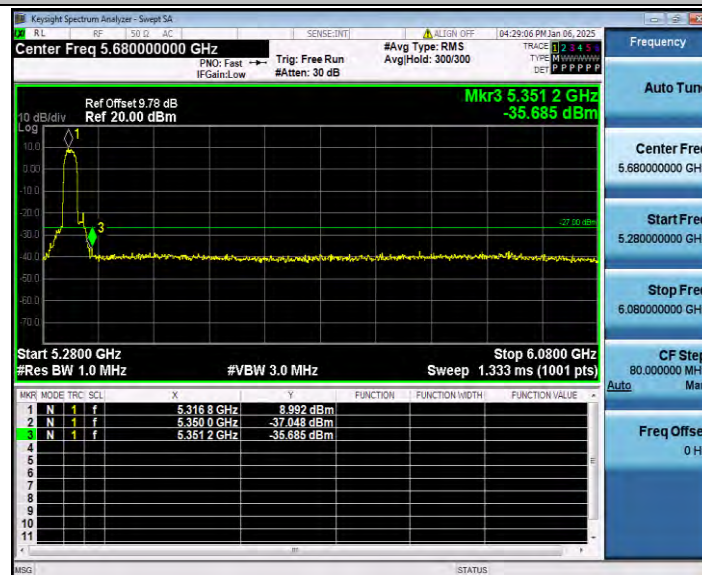
TestMode	Antenna	ChName	Freq(MHz)	Result[dBm]	Limit[dBm]	Verdict
11A	Ant0	Low	5260	-37.38	≤ -27	PASS
		High	5320	-37.17	≤ -27	PASS
11N20SISO	Ant0	Low	5260	-36.73	≤ -27	PASS
		High	5320	-35.69	≤ -27	PASS
11N40SISO	Ant0	Low	5270	-37.18	≤ -27	PASS
		High	5310	-32.62	≤ -27	PASS
11AC20SISO	Ant0	Low	5260	-36.25	≤ -27	PASS
		High	5320	-37.57	≤ -27	PASS
11AC40SISO	Ant0	Low	5270	-37.34	≤ -27	PASS
		High	5310	-37.3	≤ -27	PASS
11AC80SISO	Ant0	Low	5290	-36.98	≤ -27	PASS
		High	5290	-32.53	≤ -27	PASS
11AX20SISO	Ant0	Low	5260	-37.64	≤ -27	PASS
		High	5320	-34.61	≤ -27	PASS
11AX40SISO	Ant0	Low	5270	-37.46	≤ -27	PASS
		High	5310	-35.35	≤ -27	PASS
11AX80SISO	Ant0	Low	5290	-37.08	≤ -27	PASS
		High	5290	-36.24	≤ -27	PASS

Test Graphs B2





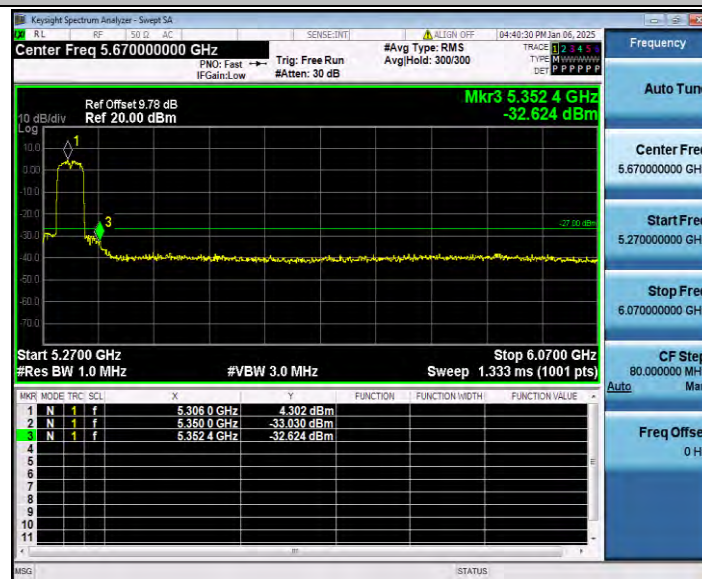
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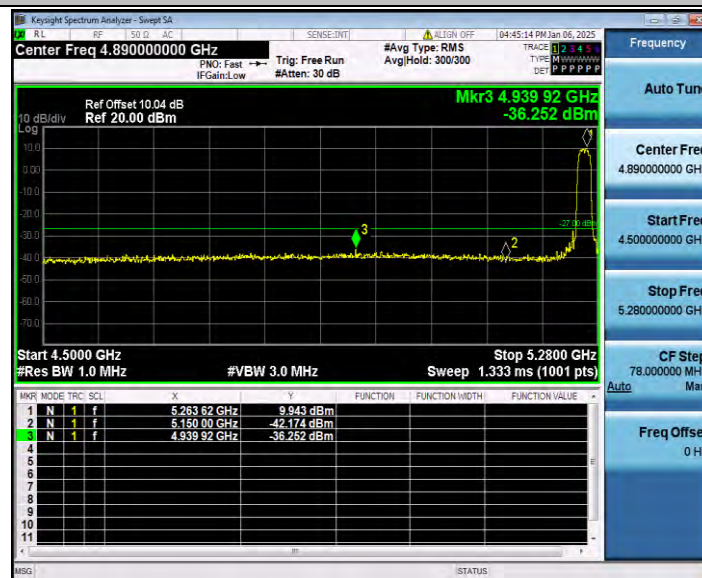
11N40SISO_Ant0_Low_5270



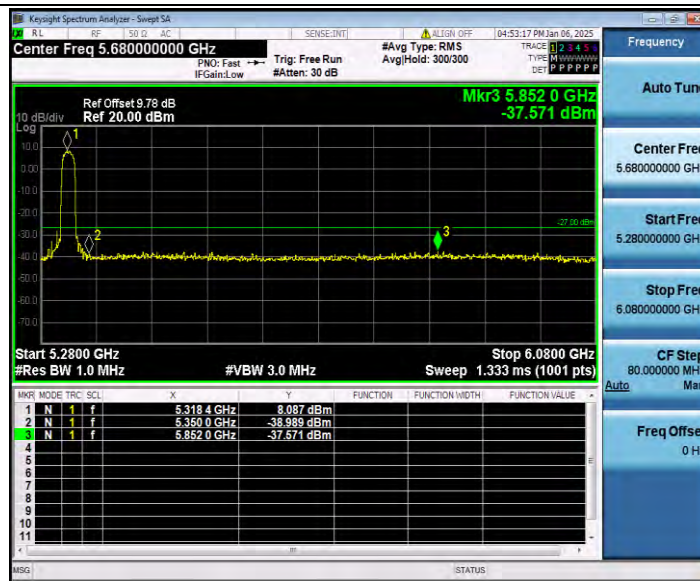
11N40SISO_Ant0_High_5310



11AC20SISO_Ant0_Low_5260



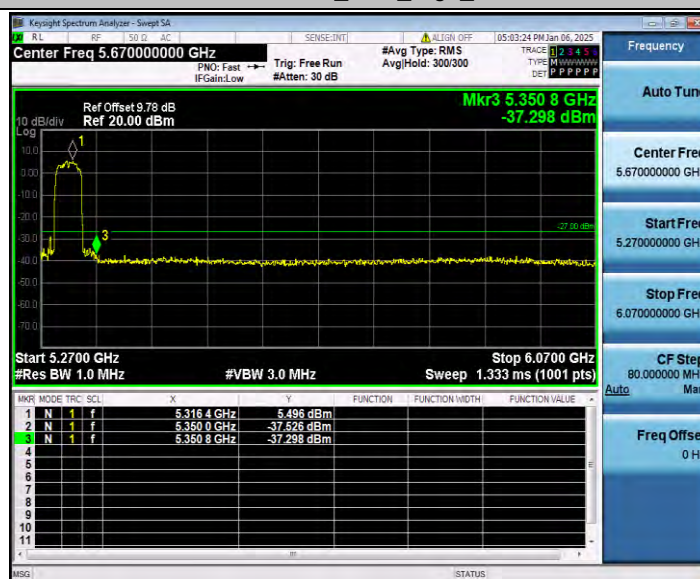
11AC20SISO_Ant0_High_5320



11AC40SISO_Ant0_Low_5270



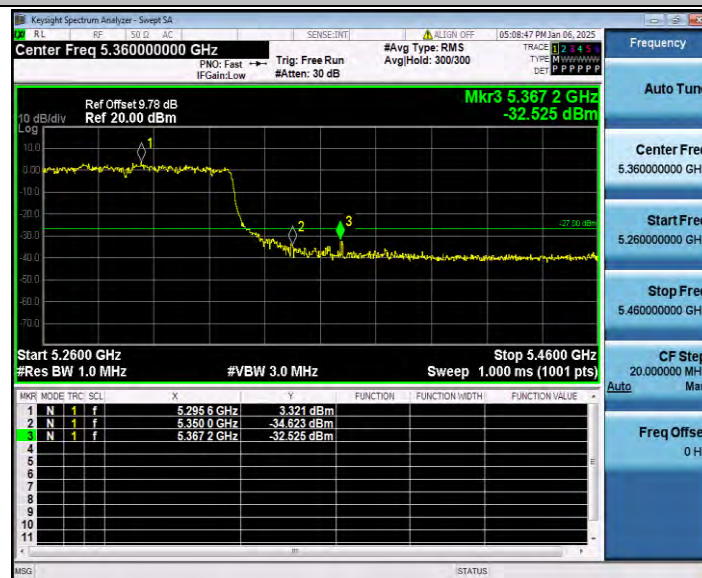
11AC40SISO_Ant0_High_5310



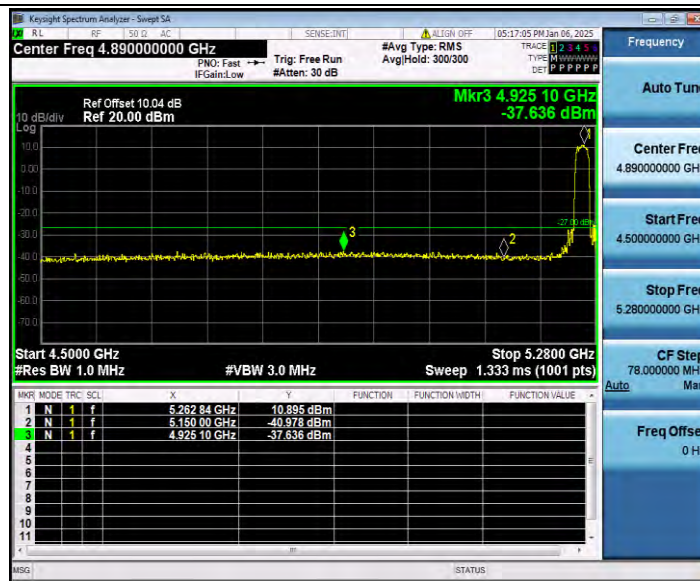
11AC80SISO_Ant0_Low_5290



11AC80SISO_Ant0_High_5290



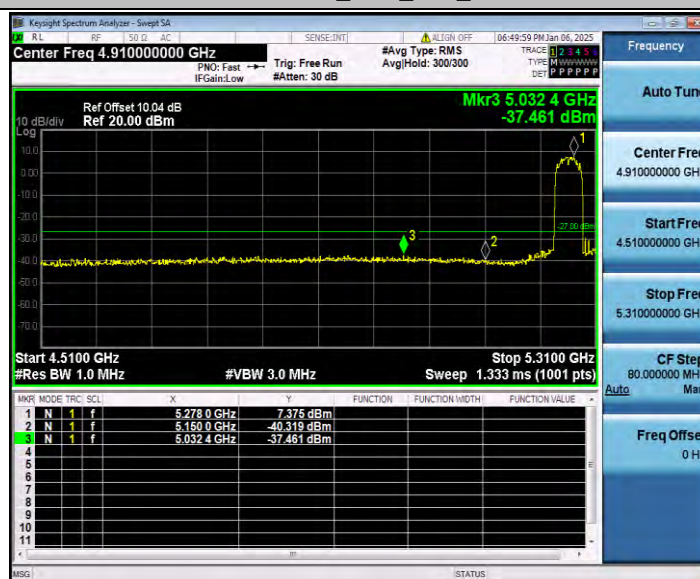
11AX20SISO_Ant0_Low_5260



11AX20SISO_Ant0_High_5320



11AX40SISO_Ant0_Low_5270



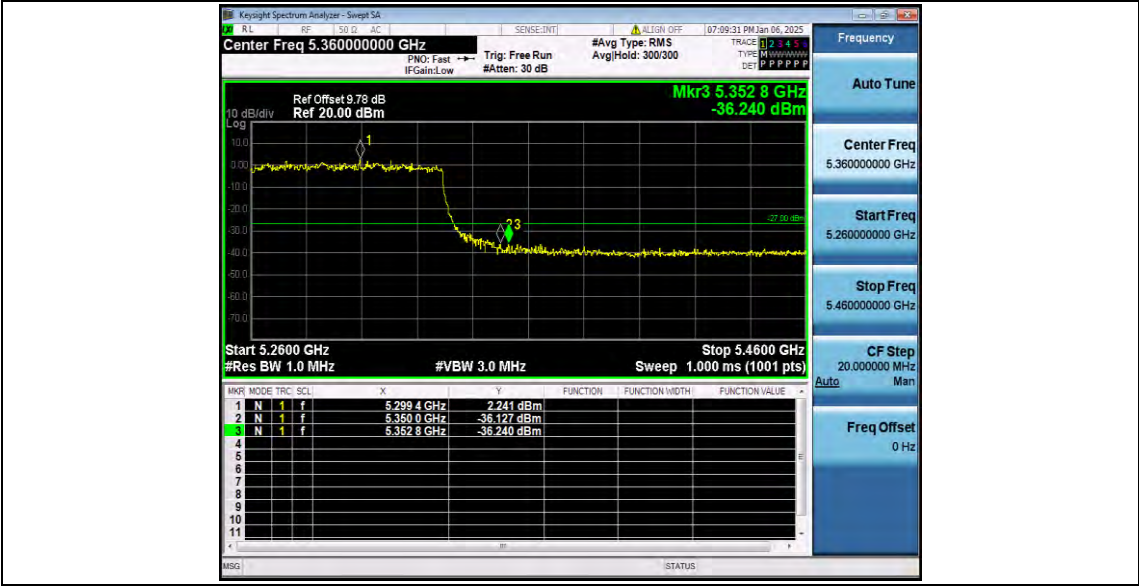
11AX40SISO_Ant0_High_5310



11AX80SISO_Ant0_Low_5290



11AX80SISO_Ant0_High_5290



Appendix E.7: Conducted Spurious Emission

Test Result

TestMode	Antenna	Freq(MHz)	FreqRange [MHz]	Max. Fre [MHz]	Max. Level [dBm]	Limit [dBm]	Verdict
11A	Ant0	5260	30~5140	5096.39	-45.38	≤-27	PASS
			5360~27000	26035.58	-36.99	≤-27	PASS
		5280	30~5140	5121.77	-45.35	≤-27	PASS
			5360~27000	24154.34	-36.91	≤-27	PASS
		5320	30~5140	5118.88	-46.12	≤-27	PASS
			5360~27000	26818.22	-37.18	≤-27	PASS
11N20SISO	Ant0	5260	30~5140	5097.25	-44.58	≤-27	PASS
			5360~27000	24275.52	-36.97	≤-27	PASS
		5280	30~5140	5118.54	-45.2	≤-27	PASS
			5360~27000	25650.39	-37.44	≤-27	PASS
		5320	30~5140	2698.78	-45.61	≤-27	PASS
			5360~27000	24797.77	-37.71	≤-27	PASS
11N40SISO	Ant0	5270	30~5140	5114.11	-44.22	≤-27	PASS
			5360~27000	26730.94	-37.14	≤-27	PASS
		5310	30~5140	4977.16	-46.12	≤-27	PASS
			5360~27000	26617.69	-37.1	≤-27	PASS
11AC20SISO	Ant0	5260	30~5140	5103.04	-45.03	≤-27	PASS
			5360~27000	24261.1	-36.76	≤-27	PASS
		5280	30~5140	5122.29	-46.22	≤-27	PASS
			5360~27000	24834.56	-37.62	≤-27	PASS
		5320	30~5140	2633.03	-46.36	≤-27	PASS
			5360~27000	23464.75	-37.41	≤-27	PASS
11AC40SISO	Ant0	5270	30~5140	5115.81	-44.84	≤-27	PASS
			5360~27000	24257.49	-37.29	≤-27	PASS
		5310	30~5140	2609.36	-46.96	≤-27	PASS
			5360~27000	23601.08	-37.29	≤-27	PASS
11AC80SISO	Ant0	5290	30~5140	5132.34	-44.75	≤-27	PASS
			5360~27000	24296.44	-37.05	≤-27	PASS
11AX20SISO	Ant0	5260	30~5140	5097.08	-45.33	≤-27	PASS
			5360~27000	24256.05	-36.7	≤-27	PASS
		5280	30~5140	5113.94	-46.06	≤-27	PASS
			5360~27000	24254.61	-37.4	≤-27	PASS
		5320	30~5140	3306.36	-45.84	≤-27	PASS
			5360~27000	25165.65	-37.15	≤-27	PASS
11AX40SISO	Ant0	5270	30~5140	4987.21	-45.46	≤-27	PASS
			5360~27000	24788.39	-36.99	≤-27	PASS

		5310	30~5140	4987.04	-46.32	≤ -27	PASS
			5360~27000	24256.77	-37.04	≤ -27	PASS
11AX80SISO	Ant0	5290	30~5140	5124.67	-41.62	≤ -27	PASS
			5360~27000	5367.93	-27.45	≤ -27	PASS

Test Graphs

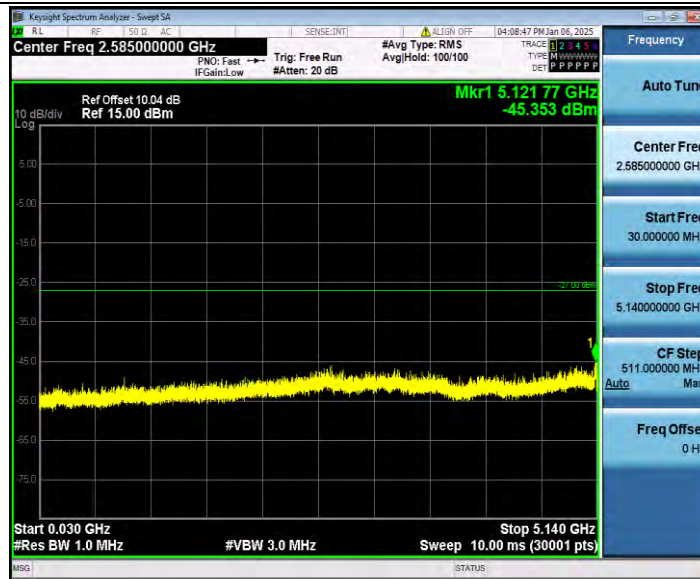
11A_Ant0_5260_30~5140



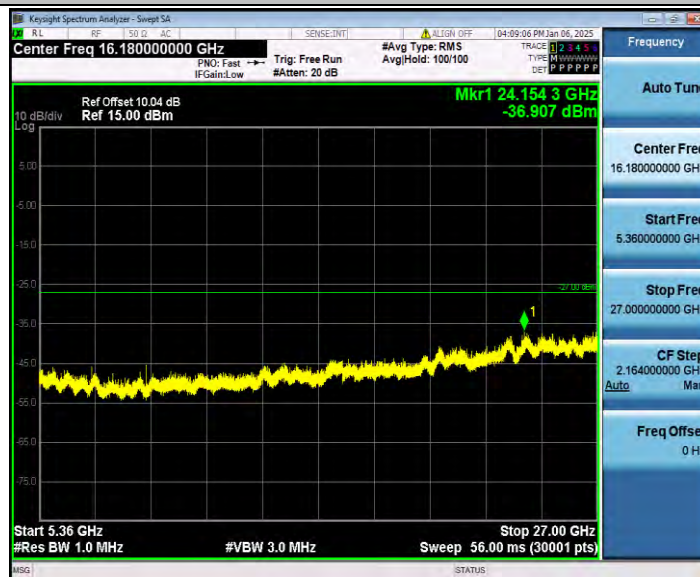
11A_Ant0_5260_5360~27000



11A_Ant0_5280_30~5140



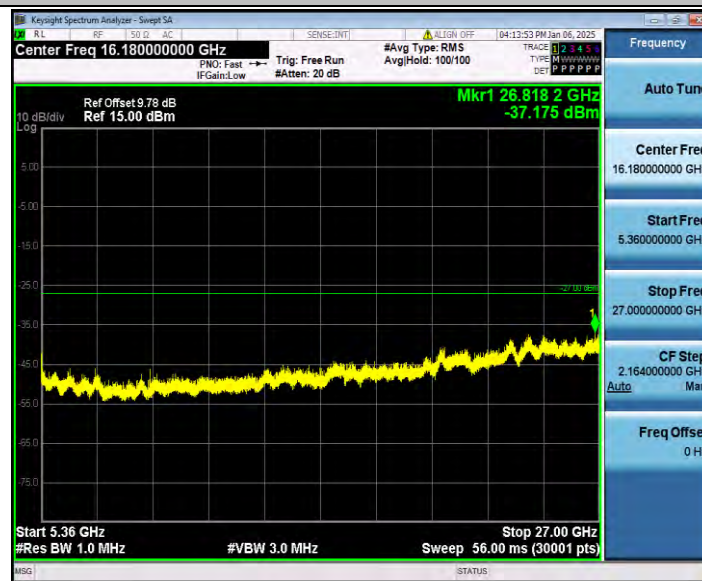
11A_Ant0_5280_5360~27000



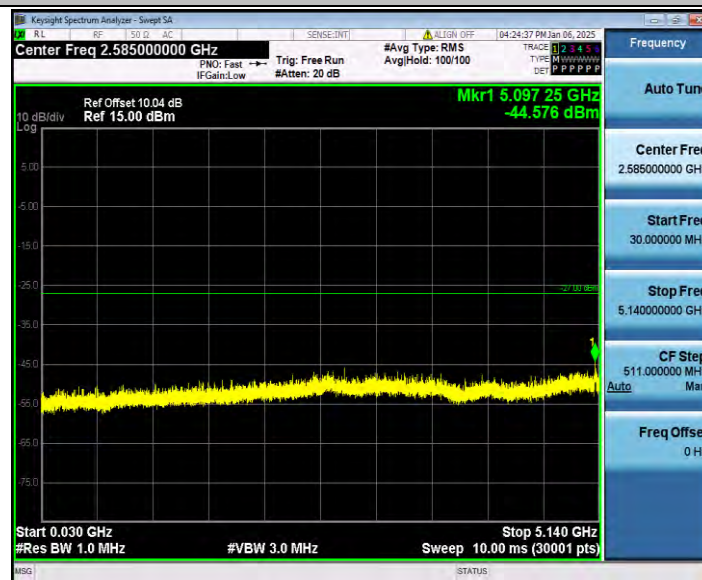
11A_Ant0_5320_30~5140



11A_Ant0_5320_5360~27000



11N20SISO_Ant0_5260_30~5140



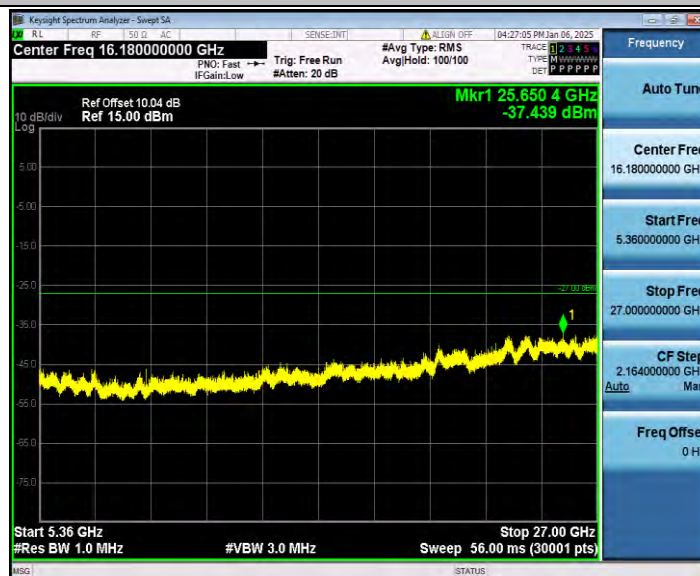
11N20SISO_Ant0_5260_5360~27000



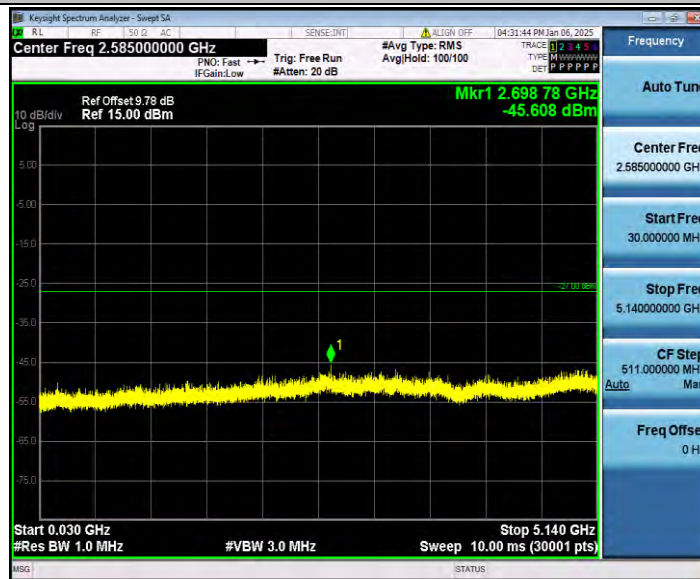
11N20SISO_Ant0_5280_30~5140



11N20SISO_Ant0_5280_5360~27000



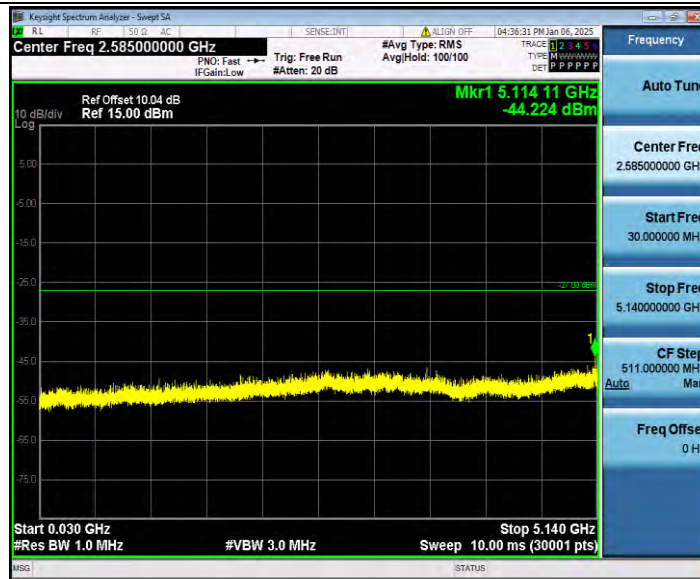
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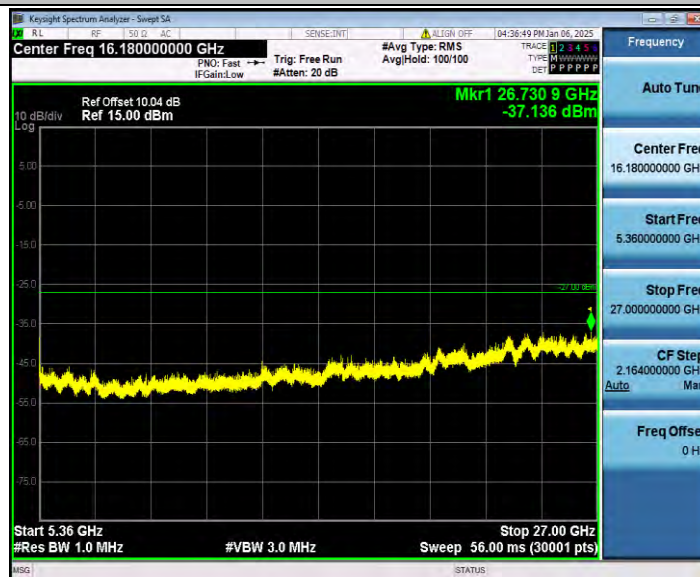
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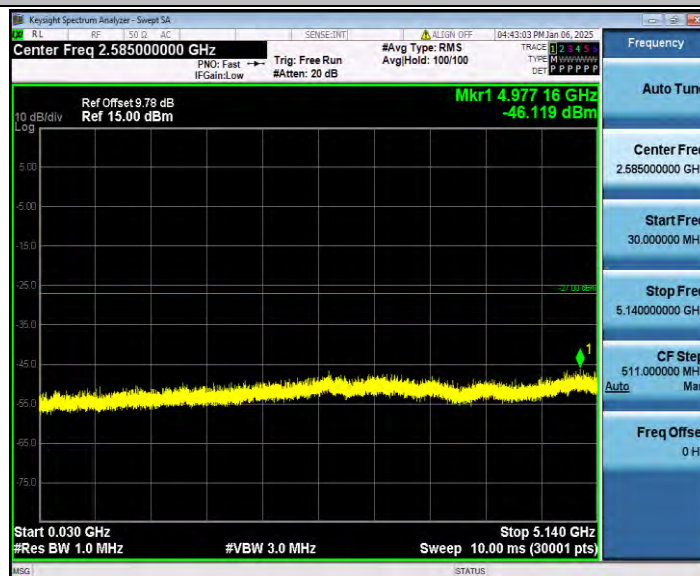
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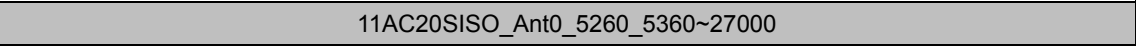
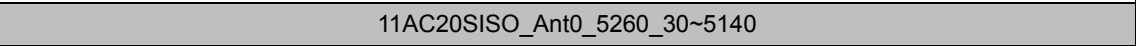


11N40SISO_Ant0_5270_5360~27000



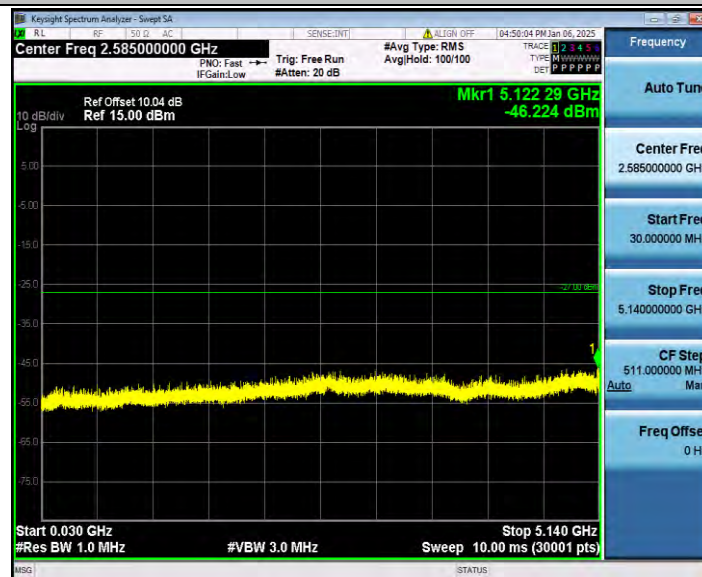
11N40SISO_Ant0_5310_30~5140



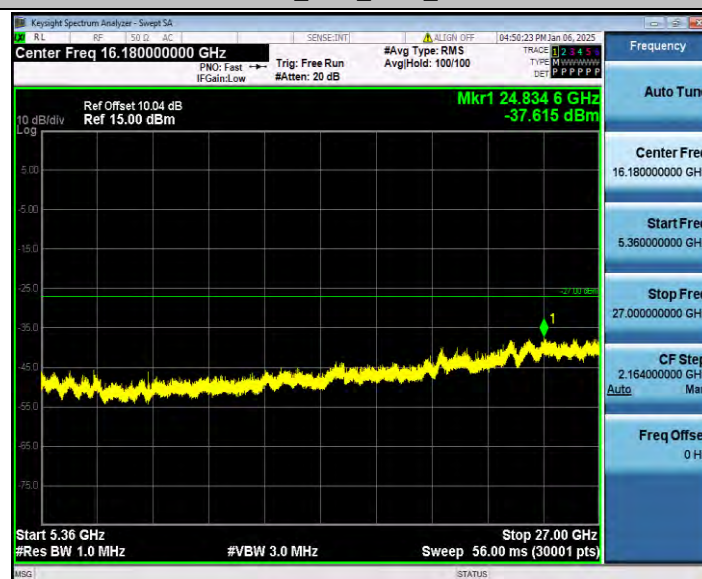




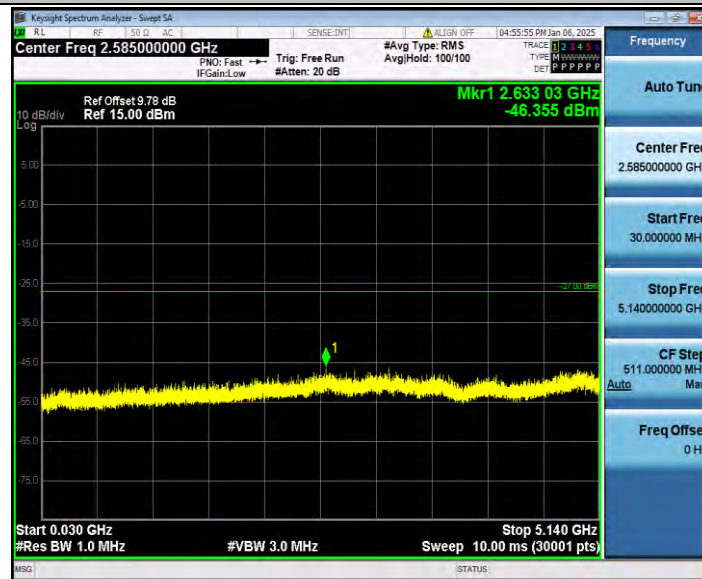
11AC20SISO_Ant0_5280_30~5140



11AC20SISO_Ant0_5280_5360~27000



11AC20SISO_Ant0_5320_30~5140



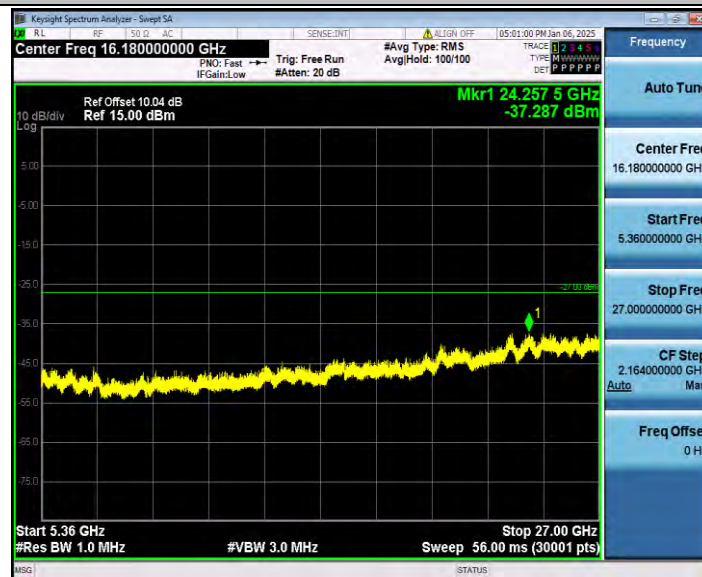
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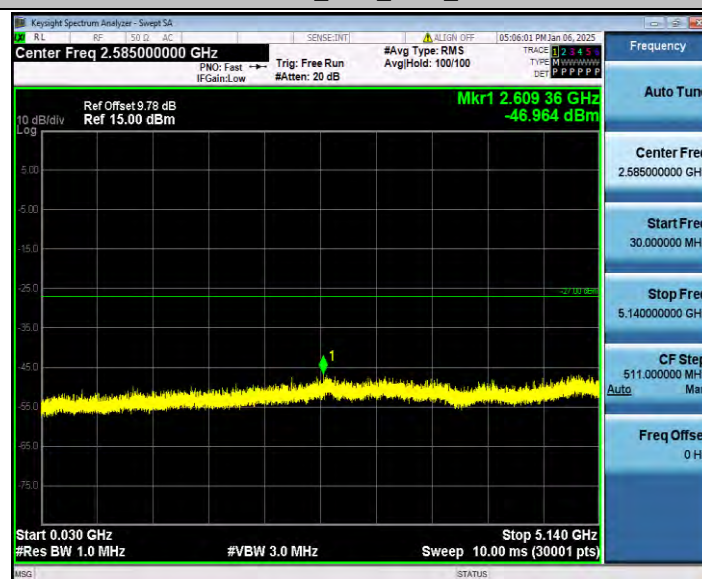
11AC40SISO_Ant0_5270_30~5140



11AC40SISO_Ant0_5270_5360~27000



11AC40SISO_Ant0_5310_30~5140



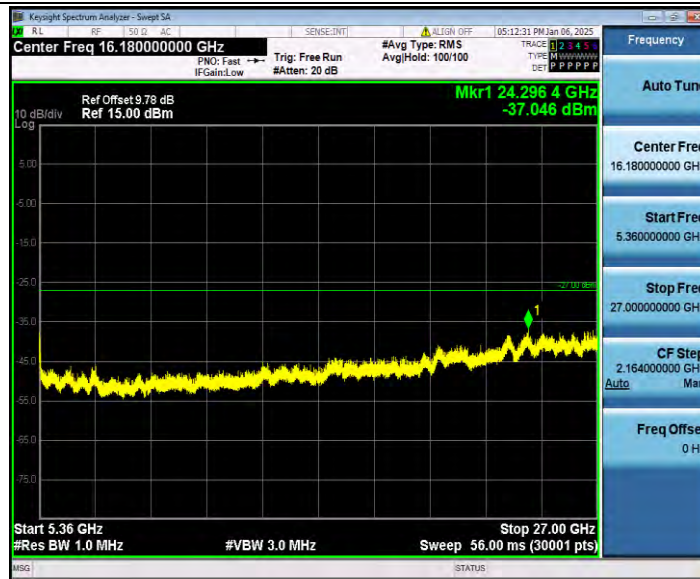
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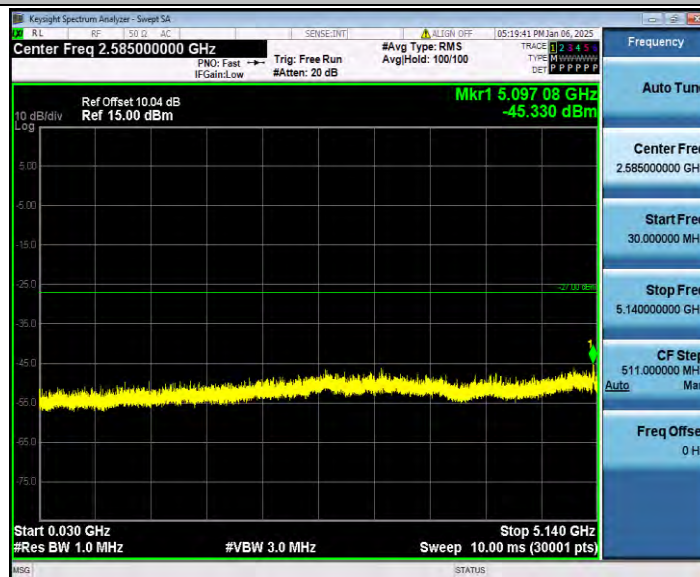
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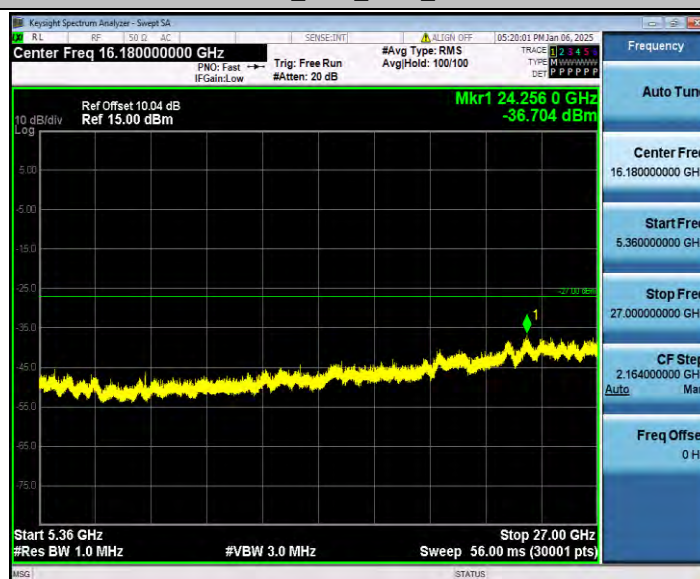
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11AX20SISO_Ant0_5260_30~5140



11AX20SISO_Ant0_5260_5360~27000



11AX20SISO_Ant0_5280_30~5140



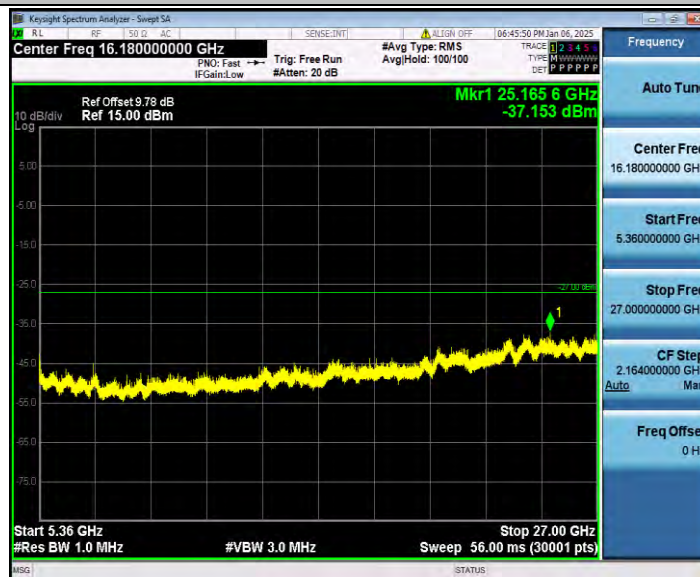
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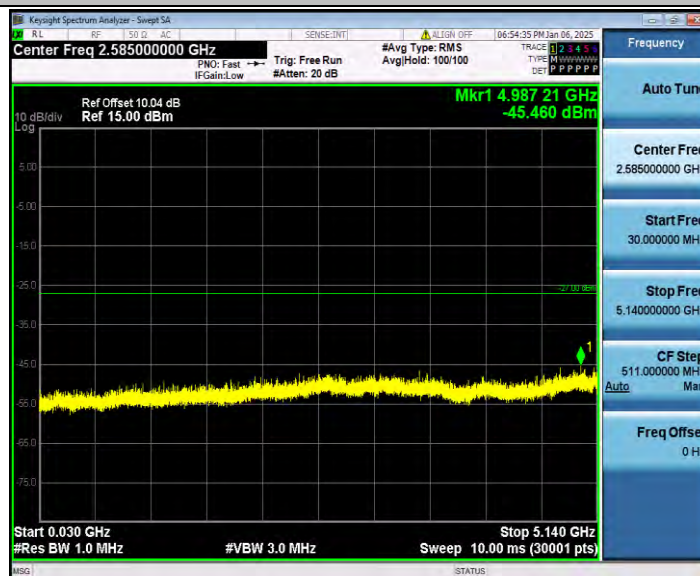
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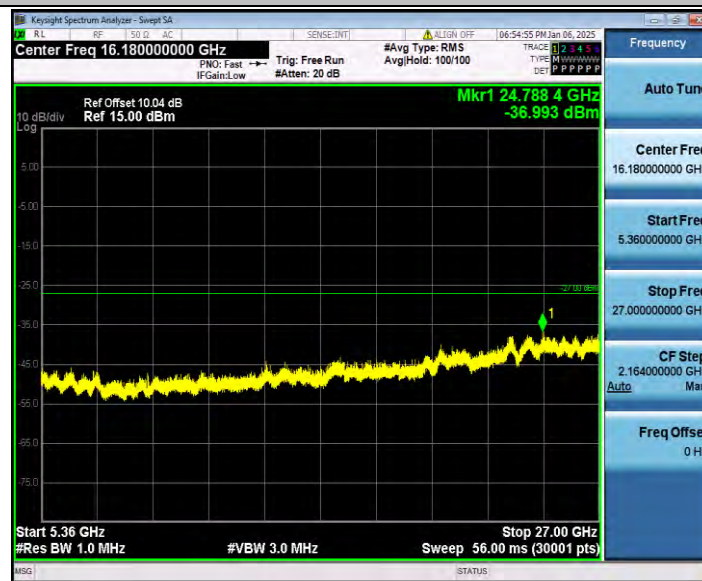
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11AX40SISO_Ant0_5270_30~5140



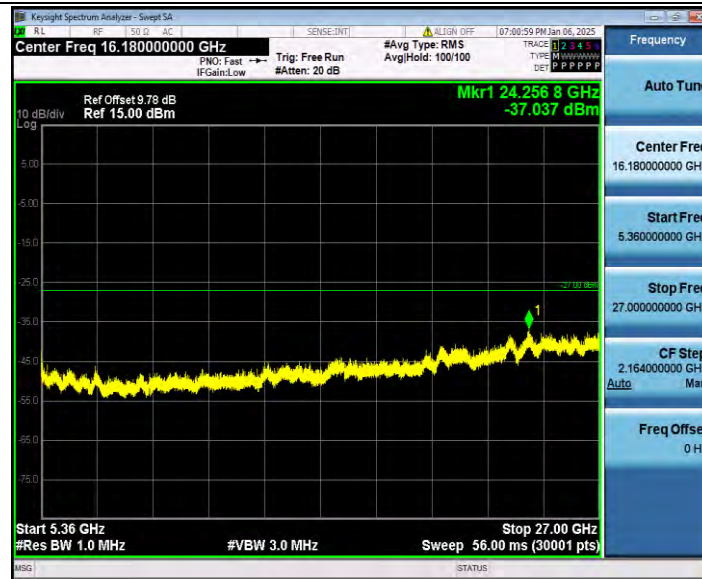
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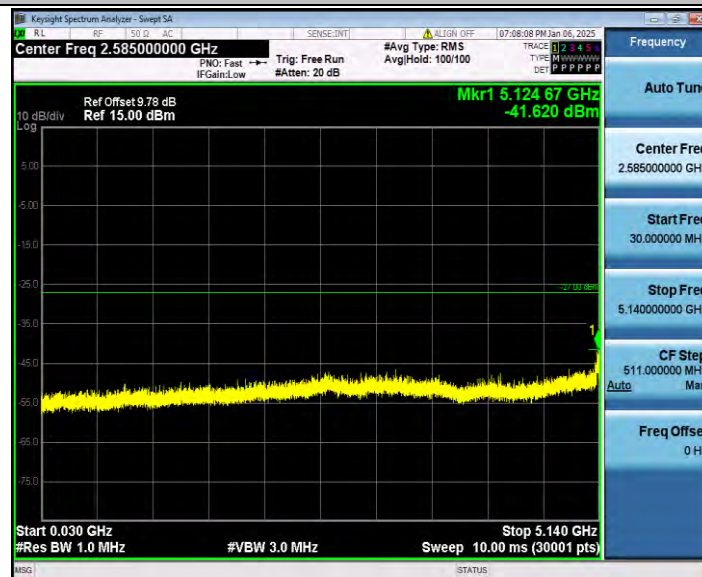
11AX40SISO_Ant0_5310_30~5140



11AX40SISO_Ant0_5310_5360~27000



11AX80SISO_Ant0_5290_30~5140



11AX80SISO_Ant0_5290_5360~27000

