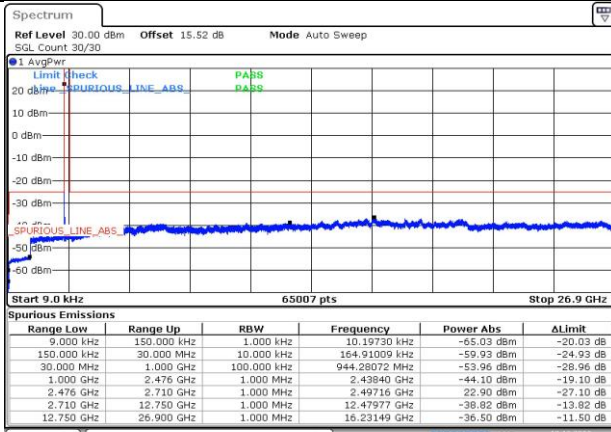


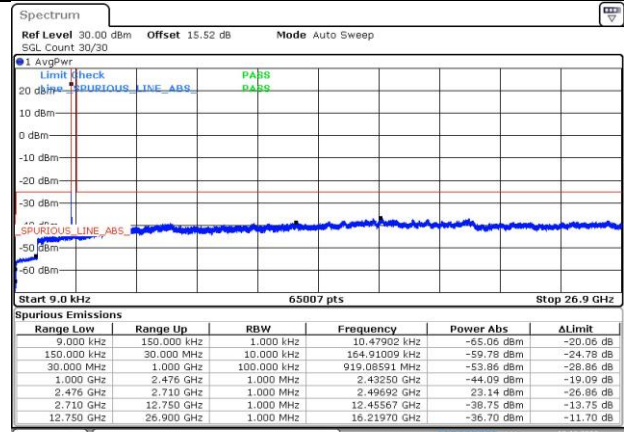
n41A / 30KHz / 15MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



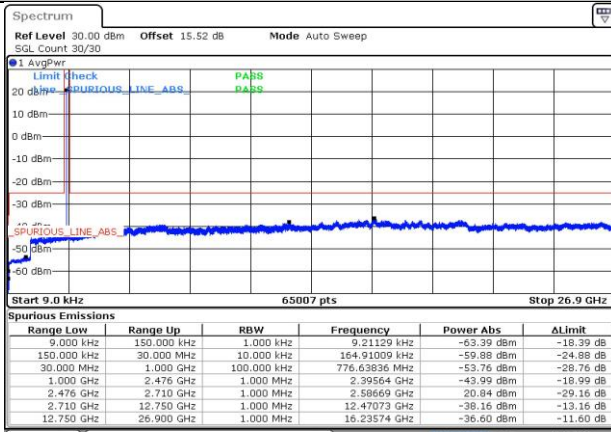
Date: 19 DEC 2023 03:14:10

LCH / DFT-QPSK_Edge_1RB_Left



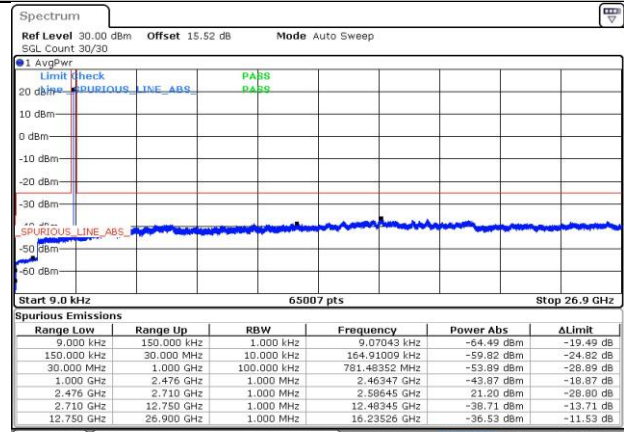
Date: 19 DEC 2023 03:14:30

MCH / DFT-Pi2BPSK_Edge_1RB_Left



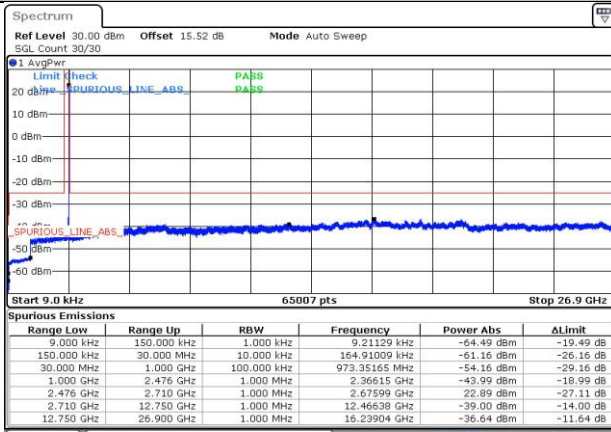
Date: 19 DEC 2023 03:14:58

MCH / DFT-QPSK_Edge_1RB_Left



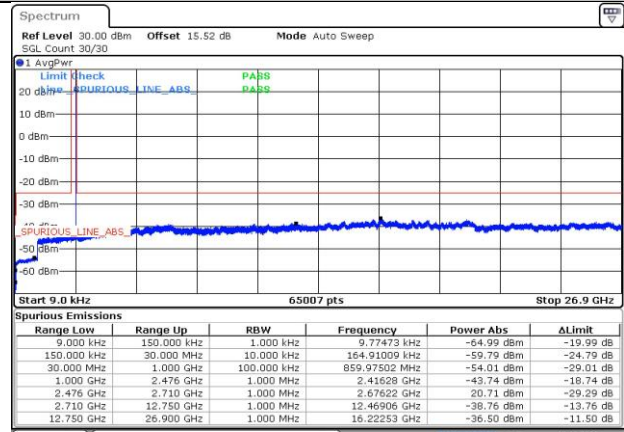
Date: 19 DEC 2023 03:15:24

HCH / DFT-Pi2BPSK_Edge_1RB_Left



Date: 19 DEC 2023 03:15:50

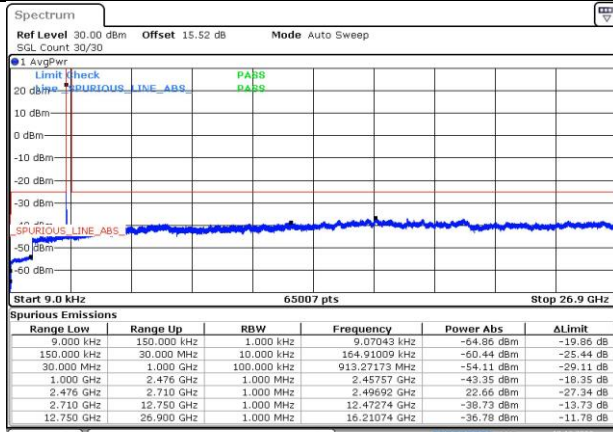
HCH / DFT-QPSK_Edge_1RB_Left



Date: 19 DEC 2023 03:16:19

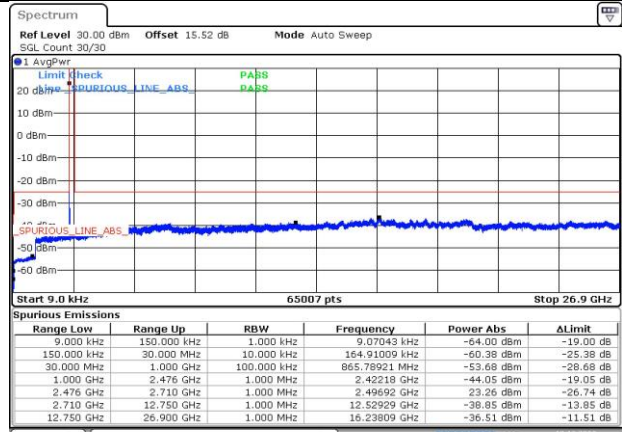
n41A / 30KHz / 20MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



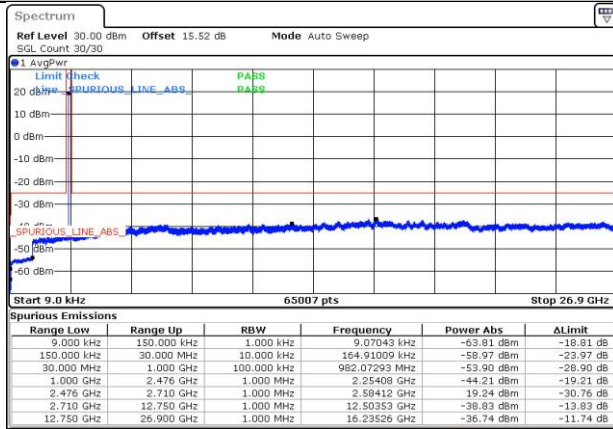
Date: 19 DEC 2023 03:16:48

LCH / DFT-QPSK_Edge_1RB_Left



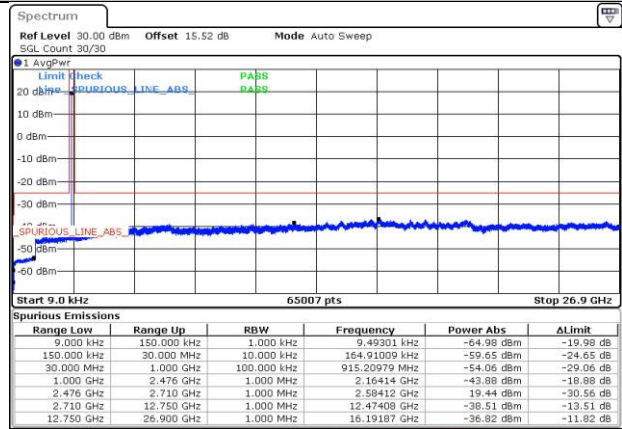
Date: 19 DEC 2023 03:17:08

MCH / DFT-Pi2BPSK_Edge_1RB_Left



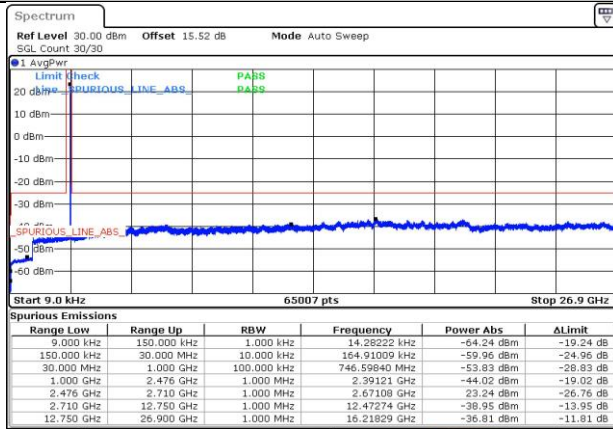
Date: 19 DEC 2023 03:17:39

MCH / DFT-QPSK_Edge_1RB_Left



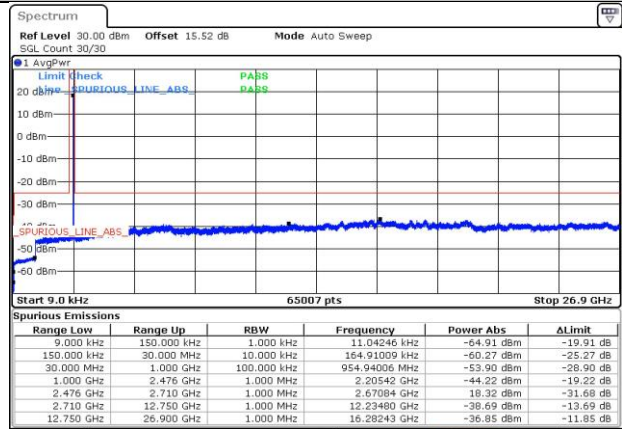
Date: 19 DEC 2023 03:17:07

HCH / DFT-Pi2BPSK_Edge_1RB_Left



Date: 19 DEC 2023 03:18:33

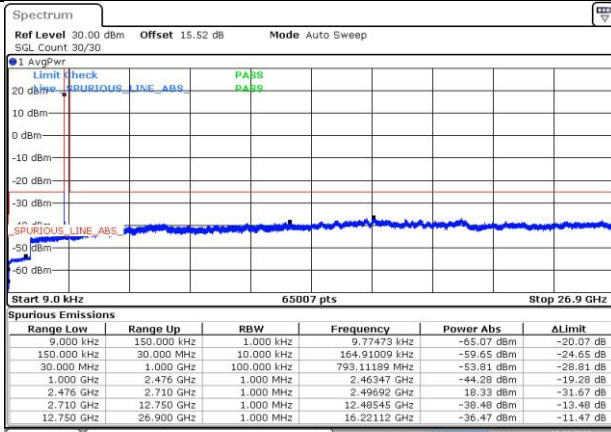
HCH / DFT-QPSK_Edge_1RB_Left



Date: 19 DEC 2023 03:19:01

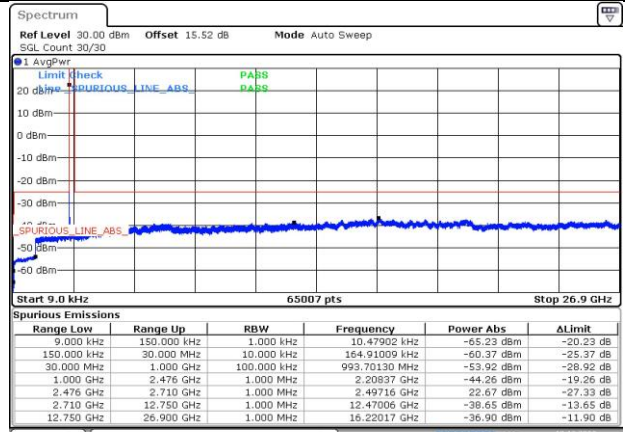
n41A / 30KHz / 30MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



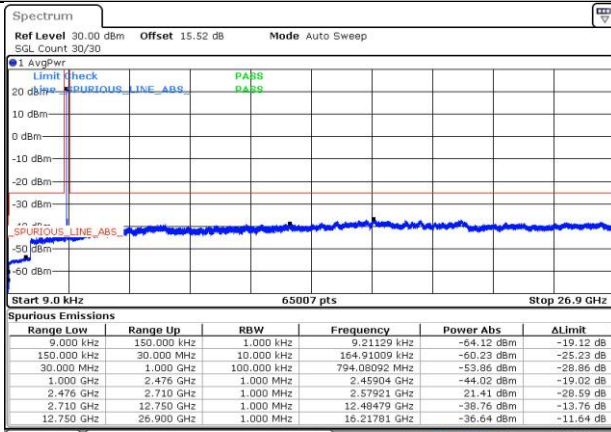
Date: 19 DEC 2023 03:19:31

LCH / DFT-QPSK_Edge_1RB_Left



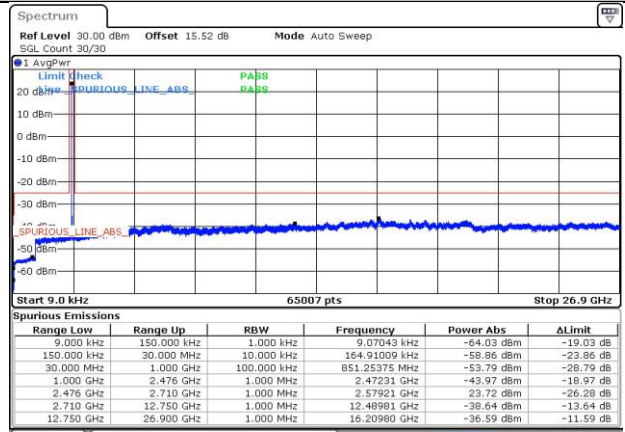
Date: 19 DEC 2023 03:19:51

MCH / DFT-Pi2BPSK_Edge_1RB_Left



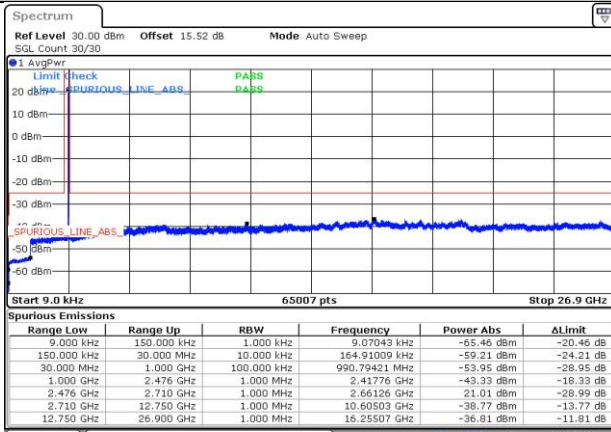
Date: 19 DEC 2023 03:20:22

MCH / DFT-QPSK_Edge_1RB_Left



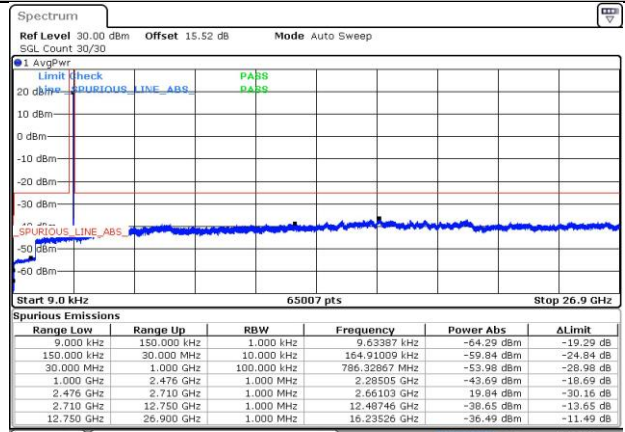
Date: 19 DEC 2023 03:20:50

HCH / DFT-Pi2BPSK_Edge_1RB_Left



Date: 19 DEC 2023 03:21:16

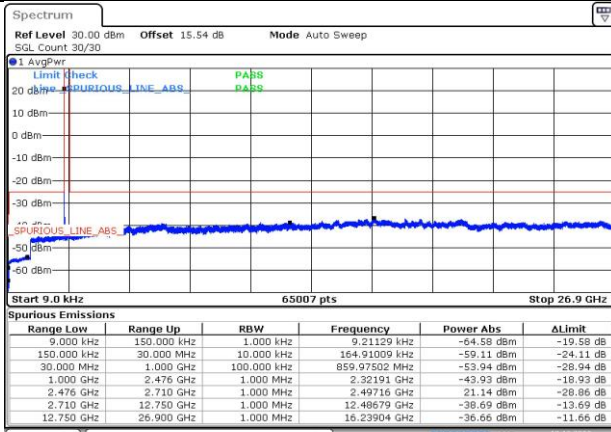
HCH / DFT-QPSK_Edge_1RB_Left



Date: 19 DEC 2023 03:21:45

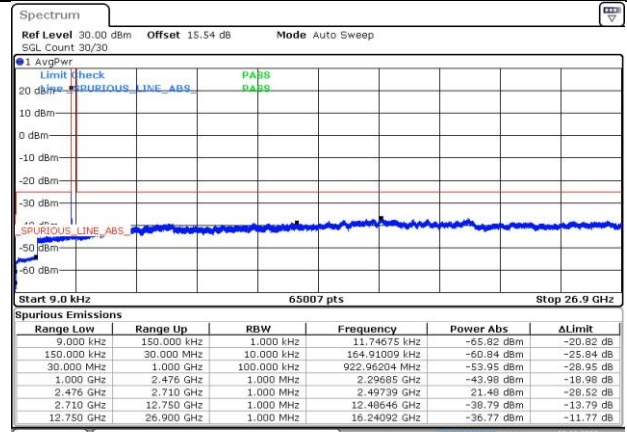
n41A / 30KHz / 40MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



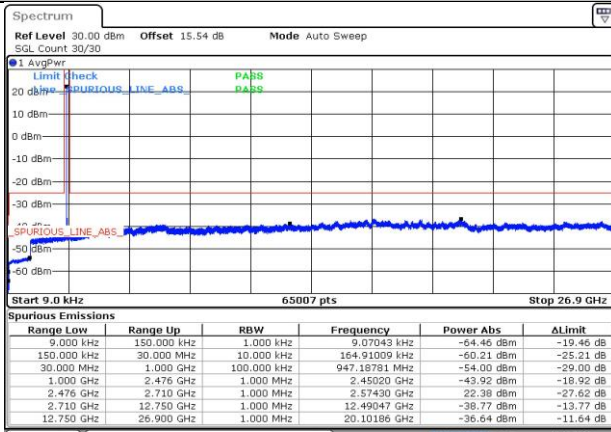
Date: 19 DEC 2023 03:22:15

LCH / DFT-QPSK_Edge_1RB_Left



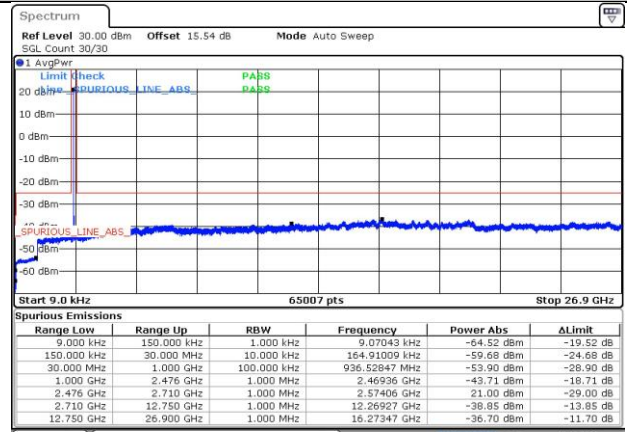
Date: 19 DEC 2023 03:22:35

MCH / DFT-Pi2BPSK_Edge_1RB_Left



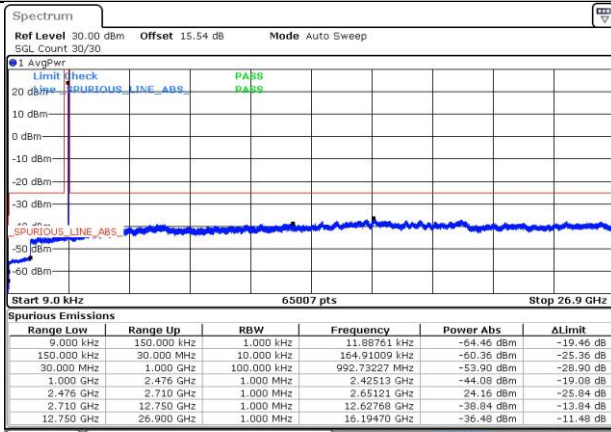
Date: 19 DEC 2023 03:23:01

MCH / DFT-QPSK_Edge_1RB_Left



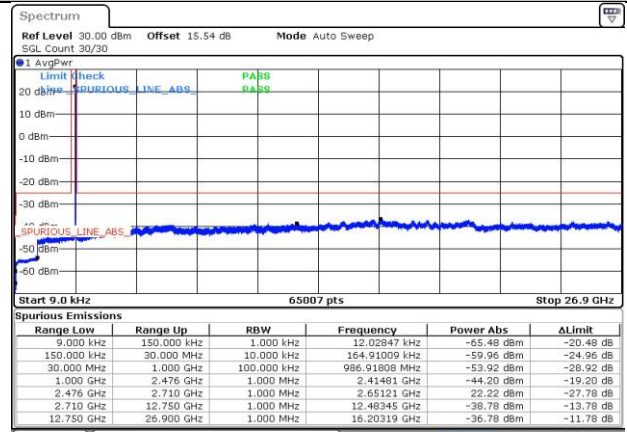
Date: 19 DEC 2023 03:23:30

HCH / DFT-Pi2BPSK_Edge_1RB_Left



Date: 19 DEC 2023 03:23:56

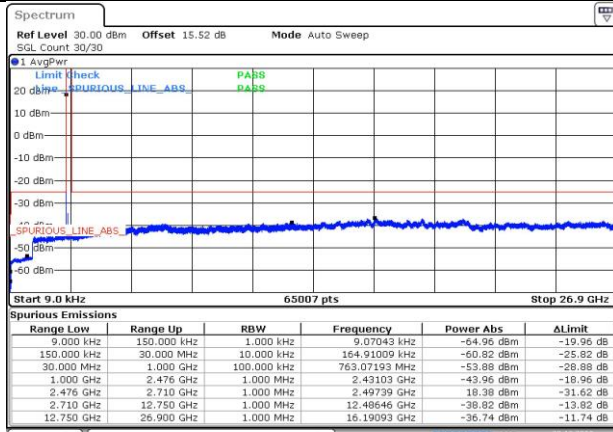
HCH / DFT-QPSK_Edge_1RB_Left



Date: 19 DEC 2023 03:24:24

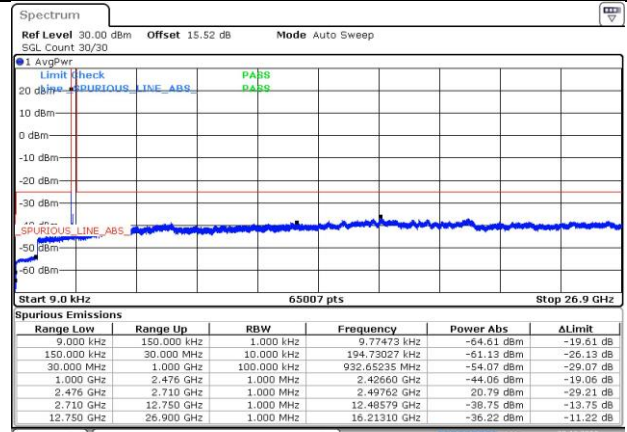
n41A / 30KHz / 50MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



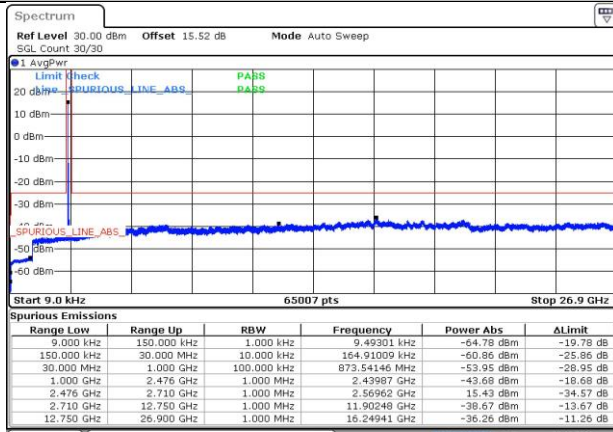
Date: 29 DEC 2023 09:18:29

LCH / DFT-QPSK_Edge_1RB_Left



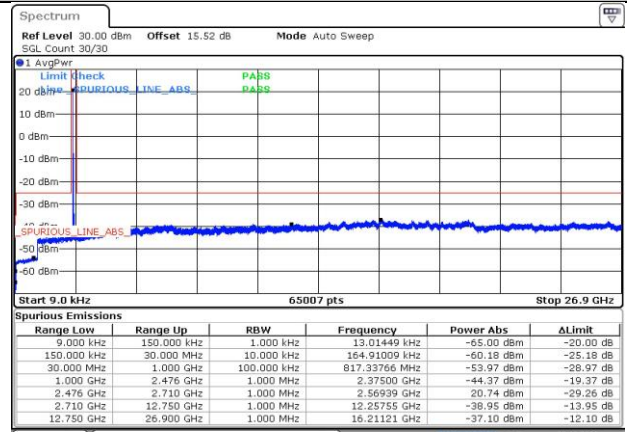
Date: 29 DEC 2023 09:18:58

MCH / DFT-Pi2BPSK_Edge_1RB_Left



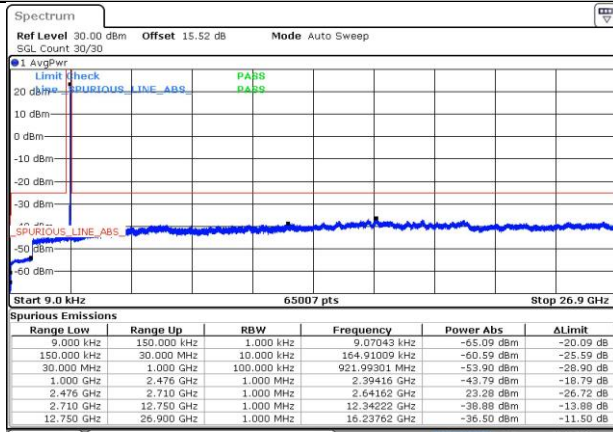
Date: 29 DEC 2023 09:19:59

MCH / DFT-QPSK_Edge_1RB_Left



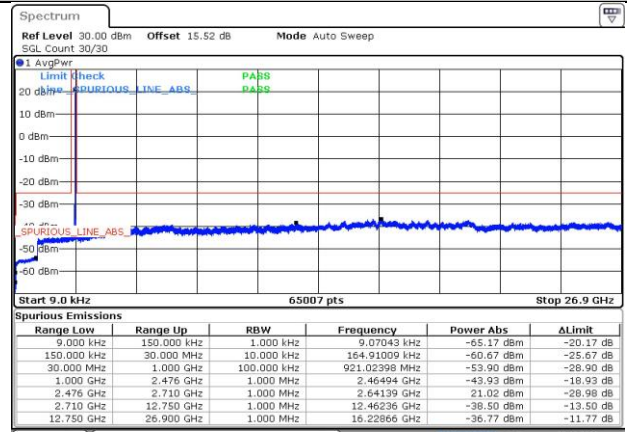
Date: 29 DEC 2023 09:20:27

HCH / DFT-Pi2BPSK_Edge_1RB_Left



Date: 29 DEC 2023 09:20:56

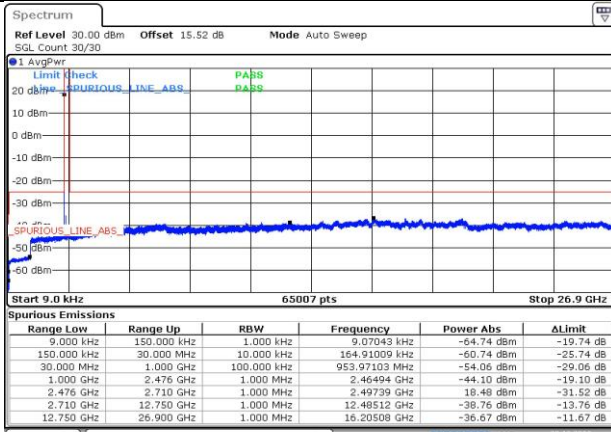
HCH / DFT-QPSK_Edge_1RB_Left



Date: 29 DEC 2023 09:21:24

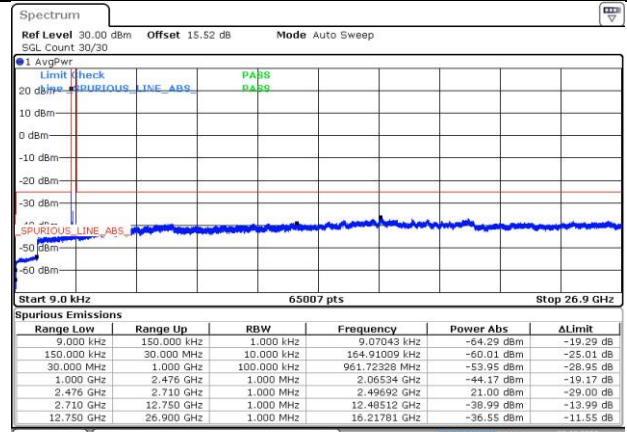
n41A / 30KHz / 60MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



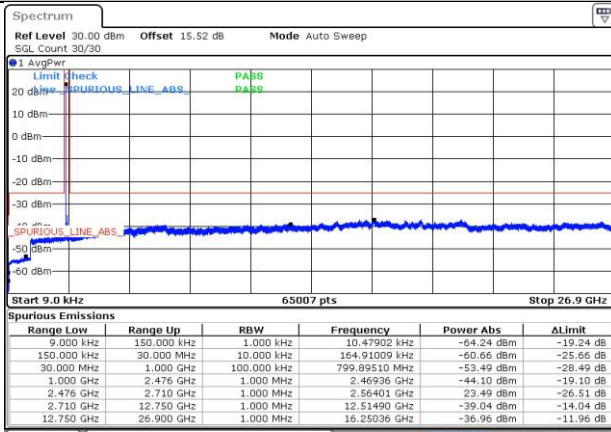
Date: 29 DEC 2023 11:48:31

LCH / DFT-QPSK_Edge_1RB_Left



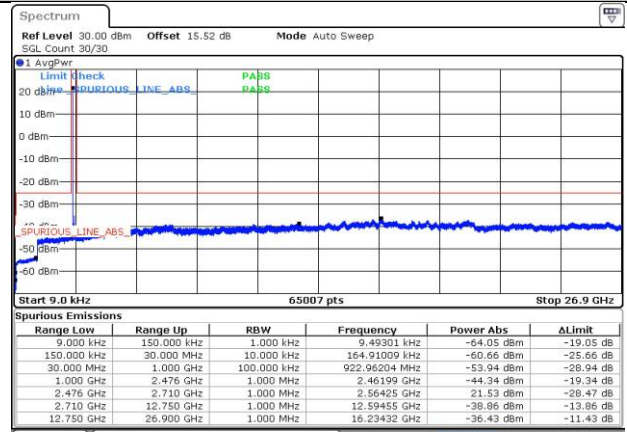
Date: 29 DEC 2023 11:49:00

MCH / DFT-Pi2BPSK_Edge_1RB_Left



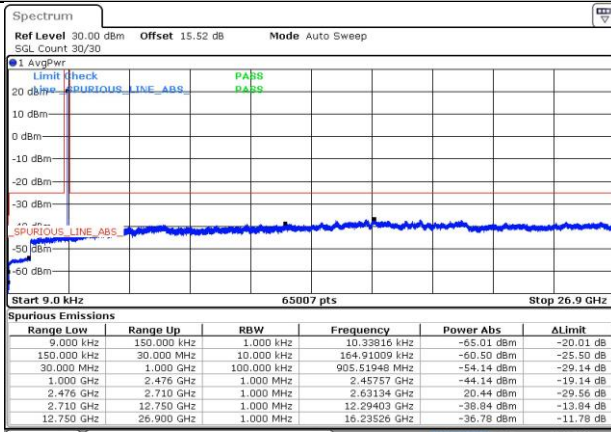
Date: 29 DEC 2023 11:49:24

MCH / DFT-QPSK_Edge_1RB_Left



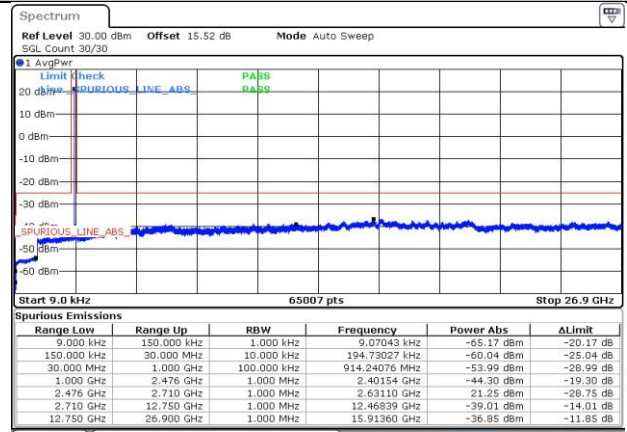
Date: 29 DEC 2023 11:49:53

HCH / DFT-Pi2BPSK_Edge_1RB_Left



Date: 29 DEC 2023 11:50:18

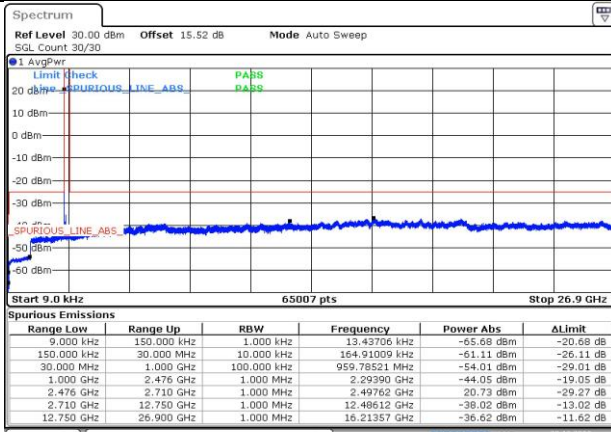
HCH / DFT-QPSK_Edge_1RB_Left



Date: 29 DEC 2023 11:50:46

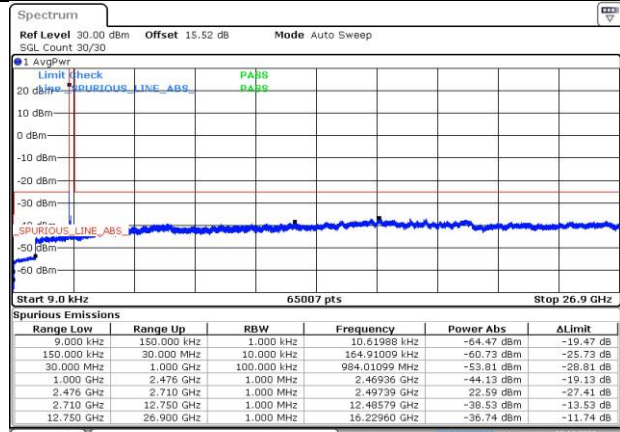
n41A / 30KHz / 70MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



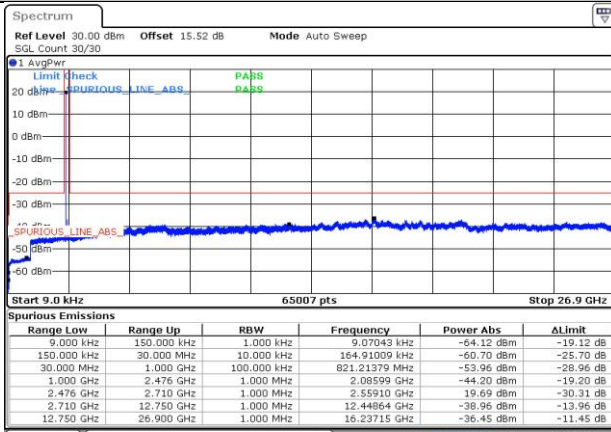
Date: 29 DEC 2023 11:51:15

LCH / DFT-QPSK_Edge_1RB_Left



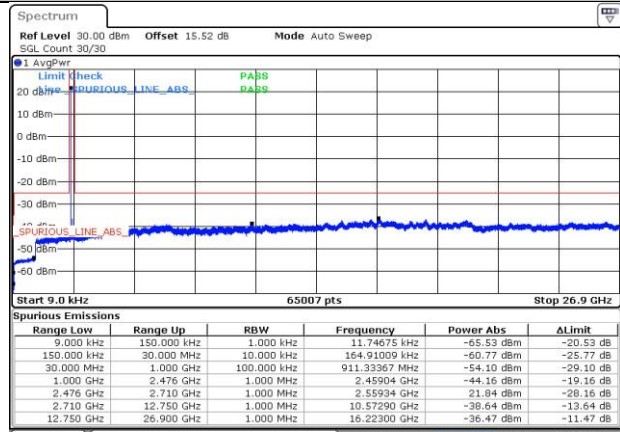
Date: 29 DEC 2023 11:51:43

MCH / DFT-Pi2BPSK_Edge_1RB_Left



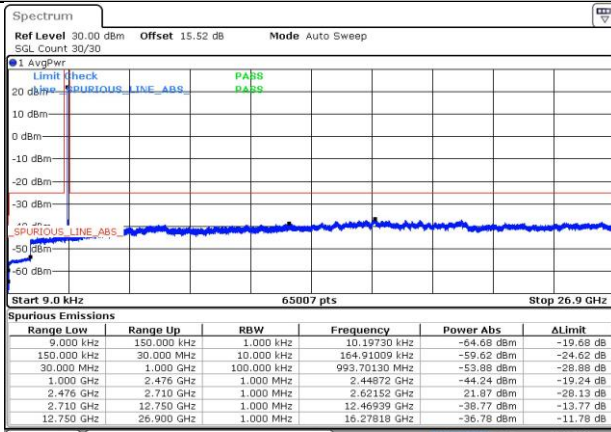
Date: 29 DEC 2023 11:52:08

MCH / DFT-QPSK_Edge_1RB_Left



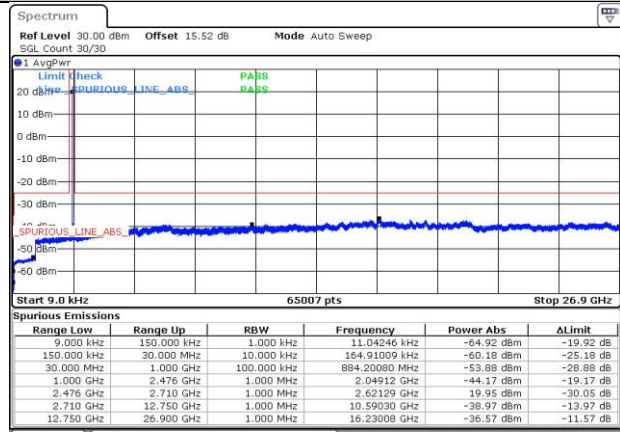
Date: 29 DEC 2023 11:52:37

HCH / DFT-Pi2BPSK_Edge_1RB_Left



Date: 29 DEC 2023 11:53:01

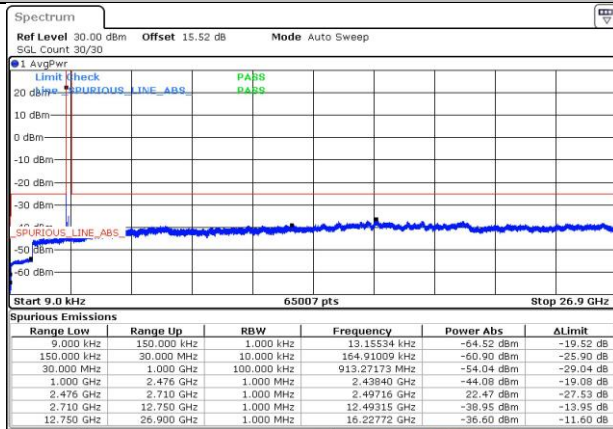
HCH / DFT-QPSK_Edge_1RB_Left



Date: 29 DEC 2023 11:53:30

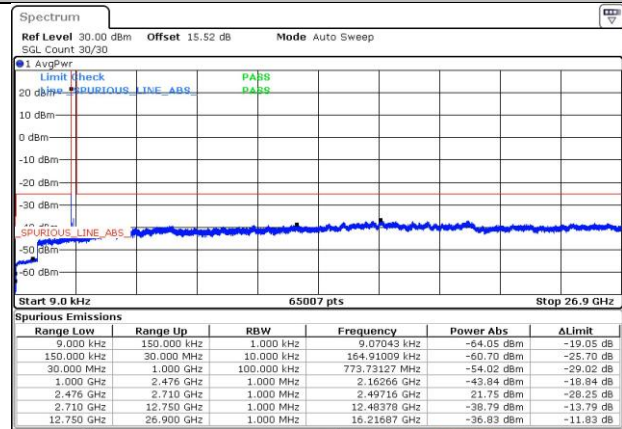
n41A / 30KHz / 80MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



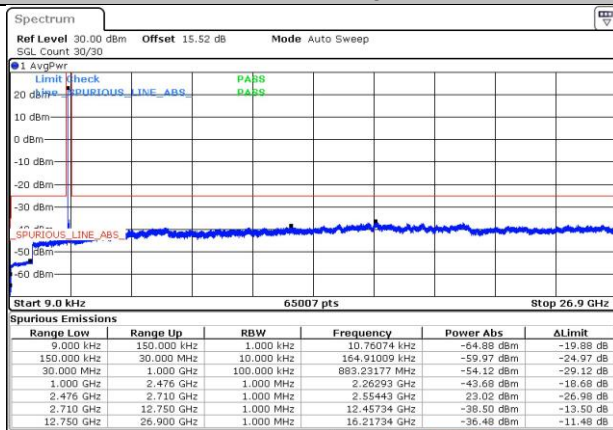
Date: 29 DEC 2023 11:53:59

LCH / DFT-QPSK_Edge_1RB_Left



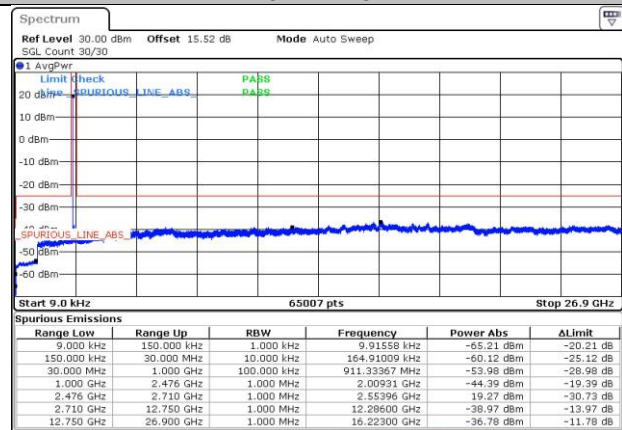
Date: 29 DEC 2023 11:54:28

MCH / DFT-Pi2BPSK_Edge_1RB_Left



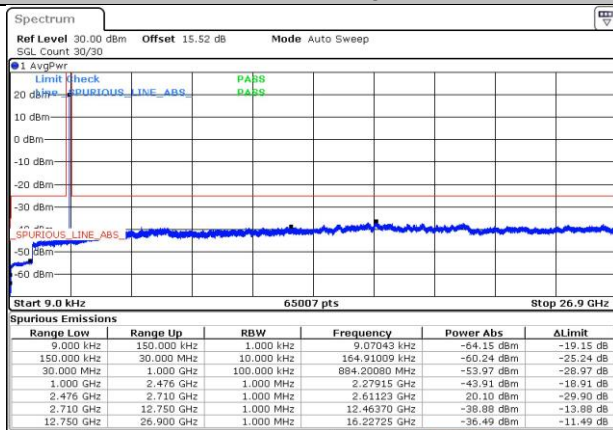
Date: 29 DEC 2023 11:55:13

MCH / DFT-QPSK_Edge_1RB_Left



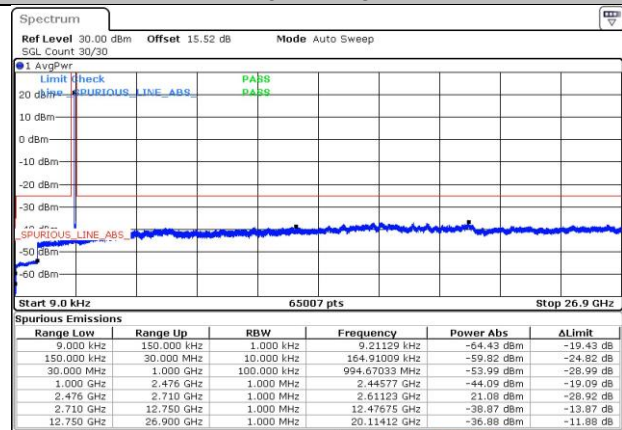
Date: 29 DEC 2023 11:55:42

HCH / DFT-Pi2BPSK_Edge_1RB_Left



Date: 29 DEC 2023 11:56:19

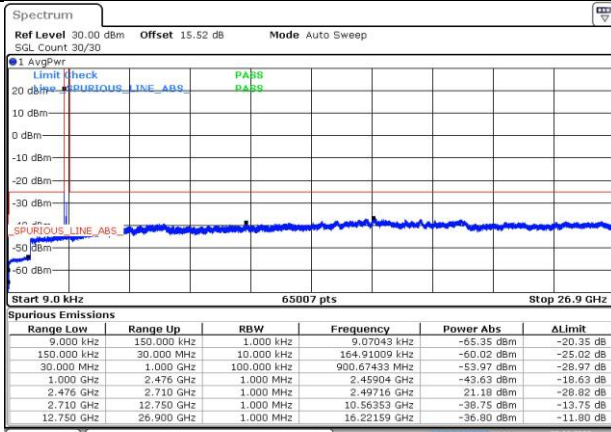
HCH / DFT-QPSK_Edge_1RB_Left



Date: 29 DEC 2023 11:56:48

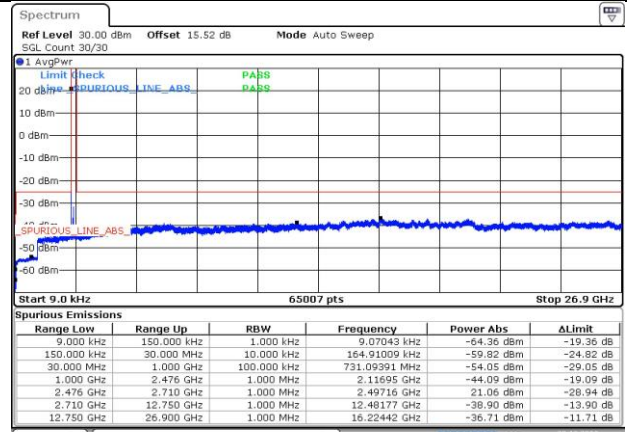
n41A / 30KHz / 90MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



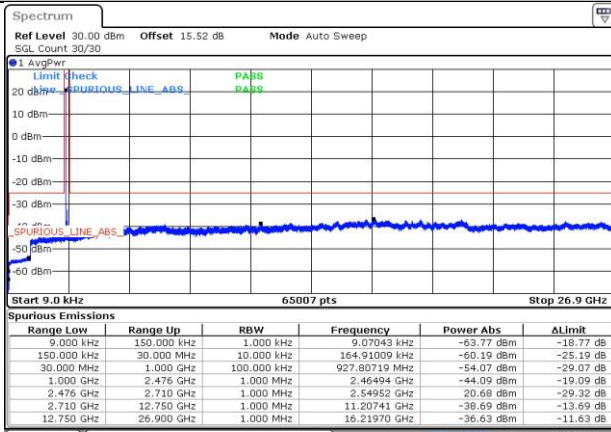
Date: 29 DEC 2023 11:57:17

LCH / DFT-QPSK_Edge_1RB_Left



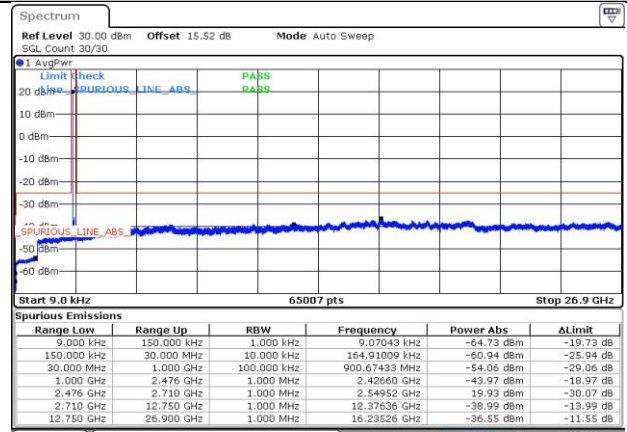
Date: 29 DEC 2023 11:57:46

MCH / DFT-Pi2BPSK_Edge_1RB_Left



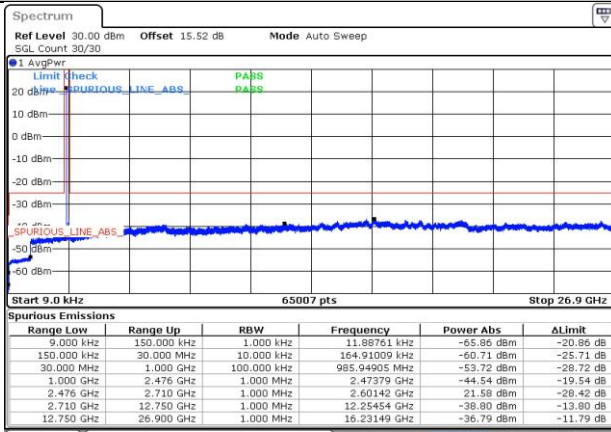
Date: 29 DEC 2023 11:58:31

MCH / DFT-QPSK_Edge_1RB_Left



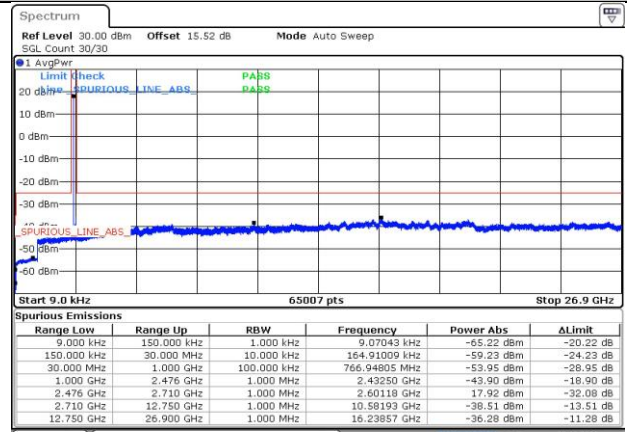
Date: 29 DEC 2023 11:59:00

HCH / DFT-Pi2BPSK_Edge_1RB_Left



Date: 29 DEC 2023 11:59:29

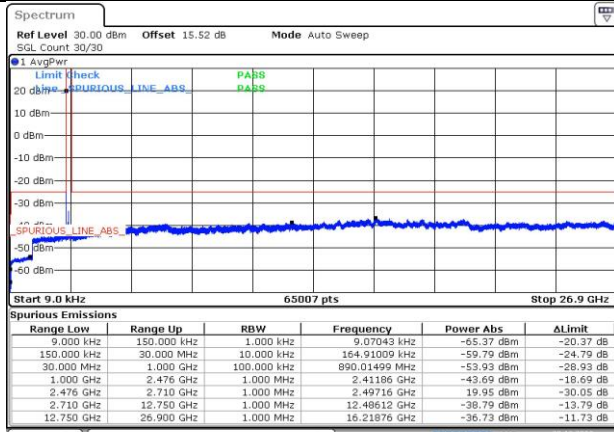
HCH / DFT-QPSK_Edge_1RB_Left



Date: 29 DEC 2023 11:59:58

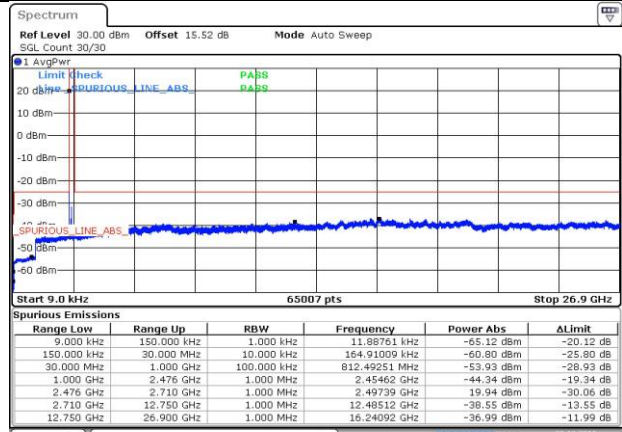
n41A / 30KHz / 100MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



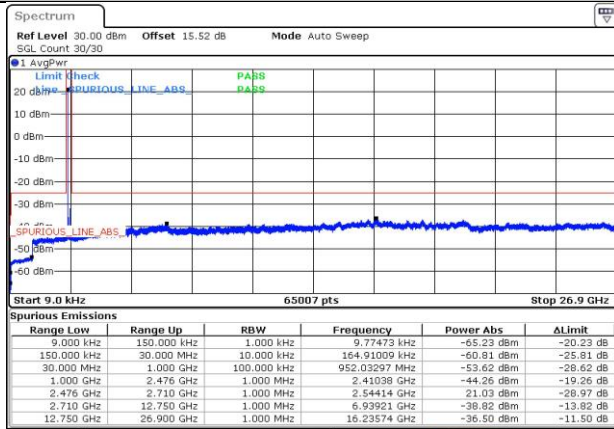
Date: 29 DEC 2023 12:00:27

LCH / DFT-QPSK_Edge_1RB_Left



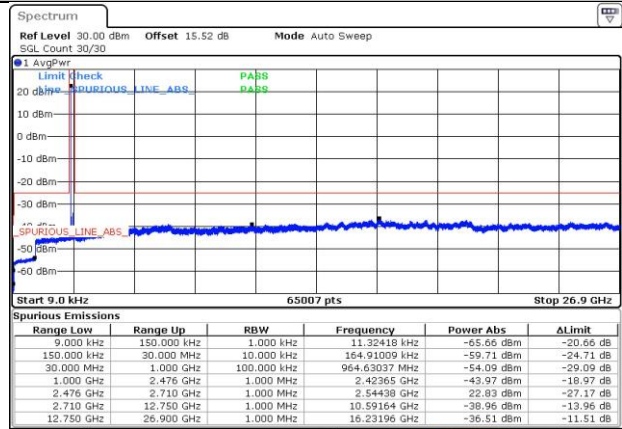
Date: 29 DEC 2023 12:00:55

MCH / DFT-Pi2BPSK_Edge_1RB_Left



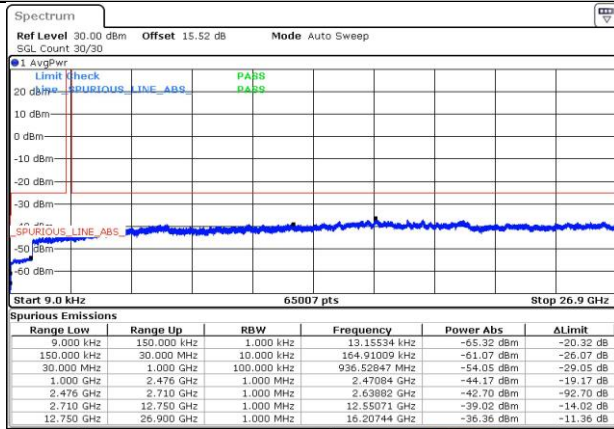
Date: 29 DEC 2023 12:01:24

MCH / DFT-QPSK_Edge_1RB_Left



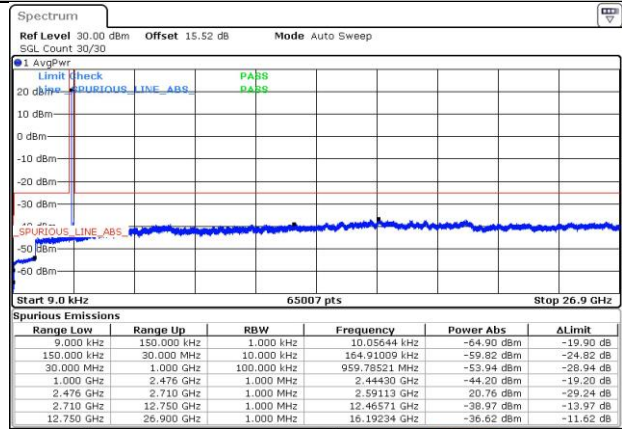
Date: 29 DEC 2023 12:01:53

HCH / DFT-Pi2BPSK_Edge_1RB_Left



Date: 29 DEC 2023 12:02:35

HCH / DFT-QPSK_Edge_1RB_Left



Date: 29 DEC 2023 12:03:13

7. Frequency Stability

7.1. Test Results

7.1.1. Frequency Error Vs Voltage

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	LV	-10.60	-0.004088	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	NV	-10.20	-0.003934	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	NT	HV	-7.30	-0.002815	Pass

7.1.2. Frequency Error Vs Temperature

SCS	Bandwidth	Channel	RB Config	Modulation	Temperature	Voltage	Deviation Result		Verdict
							(Hz)	(ppm)	
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-30°C	NV	-5.50	-0.002121	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-20°C	NV	-15.60	-0.006016	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	-10°C	NV	-11.40	-0.004396	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	0°C	NV	-8.80	-0.003394	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	10°C	NV	-15.00	-0.005785	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	20°C	NV	-13.50	-0.005206	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	30°C	NV	-13.60	-0.005245	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	40°C	NV	-11.90	-0.004589	Pass
30KHz	100MHz	MCH	Outer_Full	DFT-QPSK	50°C	NV	0.40	0.000154	Pass

The End